

## General Description

The WSR30N65CF uses advanced SGT MOSFET to provide excellent  $R_{DS(ON)}$ , low gate charge

This device is suitable for use as a Battery protection or in other Switching application.

## Features

- 100% UIS +  $R_g$  Tested.
- Reliable and Rugged
- Lead Free and Green Devices Available (RoHS Compliant)
- Moisture Sensitivity Level MSL1 (per JEDEC J-STD-020D)

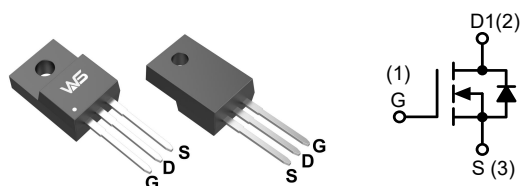
## Product Summary

$BV_{DSS}$	$R_{DS(ON)}$	$I_D$
650V	0.125 $\Omega$	25A

## Applications

- AC/DC Power Conversion in Switched mode Power Supplies (SMPS) .

## TO-220F-3L Pin Configuration



## Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$ , Unless Otherwise Noted)

Symbol	Parameter		Rating	Units
$V_{DS}$	Drain-Source Voltage		650	V
$V_{GS}$	Gate-Source Voltage		$\pm 30$	
$I_S$	Diode Continuous Forward Current	$T_C=25^\circ\text{C}$	25	A
$I_D$	Continuous Drain Current	$T_C=25^\circ\text{C}$	25	
		$T_C=100^\circ\text{C}$	19	
$I_{DM}^2$	Pulse Drain Current	$T_C=25^\circ\text{C}$	75	
$P_D$	Maximum Power Dissipation	$T_C=25^\circ\text{C}$	54	W
		$T_C=100^\circ\text{C}$	22	
$R_{\theta JA}^4$	Thermal Resistance-Junction to Ambient	Steady State	62.5	$^\circ\text{C/W}$
$R_{\theta JC}$	Thermal Resistance-Junction to Case		2.3	
$I_{AS}^3$	Avalanche Current, Single pulse	$L=0.5\text{mH}$	8.2	A
$E_{AS}^3$	Avalanche Energy, Single pulse	$L=0.5\text{mH}$	336	mJ
$T_{STG}$	Storage Temperature Range		-55 to 150	$^\circ\text{C}$
$T_J$	Maximum Junction Temperature		150	

**Electrical Characteristics** ( $T_A=25^{\circ}\text{C}$ , Unless Otherwise Noted)

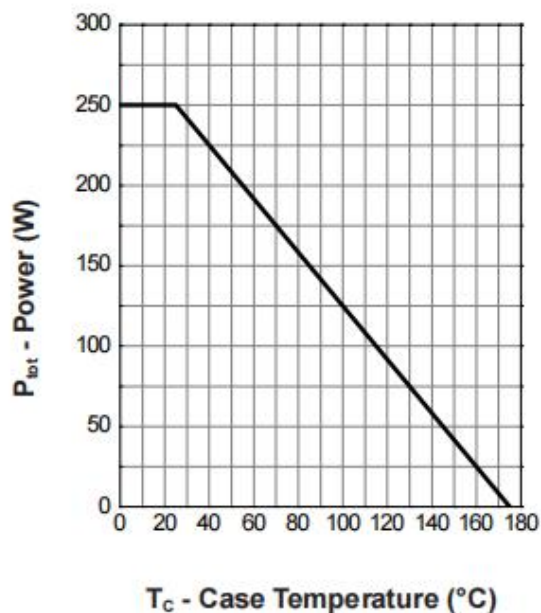
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
Static Characteristics						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250μA	650	---	---	V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =520V , V <sub>GS</sub> =0V T <sub>J</sub> =85°C	---	---	1.0 30	μA
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>DS</sub> =250μA	2.5	3.5	4.5	V
I <sub>GSS</sub>	Gate Leakage Current	V <sub>GS</sub> =±30V , V <sub>DS</sub> =0V	---	---	±100	nA
R <sub>DS(ON)</sub> <sup>5</sup>	Drain-Source On-state Resistance	V <sub>GS</sub> =10V , I <sub>D</sub> =12A	---	0.125	0.15	Ω
Diode Characteristics						
V <sub>SD</sub> <sup>5</sup>	Diode Forward Voltage	I <sub>SD</sub> =25A , V <sub>GS</sub> =0V	---	0.8	1.3	V
t <sub>rr</sub>	Reverse Recovery Time	I <sub>DS</sub> =12A , di <sub>SD</sub> /dt=100A/μs	---	313	---	ns
Q <sub>rr</sub>	Reverse Recovery Charge		---	5.2	---	nC
Dynamic Characteristics <sup>6</sup>						
R <sub>g</sub>	Gate Resistance	V <sub>GS</sub> =0V , V <sub>DS</sub> =0V , f=1.0MHz	---	3	---	Ω
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> =0V , V <sub>DS</sub> =325V , Frequency=1.0MHz	---	2210	2890	pF
C <sub>oss</sub>	Output Capacitance		---	55	---	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	5	---	
T <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DD</sub> =30V , R <sub>L</sub> =30Ω , I <sub>DS</sub> =1A , V <sub>GEN</sub> =10V , R <sub>G</sub> =6Ω	---	20	---	ns
T <sub>r</sub>	Turn-on Rise Time		---	19	---	
T <sub>d(off)</sub>	Turn-off Delay Time		---	80	---	
T <sub>f</sub>	Turn-off Fall Time		---	252	---	
Gate Charge Characteristics <sup>6</sup>						
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> =325V , V <sub>GS</sub> =10V , I <sub>DS</sub> =12A	---	50	---	nC
Q <sub>gs</sub>	Gate-Source Charge		---	11	---	
Q <sub>gd</sub>	Gate-Drain Charge		---	19	---	

**Note:**

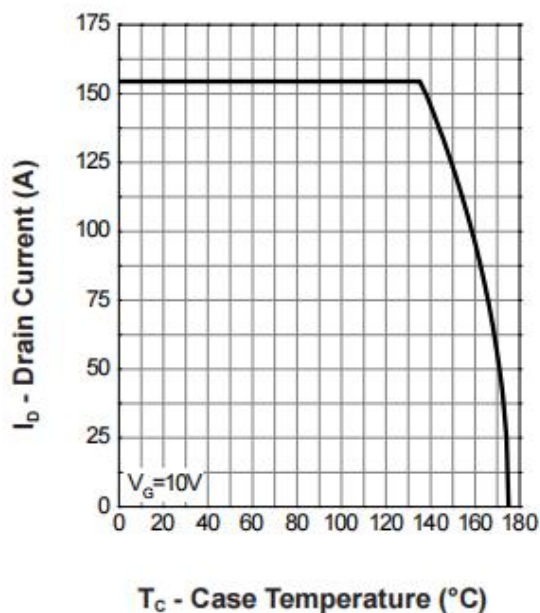
1. Calculated continuous current based on maximum allowable junction temperature. Bonding wire limitation current is 8A.
2. Pulse width limited by max. junction temperature.
3. UIS tested and pulse width limited by maximum junction temperature  $150^{\circ}\text{C}$  (initial temperature  $T_J=25^{\circ}\text{C}$ ).
4. Surface Mounted on  $1\text{in}^2$  pad area.
5. Pulse test ; pulse width $\leq 300\mu s$ , duty cycle $\leq 2\%$ .
6. Guaranteed by design, not subject to production testing.

## Typical Characteristics

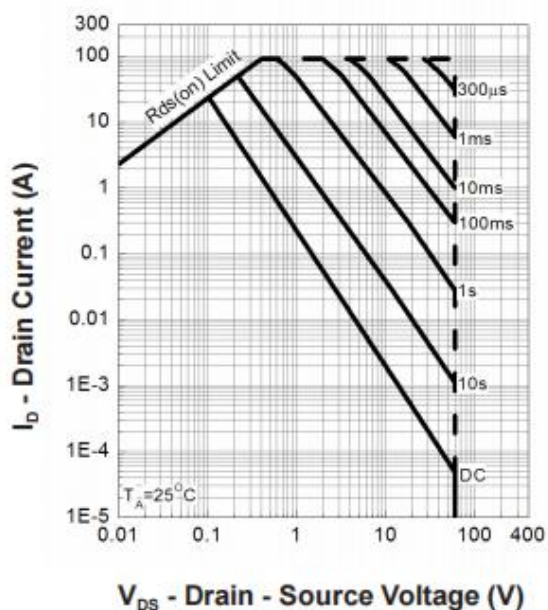
Power Dissipation



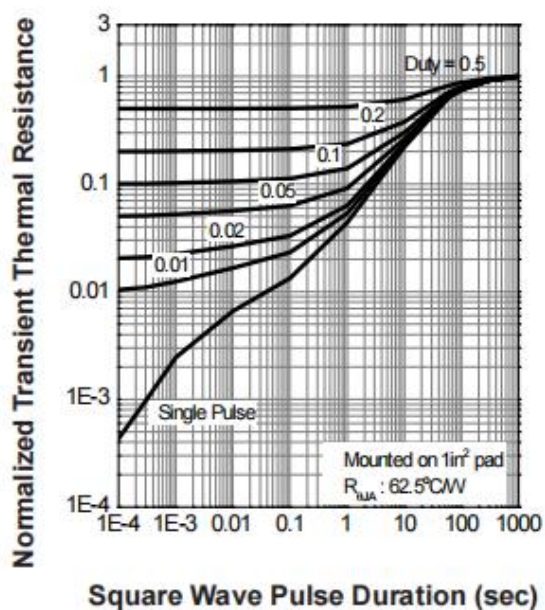
Drain Current



Safe Operation Area

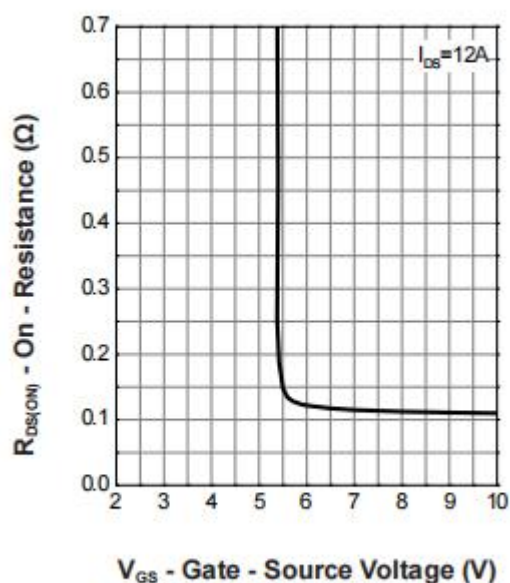


Thermal Transient Impedance

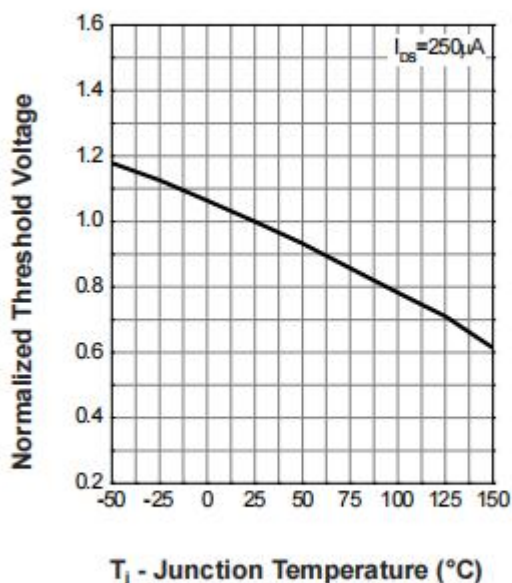


Typical Characteristics (Cont.)

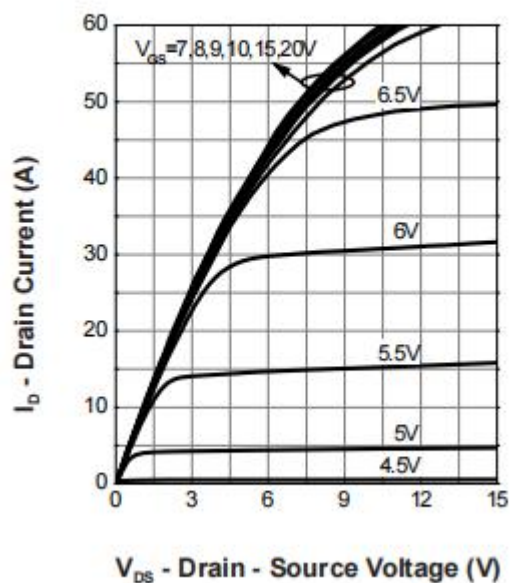
Gate-Source On Resistance



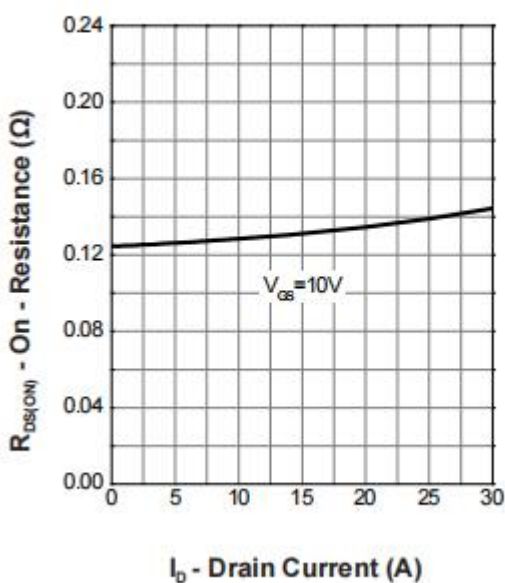
Gate Threshold Voltage



Output Characteristics



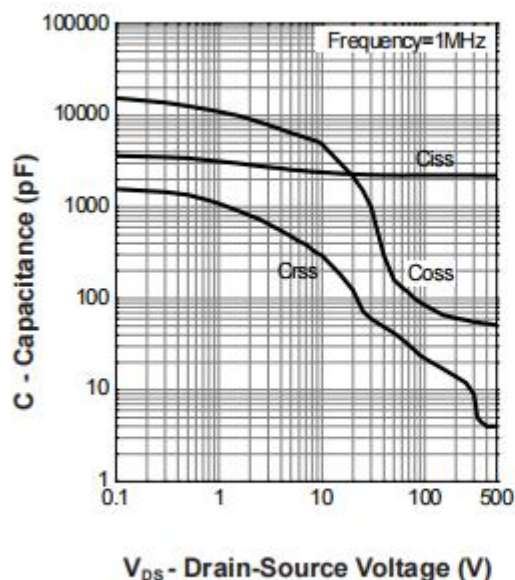
Drain-Source On Resistance



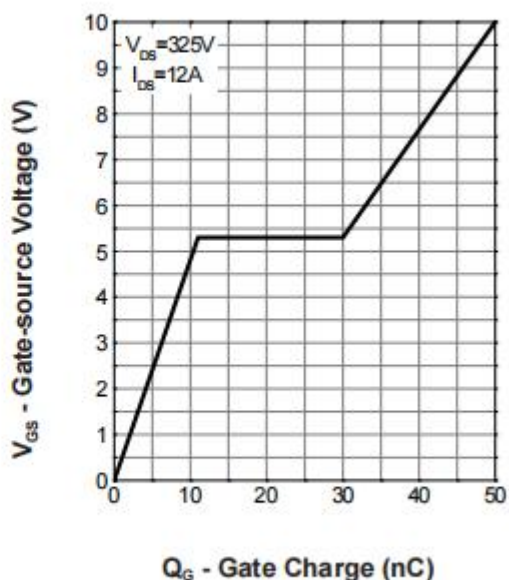


Typical Characteristics (Cont.)

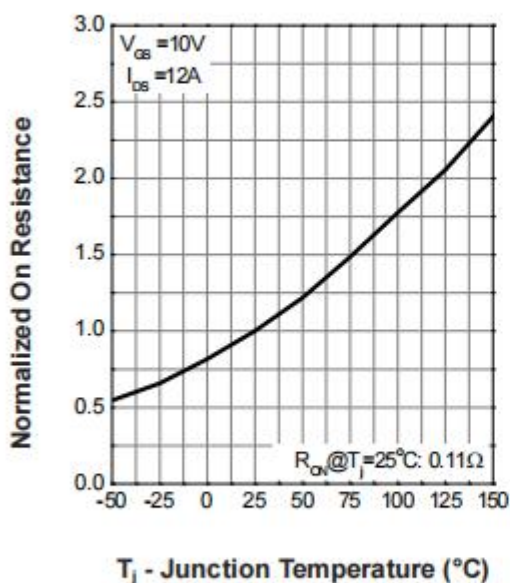
Capacitance



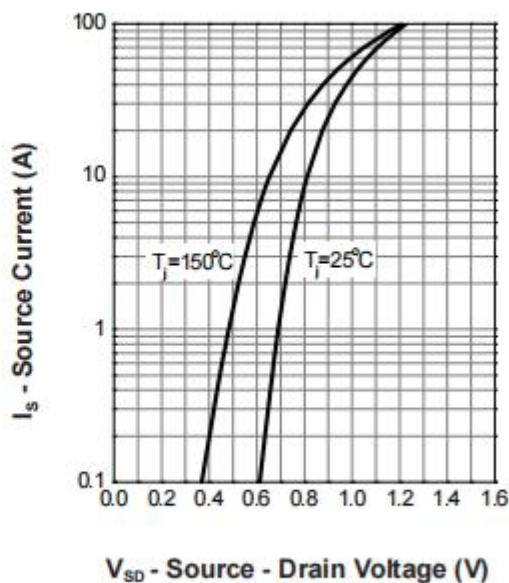
Gate Charge



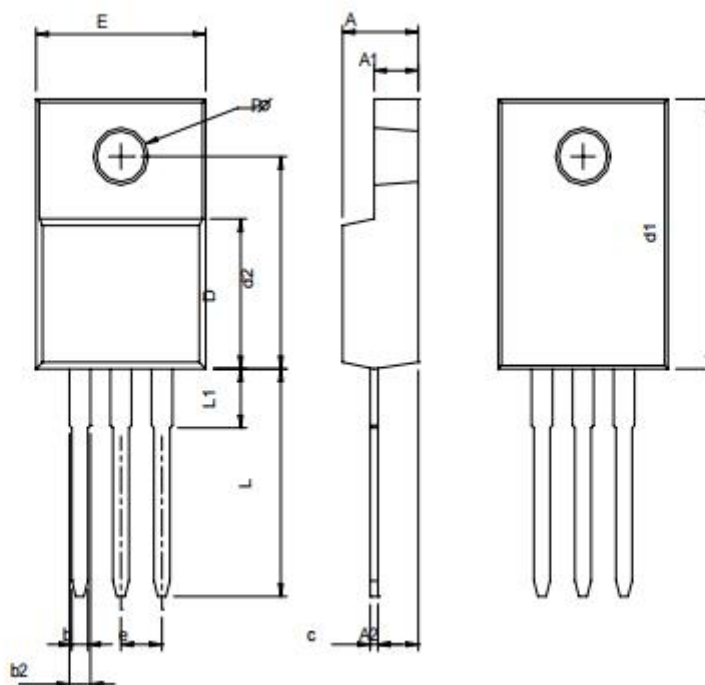
Drain-Source On Resistance



Source-Drain Diode Forward

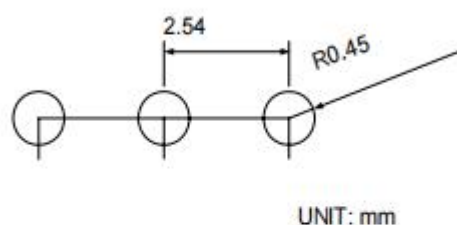


## Packaging information



Symbol	TO-220FP			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	4.20	4.80	0.165	0.189
A1	2.34	3.20	0.092	0.126
A2	2.10	2.90	0.083	0.114
b	0.50	0.90	0.020	0.035
b2	0.91	1.90	0.035	0.075
c	0.30	0.80	0.012	0.031
D	8.10	9.40	0.319	0.370
d1	14.50	16.50	0.571	0.650
d2	12.10	12.90	0.476	0.508
E	9.70	10.70	0.382	0.421
e	2.54 BSC		0.100 BSC	
L	13.00	14.50	0.512	0.570
L1	1.60	4.00	0.063	0.157
P	3.00	3.60	0.118	0.142

## RECOMMENDED LAND PATTERN



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