General Description

The WST2335A is the highest performance trench P-Channel MOSFET with extreme high cell density , which provide excellent $R_{DS(ON)}$ and gate charge for most of the small power switching and load switch applications.

The WST2335A meet the RoHS and Green Product requirement with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- Green Device Available

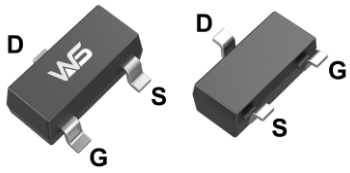
Product Summery

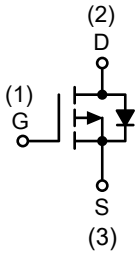
BV_{DSS}	$R_{DS(ON)}$	I_D
-20V	35mΩ	-5.8A

Applications

- High Frequency Point-of-Load Synchronous Small power switching for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch

SOT-23L Pin Configuration





Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-20	V
V_{GS}	Gate-Source Voltage	±8	
I_D@T_C=25°C	Continuous Drain Current, V_{GS} @ -4.5V [1]	-5.8	A
I_D@T_C=70°C	Continuous Drain Current, V_{GS} @ -4.5V [1]	-3.7	
I_{DM}	Pulsed Drain Current [2]	-18.1	
P_D@T_A=25°C	Total Power Dissipation [3]	1	W
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	

Thermal Data

Symbol	Parameter	Typ.	Max.	Units
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient [1]	---	125	°C/W
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case [1]	---	80	

Electrical Characteristics (T_J=25°C, Unless Otherwise Noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=-250μA	-20	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to 25°C, I_D=-1mA	---	-0.01	---	V/°C
$R_{DS(ON)}$	Static Drain-Source On-Resistance [2]	V_{GS}=-4.5V , I_D=-4A	---	35	45	mΩ
		V_{GS}=-2.5V , I_D=-2A	---	45	57	
		V_{GS}=-1.8V , I_D=-1.5A	---	85	105	
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D=-250μA	-0.3	-0.5	-1.0	V
$\Delta V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	2.96	---	mV/°C
I_{DSS}	Drain-Source Leakage Current	V_{DS}=-16V , V_{GS}=0V , T_J=25°C	---	---	-1.0	μA
		V_{DS}=-16V , V_{GS}=0V , T_J=55°C	---	---	-5.0	
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±8V , V_{DS}=0V	---	---	±100	nA
g_{fs}	Forward Transconductance	V_{DS}=-5V , I_D=-4A	---	21	---	S
Q_g	Total Gate Charge (-4.5V)	V_{DS}=-15V , V_{GS}=-4.5V , I_D=-4A	---	27.3	38.2	nC
Q_{gs}	Gate-Source Charge		---	3.6	5.0	
Q_{gd}	Gate-Drain Charge		---	6.5	9.1	
$T_{d(on)}$	Turn-On Delay Time	V_{DD}=-10V , V_{GS}=-4.5V , R_G=3.3Ω , I_D=-4A	---	9.2	18.4	ns
T_r	Rise Time		---	59	106	
$T_{d(off)}$	Turn-Off Delay Time		---	99	198	
T_f	Fall Time		---	71	142	
C_{iss}	Input Capacitance	V_{DS}=-15V , V_{GS}=0V , f=1.0MHz	---	1025	1120	pF
C_{oss}	Output Capacitance		---	220	308	
C_{rss}	Reverse Transfer Capacitance		---	187	262	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
I_S	Continuous Source Current [1,4]	V_G=V_D=0V , Force Current	---	---	-4.7	A
I_{SM}	Pulsed Source Curren [2,4]		---	---	-18.1	
V_{SD}	Diode Forward Voltage [2]	V_{GS}=0V , I_S=-1A , T_J=25°C	---	---	-1.0	V
t_{rr}	Reverse Recovery Time	I_F=-4A , dI/dt=100A/μs , T_J=25°C	---	52	---	nS
Q_{rr}	Reverse Recovery Charge		---	28	---	nC

Note:

1. The data tested by surface mounted on a 1 inch^2 FR-4 board with 2OZ copper, t≤10sec.
2. The data tested by pulsed, pulse width ≤ 300μs , duty cycle ≤ 2%.
3. The power dissipation is limited by 150°C junction temperature.
4. The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Characteristics

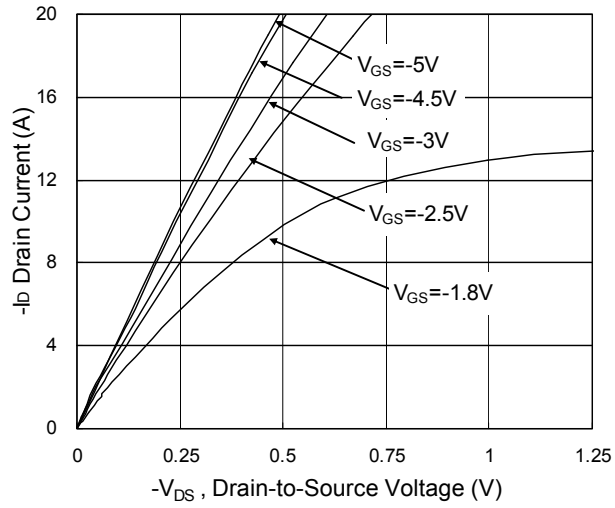


Fig.1 Typical Output Characteristics

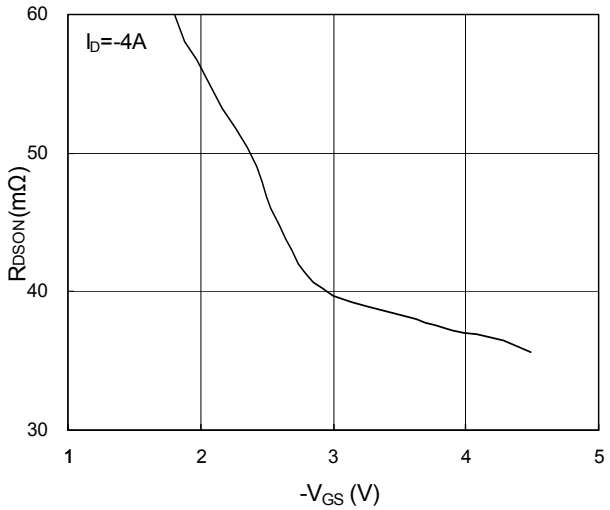


Fig.2 On-Resistance vs. Gate-Source

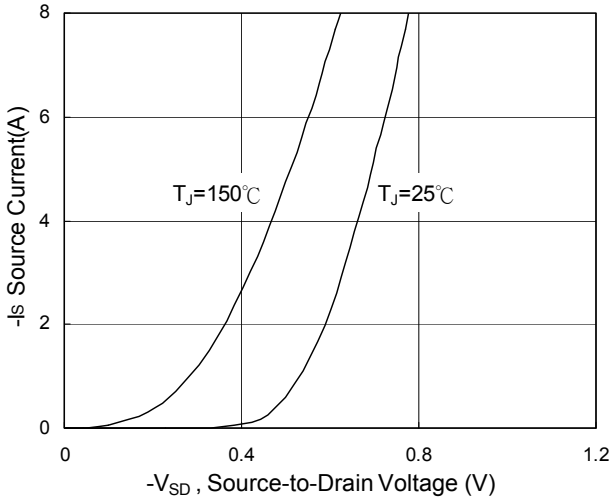


Fig.3 Forward Characteristics Of Reverse

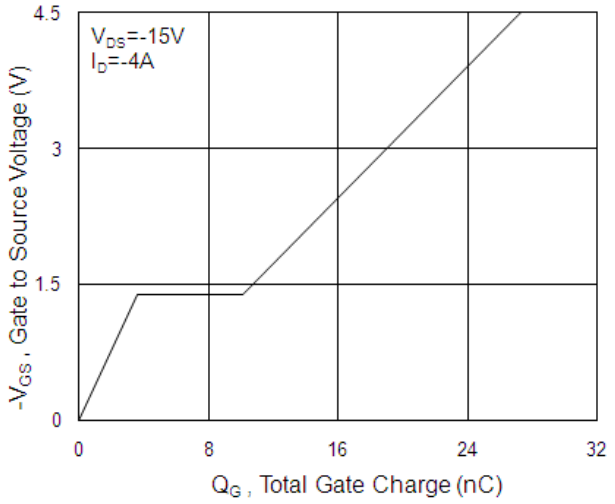


Fig.4 Gate-Charge Characteristics

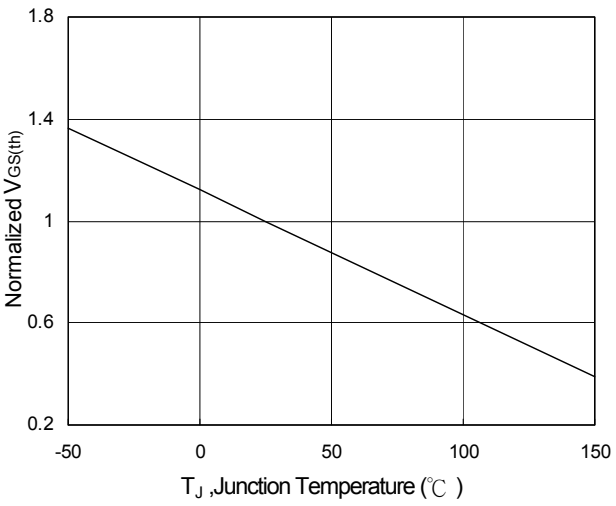


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

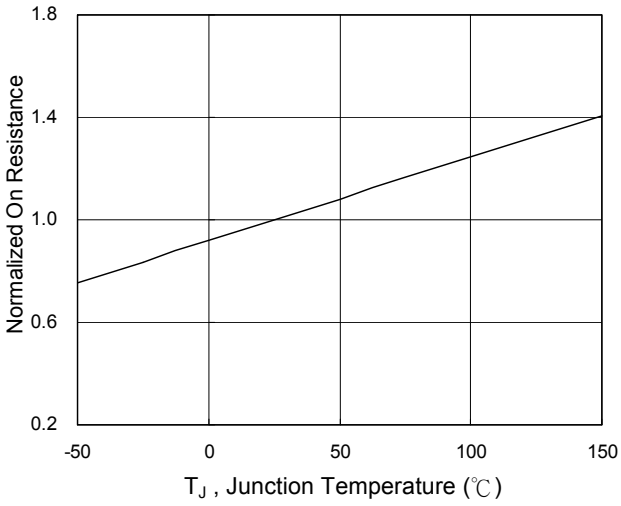


Fig.6 Normalized R_{DSON} vs. T_J

Typical Characteristics (Cont.)

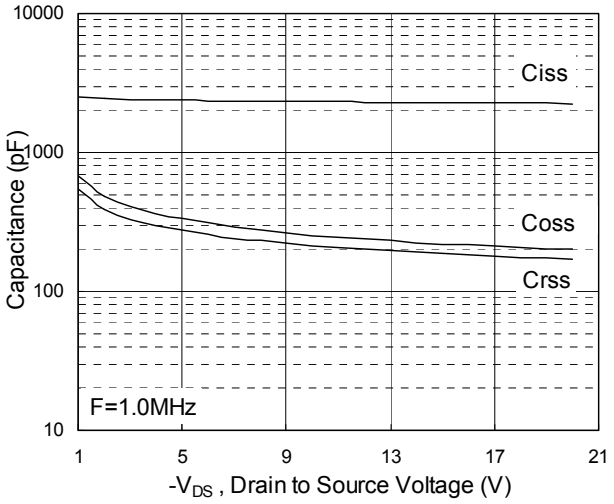


Fig.7 Capacitance

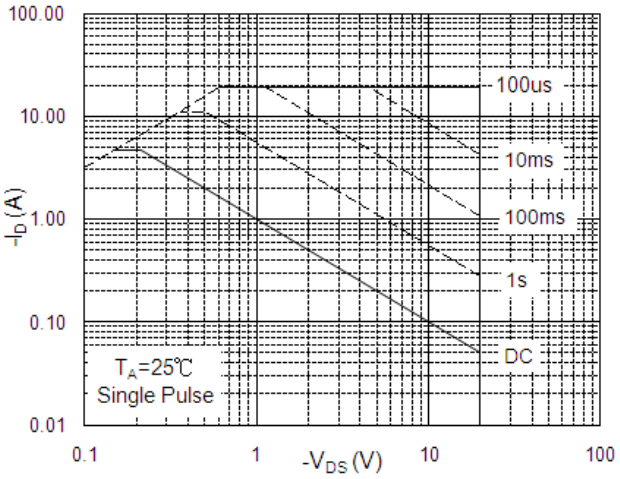


Fig.8 Safe Operating Area

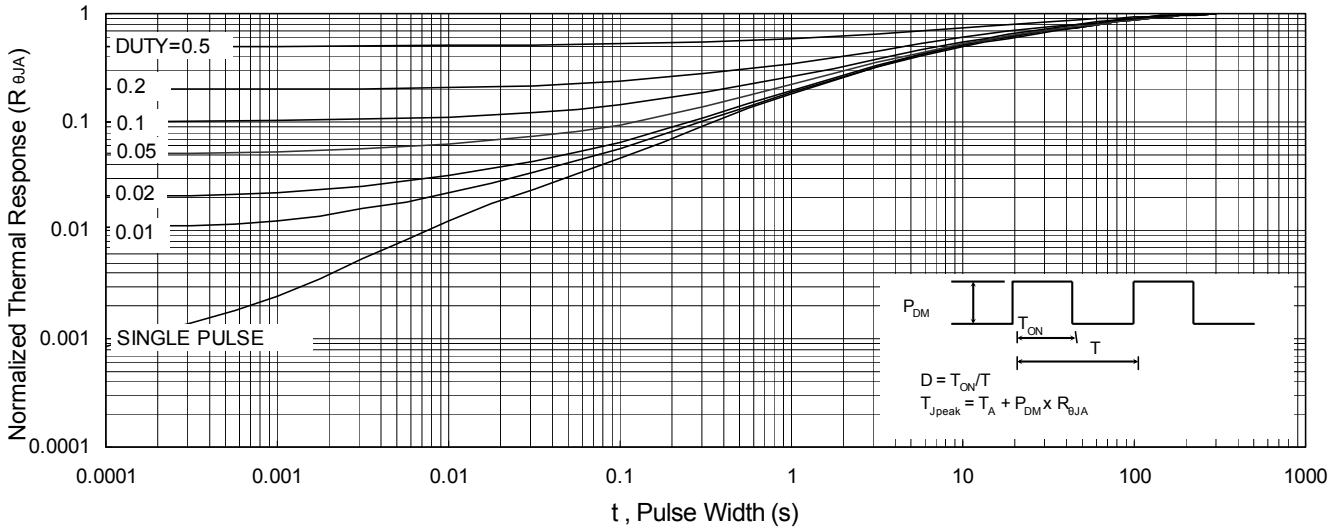


Fig.9 Normalized Maximum Transient Thermal Impedance

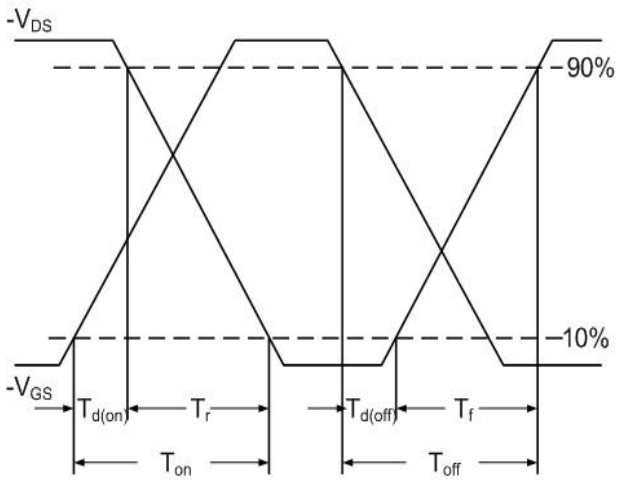


Fig.10 Switching Time Waveform

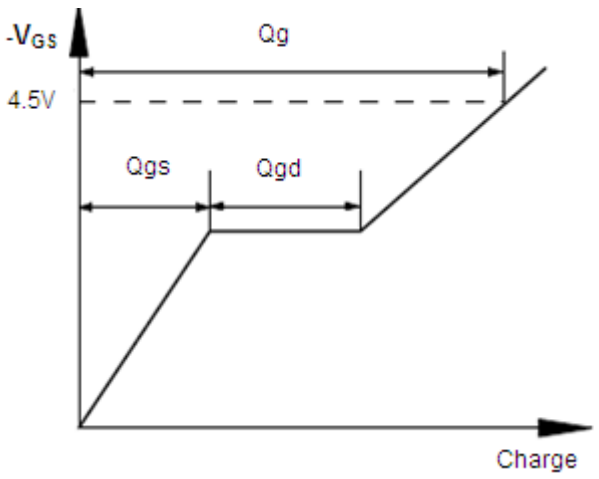
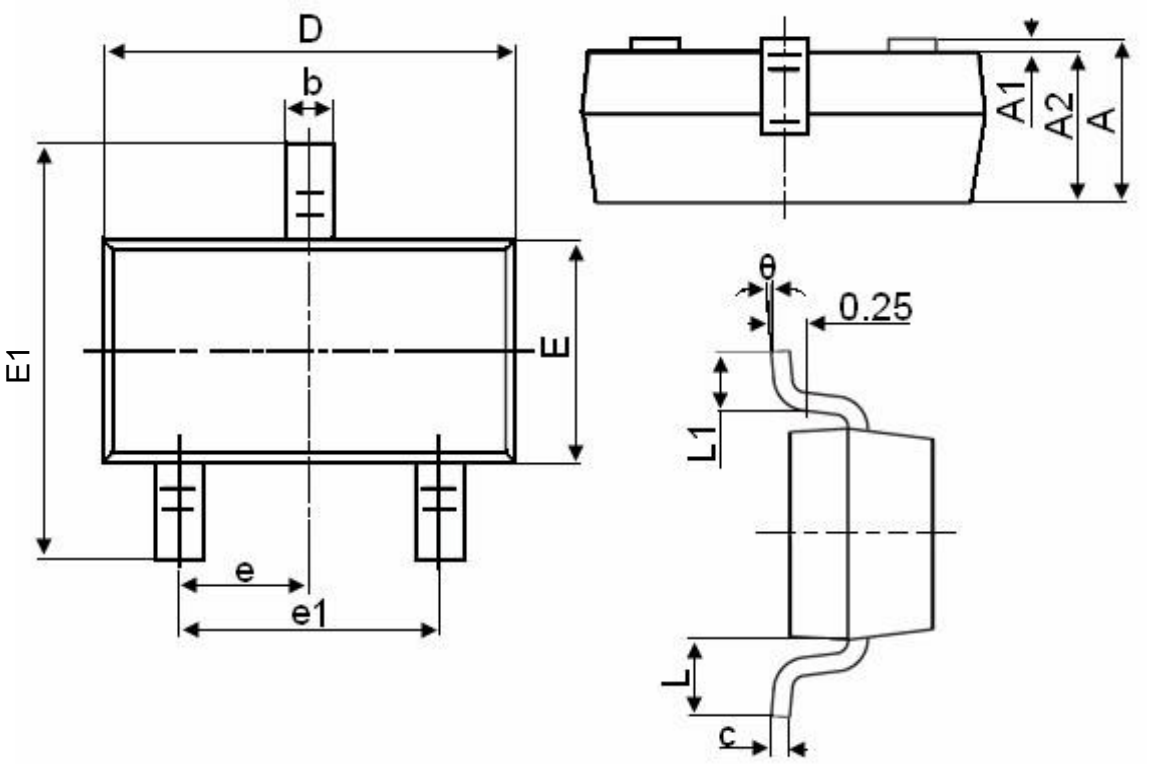


Fig.11 Gate Charge Waveform

Packaging information



Symbol	Dimensions in Millimeters	
	MIN.	MAX.
A	0.900	1.150
A1	0.000	0.100
A2	0.900	1.050
b	0.300	0.500
c	0.080	0.150
D	2.800	3.000
E	1.200	1.400
E1	2.250	2.550
e	0.950TYP	
e1	1.800	2.000
L	0.550REF	
L1	0.300	0.500
θ	0°	8°

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