

## **General Description**

The WSR60N06D uses advanced trench technology and design to provide excellent RDS(ON) with low gate charge. It can be used in a wide variety of applications.

#### **Features**

- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E<sub>AS</sub>
- Excellent package for good heat dissipation

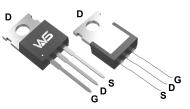
#### **Product Summery**

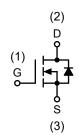
BV <sub>DSS</sub>	R <sub>DSON</sub>	I <sub>D</sub>
60V	13.5mΩ	60A

## **Application**

- Power switching application
- LED backlighting
- Uninterruptible power supply

## **TO-220-3L Pin Configuration**





#### **Absolute Maximum Ratings**

Symbol	Parameter	Units		
$V_{DS}$	Drain-Source Voltage	60	V	
$V_{GS}$	Gate-Source Voltage ±20			
I <sub>D</sub> @T <sub>C</sub> =25℃	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	Α		
I <sub>D</sub> @T <sub>C</sub> =100℃	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	41	Α	
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup>	120	Α	
EAS	Single Pulse Avalanche Energy <sup>3</sup>	390	mJ	
P <sub>D</sub> @T <sub>C</sub> =25℃	Total Power Dissipation <sup>4</sup> 89		W	
T <sub>J</sub> T <sub>STG</sub>	Operating Junction Temperature Range	$^{\circ}$		

### **Thermal Data**

Symbol	Parameter	Тур.	Max.	Unit
$R_{ heta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup>	62	°C/W	
$R_{ heta JC}$	Thermal Resistance Junction-Case <sup>1</sup>		1.68	°C/W

## Electrical Characteristics (T<sub>J</sub>=25 ℃, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit	
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS}$ =0 $V$ , $I_D$ =250 $u$ A	60			V	
$\triangle BV_{DSS}/\triangle T_{J}$	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25 $^{\circ}\mathrm{C}$ , ID=1mA		0.057		V/°C	
D	Static Drain-Source On-Resistance <sup>2</sup>	$V_{GS}$ =10V , $I_D$ =20A		13.5 20		0	
$R_{DS(ON)}$		V <sub>GS</sub> =4.5V , I <sub>D</sub> =10A		19	30	mΩ	
$V_{GS(th)}$	Gate Threshold Voltage	\/ -\/   -250\	1.2	1.8	2.5	V	
$\triangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient	$V_{GS}=V_{DS}$ , $I_D=250uA$		-5.68		mV/℃	
	Drain Source Loakage Current	$V_{DS}$ =48 $V$ , $V_{GS}$ =0 $V$ , $T_{J}$ =25 $^{\circ}$ C			1		
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =48V , V <sub>GS</sub> =0V , T <sub>J</sub> =55°C			5	· uA	
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}$ = $\pm 20V$ , $V_{DS}$ = $0V$			±100	nA	
gfs	Forward Transconductance	V <sub>DS</sub> =5V , I <sub>D</sub> =20A		25		S	
Rg	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz		1.7		Ω	
$Q_g$	Total Gate Charge (4.5V)			19.3			
$Q_{gs}$	Gate-Source Charge	$V_{DS}$ =30V , $V_{GS}$ =4.5V , $I_{D}$ =15A		7.1		nC	
$Q_{gd}$	Gate-Drain Charge			7.6			
T <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DS</sub> =30V , V <sub>GS</sub> =10V ,		7.2			
Tr	Rise Time	I <sub>D</sub> =15A , R=3.3Ω.		50			
T <sub>d(off)</sub>	Turn-Off Delay Time			7.6		ns	
T <sub>f</sub>	Fall Time			36.4		1	
Ciss	Input Capacitance			2426			
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> =15V , V <sub>GS</sub> =0V , f=1MHz		145		pF	
C <sub>rss</sub>	Reverse Transfer Capacitance			97			

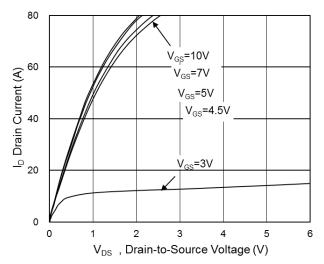
#### **Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current <sup>1,6</sup>	V =V =0V Force Current			35	Α
I <sub>SM</sub>	Pulsed Source Current <sup>2,6</sup>	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			90	Α
$V_{SD}$	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =1A , T <sub>J</sub> =25℃			1.2	V
t <sub>rr</sub>	Reverse Recovery Time	IF 44 - 11/-14 4004/ T1 05°C		16.3		nS
Q <sub>rr</sub>	Reverse Recovery Charge	IF=1A ,dI/dt=100A/µs,TJ=25℃		11		nC

#### Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board,  $t \le 10$  sec.
- 3. Pulse Test: Pulse Width  $\leq$  300 $\mu$ s, Duty Cycle  $\leq$  2%.
- 4. Guaranteed by design, not subject to production
- 5. E\_{AS} condition: Tj=25  $^{\circ}\text{C}\,,V_{DD}{=}30\text{V},V_{G}{=}10\text{V},L{=}0.5\text{mH},Rg{=}25\Omega$

## **Typical Characteristics**



**Fig.1 Typical Output Characteristics** 

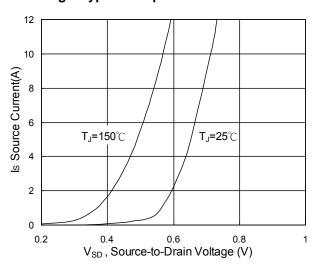


Fig.3 Forward Characteristics of Reverse

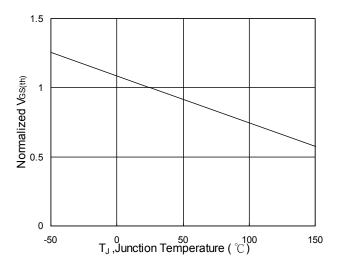


Fig.5 Normalized  $V_{\text{GS(th)}}\,\text{vs}\,\,T_{\text{J}}$ 

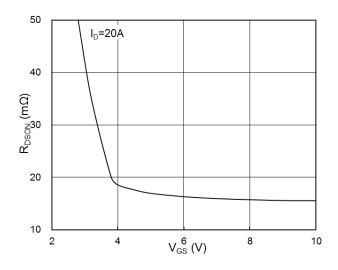


Fig.2 On-Resistance vs Gate-Source Voltage

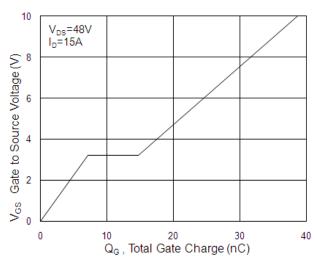


Fig.4 Gate-Charge Characteristics

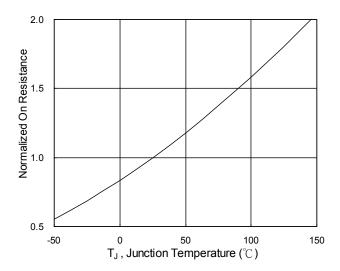
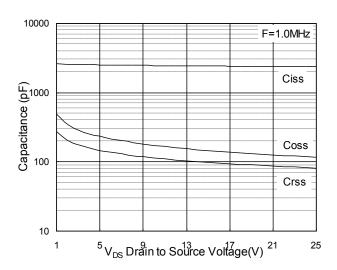


Fig.6 Normalized R<sub>DSON</sub> vs T<sub>J</sub>





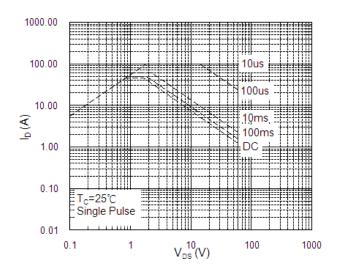


Fig.7 Capacitance

Fig.8 Safe Operating Area

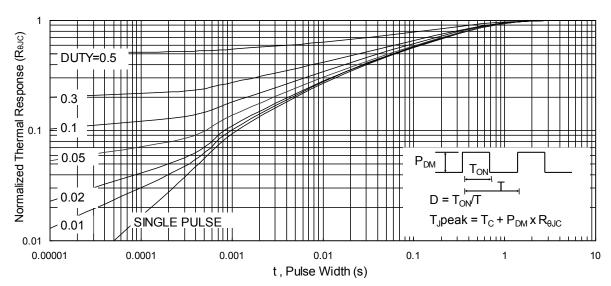
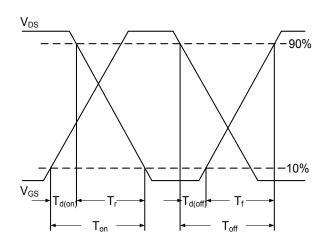


Fig.9 Normalized Maximum Transient Thermal Impedance





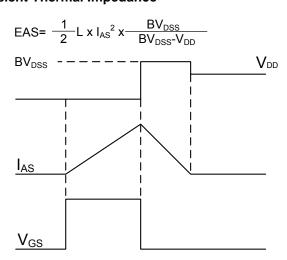
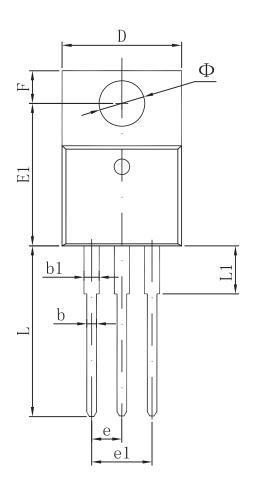
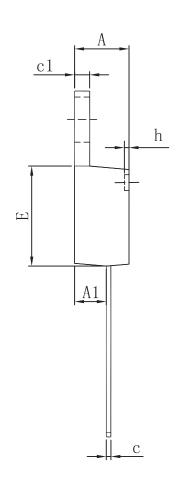


Fig.11 Unclamped Inductive Switching Waveform



# **Packaging information**





Cumbal	Dimensions In Millimeters		Dimension	s In Inches
Symbol	Min	Max	Min	Max
A	4.470	4.670	0.176	0.184
A1	2.520	2.820	0.099	0.111
b	0.710	0.910	0.028	0.036
b1	1.170	1.370	0.046	0.054
С	0.310	0.530	0.012	0.021
c1	1.170	1.370	0.046	0.054
D	10.010	10.310	0.394	0.406
Е	8.500	8.900	0.335	0.350
E1	12.060	12.460	0.475	0.491
е	2.540 TYP		0.100	) TYP
e1	4. 980	5. 180	0.196	0.204
F	2.590	2.890	0.102	0.114
h	0.000	0.300	0.000	0.012
L	13.400	13.800	0.528	0.543
L1	3.560	3.960	0.140	0.156
Ф	3. 735	3. 935	0.147	0.155



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