

General Description

The WSR60N06 uses advanced trench technology and design to provide excellent RDS(ON) with low gate charge. It can be used in a wide variety of applications.

Features

- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation

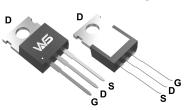
Product Summery

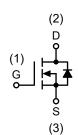
B _{VDSS}	R _{DSON}	I _D
60V	12mΩ	60A

Application

- Power switching application
- LED backlighting
- Uninterruptible power supply

TO-220-3L Pin Configuration





Absolute Maximum Ratings

Symbol	Parameter	Units	
V_{DS}	Drain-Source Voltage	60	V
V_{GS}	Gate-Source Voltage	±20	V
I _D @T _C =25℃	Continuous Drain Current, V _{GS} @ 10V ¹	Α	
I _D @T _C =100℃	Continuous Drain Current, V _{GS} @ 10V ¹	41	Α
I _{DM}	Pulsed Drain Current ²	120	Α
EAS	Single Pulse Avalanche Energy ³	290	mJ
P _D @T _C =25℃	Total Power Dissipation ⁴ 85		W
T _J T _{STG}	Operating Junction Temperature Range -55 to 150		

Thermal Data

Symbol	Parameter	Тур.	Max.	Unit
$R_{ heta JA}$	Thermal Resistance Junction-Ambient ¹		62	°C/W
$R_{ heta JC}$	Thermal Resistance Junction-Case ¹		0.57	°C/W

Electrical Characteristics (T_J=25 ℃, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit	
BV _{DSS}	Drain-Source Breakdown Voltage	V_{GS} =0V , I_D =250uA	60			V	
$\triangle BV_{DSS}/\triangle T_{J}$	BV _{DSS} Temperature Coefficient	Reference to 25°C , I _D =1mA		0.057		V/°C	
В	Static Drain-Source On-Resistance ²	V _{GS} =10V , I _D =30A		12	14	mΩ	
R _{DS(ON)}		V _{GS} =4.5V , I _D =20A		15	20		
$V_{GS(th)}$	Gate Threshold Voltage	-V _{GS} =V _{DS} , I _D =250uA	2.0	3.0	4.0	V	
$\triangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	V _{GS} -V _{DS} , I _D -250uA		-5.68		mV/℃	
	Drain Source Lookage Current	V_{DS} =48V , V_{GS} =0V , T_J =25 $^{\circ}$ C			1		
I _{DSS}	Drain-Source Leakage Current	V_{DS} =48V , V_{GS} =0V , T_J =55 $^{\circ}$ C			5	uA uA	
I _{GSS}	Gate-Source Leakage Current	V_{GS} = $\pm 20 V$, V_{DS} = $0 V$			±100	nA	
gfs	Forward Transconductance	V _{DS} =5V , I _D =15A		30		S	
Rg	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz		1.7	3.4	Ω	
Q_{g}	Total Gate Charge (4.5V)			36	45		
Q_gs	Gate-Source Charge	V_{DS} =30V , V_{GS} =4.5V , I_{D} =30A		9.9	18	nC	
Q _{gd}	Gate-Drain Charge			6.6	15		
T _{d(on)}	Turn-On Delay Time	V _{DS} =30V , V _{GS} =10V , I _D =2A ,		5.2	10		
Tr	Rise Time	$R=1\Omega$.		12	14.4		
T _{d(off)}	Turn-Off Delay Time			27	32	ns	
T _f	Fall Time			38	55		
Ciss	Input Capacitance			2498	2925		
C _{oss}	Output Capacitance	V _{DS} =25V , V _{GS} =0V , f=1MHz		185	203	pF	
C _{rss}	Reverse Transfer Capacitance			80	136		

Diode Characteristics

Symbol	Parameter Conditions		Min.	Тур.	Max.	Unit
Is	Continuous Source Current ^{1,6}	V -V -0V Force Current			38	Α
I _{SM}	Pulsed Source Current ^{2,6}	V _G =V _D =0V , Force Current			90	Α
V_{SD}	Diode Forward Voltage ²	V _{GS} =0V , I _S =1A , T _J =25℃			1.2	V
t _{rr}	Reverse Recovery Time	IF 4A 41/44 400A/ TI 05°C		35		nS
Q _{rr}	Reverse Recovery Charge	IF=1A ,dI/dt=100A/μs,TJ=25℃		47		nC

Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, $t \le 10$ sec.
- 3. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production
- 5. E_{AS} condition: Tj=25 $^{\circ}\text{C}$,V_DD=30V,V_G=10V,L=0.5mH,Rg=25

Typical Characteristics

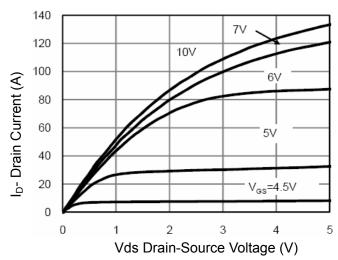


Figure 1 Output Characteristics

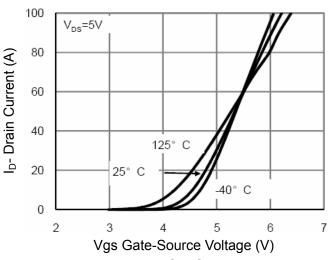


Figure 2 Transfer Characteristics

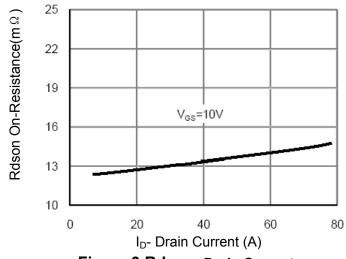


Figure 3 Rdson- Drain Current

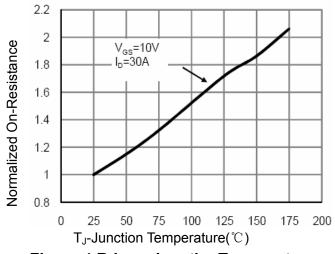


Figure 4 Rdson-JunctionTemperature

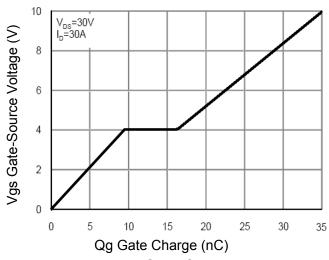


Figure 5 Gate Charge

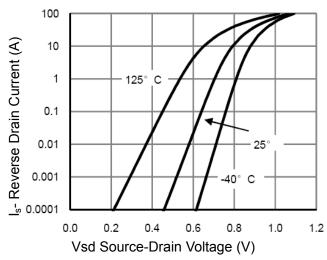


Figure 6 Source- Drain Diode Forward



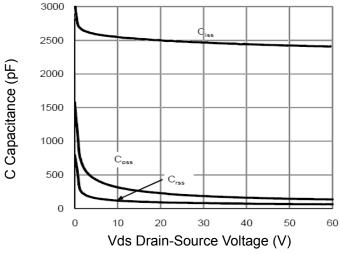


Figure 7 Capacitance vs Vds

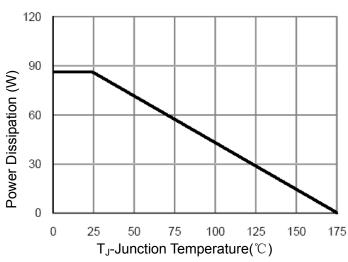


Figure 9 Power De-rating

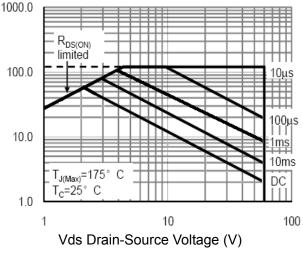


Figure 8 Safe Operation Area

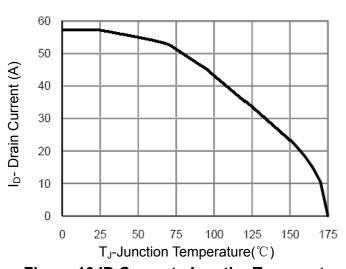


Figure 10 ID Current- JunctionTemperature

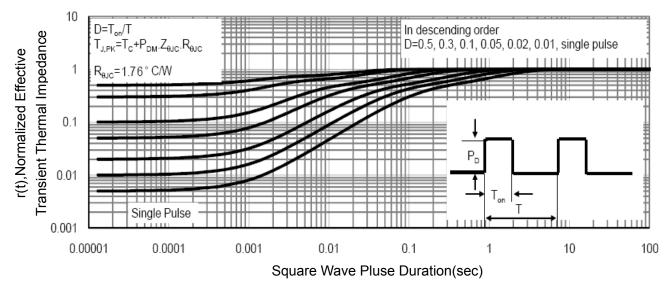
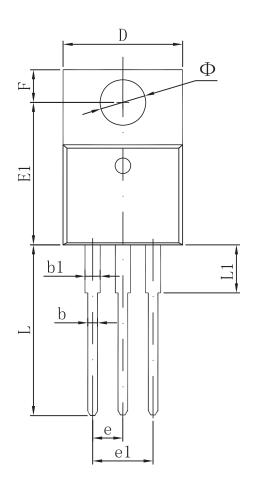
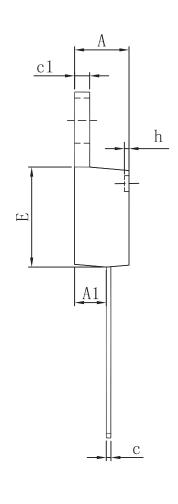


Figure 11 Normalized Maximum Transient Thermal Impedance



Packaging information





Cumbal	Dimensions In Millimeters		Dimension	s In Inches
Symbol	Min	Max	Min	Max
A	4.470	4.670	0.176	0.184
A1	2.520	2.820	0.099	0.111
b	0.710	0.910	0.028	0.036
b1	1.170	1.370	0.046	0.054
С	0.310	0.530	0.012	0.021
c1	1.170	1.370	0.046	0.054
D	10.010	10.310	0.394	0.406
Е	8.500	8.900	0.335	0.350
E1	12.060	12.460	0.475	0.491
е	2.540 TYP		0.100) TYP
e1	4. 980	5. 180	0.196	0.204
F	2.590	2.890	0.102	0.114
h	0.000	0.300	0.000	0.012
L	13.400	13.800	0.528	0.543
L1	3.560	3.960	0.140	0.156
Ф	3. 735	3. 935	0.147	0.155



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