

General Description

This WSR20N65F is produced using Truesemi's

advanced planar stripe DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for efficiency switched mode power supplies, active power factor correction based on half bridge topology.

Features

- High ruggedness
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

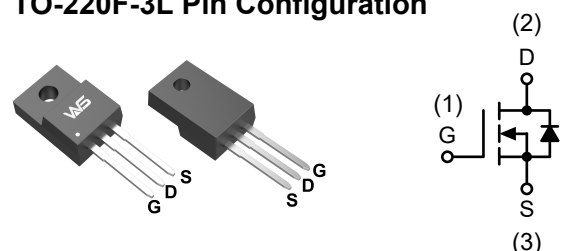
Product Summary

BV_{DSS}	$R_{DS(on)}$	I_D
650V	400mΩ	20A

Applications

- Power Management .
- AC-DC Converter
- LED TV Back Light

TO-220F-3L Pin Configuration



Absolute Maximum Ratings

$T_C = 25^\circ\text{C}$ unless otherwise specified

Symbol	Parameter		Value	Units
V_{DSS}	Drain-Source Voltage		650	V
V_{GS}	Gate-Source Voltage		± 30	V
I_D	Drain Current	$T_C = 25^\circ\text{C}$	20*	A
		$T_C = 100^\circ\text{C}$	12*	A
I_{DM}	Pulsed Drain Current		76*	A
E_{AS}	Single Pulsed Avalanche Energy	(Note 2)	884	mJ
E_{AR}	Repetitive Avalanche Energy	(Note 1)	4	mJ
I_{AR}	Repetitive avalanche current	(Note 1)	20	A
P_D	Power Dissipation ($T_C = 25^\circ\text{C}$)		80	W
T_J, T_{STG}	Operating and Storage Temperature Range		-55 to +150	$^\circ\text{C}$

* Drain current limited by maximum junction temperature.

Thermal Resistance Characteristics

Symbol	Parameter	Value	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	1.56	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	62.5	$^\circ\text{C/W}$

Electrical Characteristics $T_C=25^\circ\text{C}$ unless otherwise specified

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
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On Characteristics

V_{GS}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	3	--	5	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS} = 10\text{ V}, I_D = 10\text{ A}$	--	400	480	m Ω
g_{fs}	Forward transfer conductance(note 3)	$V_{DS} = 10\text{ V}, I_D = 10\text{ A}$ (Note 3)	--	18	--	S

Off Characteristics

BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	650	--	--	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 650\text{ V}, V_{GS} = 0\text{ V}$	--	--	1	μA
		$V_{DS} = 650\text{ V}, T_C = 125^\circ\text{C}$	--	--	100	
I_{GSSF}	Gate-Body Leakage Current, Forward	$V_{GS} = 30\text{ V}, V_{DS} = 0\text{ V}$	--	--	100	nA
I_{GSSR}	Gate-Body Leakage Current, Reverse	$V_{GS} = -30\text{ V}, V_{DS} = 0\text{ V}$	--	--	-100	nA

Dynamic Characteristics

C_{iss}	Input Capacitance	$V_{DS} = 25\text{ V}, V_{GS} = 0\text{ V},$ $f = 1.0\text{ MHz}$	--	5150	--	pF
C_{oss}	Output Capacitance		--	264	--	pF
C_{rss}	Reverse Transfer Capacitance		--	24	--	pF

Switching Characteristics

$t_{d(on)}$	Turn-On Time	$V_{DS} = 300\text{ V}, I_D = 20\text{ A},$ $R_G = 25\text{ }\Omega$ (Note 3,4)	--	149	--	ns
t_r	Turn-On Rise Time		--	197	--	ns
$t_{d(off)}$	Turn-Off Delay Time		--	83	--	ns
t_f	Turn-Off Fall Time		--	468	--	ns
Q_g	Total Gate Charge	$V_{DS} = 480\text{ V}, I_D = 20\text{ A},$ $V_{GS} = 10\text{ V}$ (Note 3,4)	--	57	65	nC
Q_{gs}	Gate-Source Charge		--	23	--	nC
Q_{gd}	Gate-Drain Charge		--	13	--	nC

Source-Drain Diode Maximum Ratings and Characteristics

I _S	Continuous Source-Drain Diode Forward Current		--	--	20	A
I _{SM}	Pulsed Source-Drain Diode Forward Current		--	--	72	
V _{SD}	Source-Drain Diode Forward Voltage	I _S = 20A, V _{GS} = 0 V	--	--	1.4	V
t _{rr}	Reverse Recovery Time	I _S =20A, V _{GS} = 0 V di _F /dt = 100 A/μs (Note 3,4)	--	435	--	ns
Q _{rr}	Reverse Recovery Charge		--	4.1	--	μC

Note:

1. Repeated rating: Pulse width limited by safe operating area
2. $L=5\text{ mH}$, $I_{AS}=20\text{ A}$, $V_{DD}=50\text{ V}$, $R_G=25\text{ }\Omega$, Starting $T_J=25^\circ\text{C}$
3. Pulse test: Pulse width $\leq 300\text{ }\mu\text{s}$, Duty cycles $\leq 2\%$
4. Essentially independent of operating temperature typical characteristics

Typical Characteristics

Fig. 1 Typical Output Characteristics

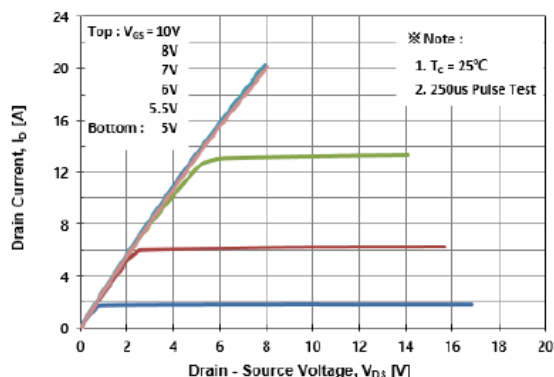


Fig. 2 Typical Output Characteristics

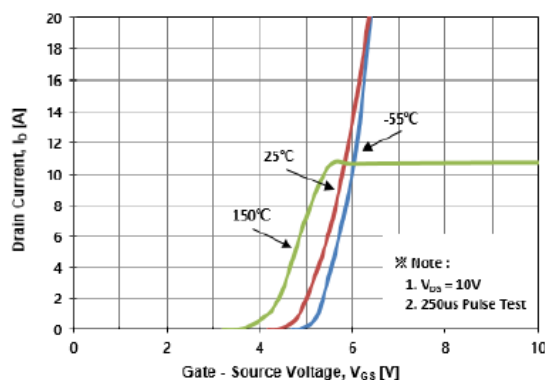


Fig.3 On-Resistance Variation with Drain Current and Gate Voltage

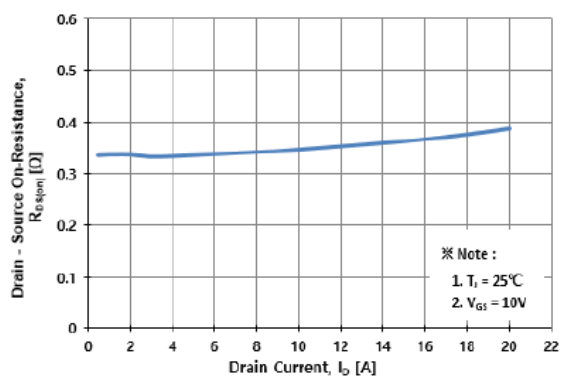


Fig. 4 Body Diode Forward Voltage Variation with Source Current

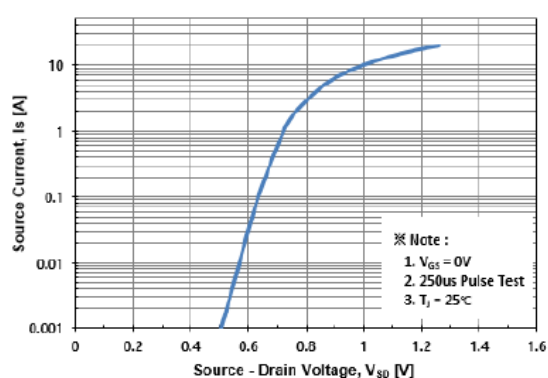


Fig. 5 Typical Capacitance Characteristics

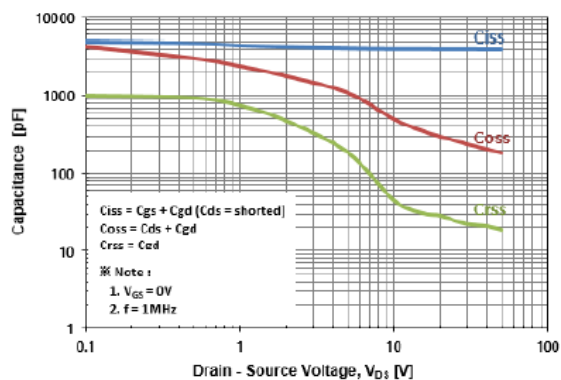
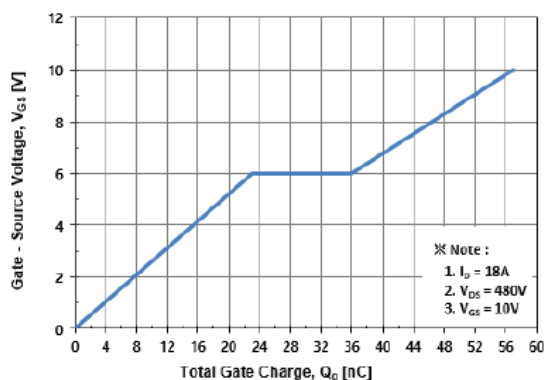


Fig. 6 Typical Total Gate Charge Characteristics



Typical Characteristics

Fig. 7 Breakdown Voltage Variation vs. Temperature

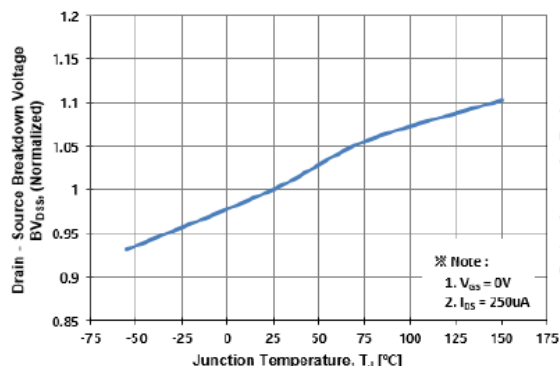


Fig. 8 On-Resistance Variation vs. Temperature

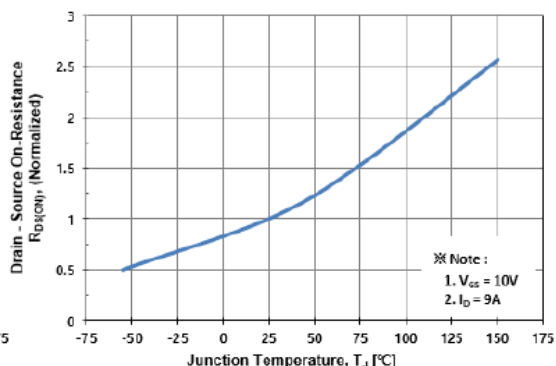


Fig. 9 Maximum Drain Current vs. Case Temperature

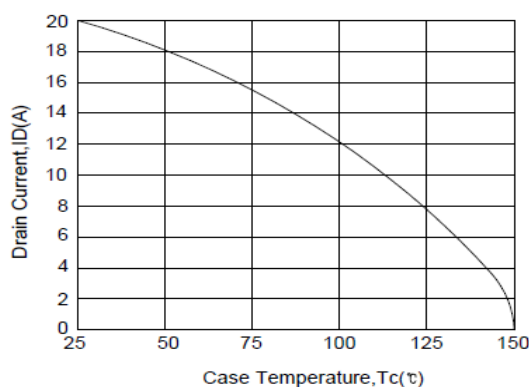


Fig. 10 Maximum Safe Operating Area

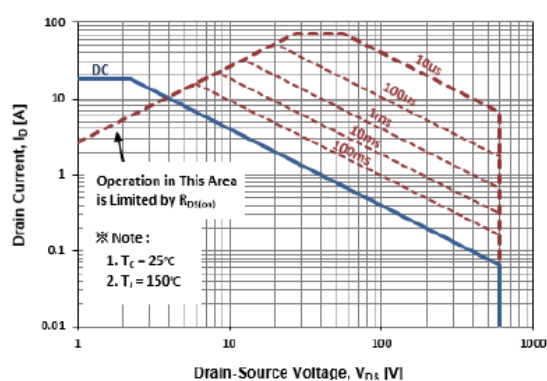
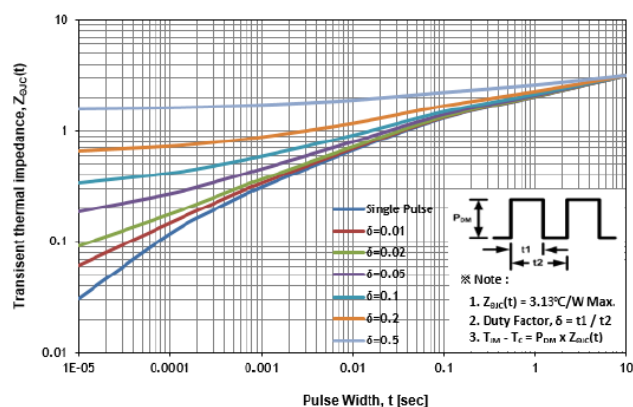
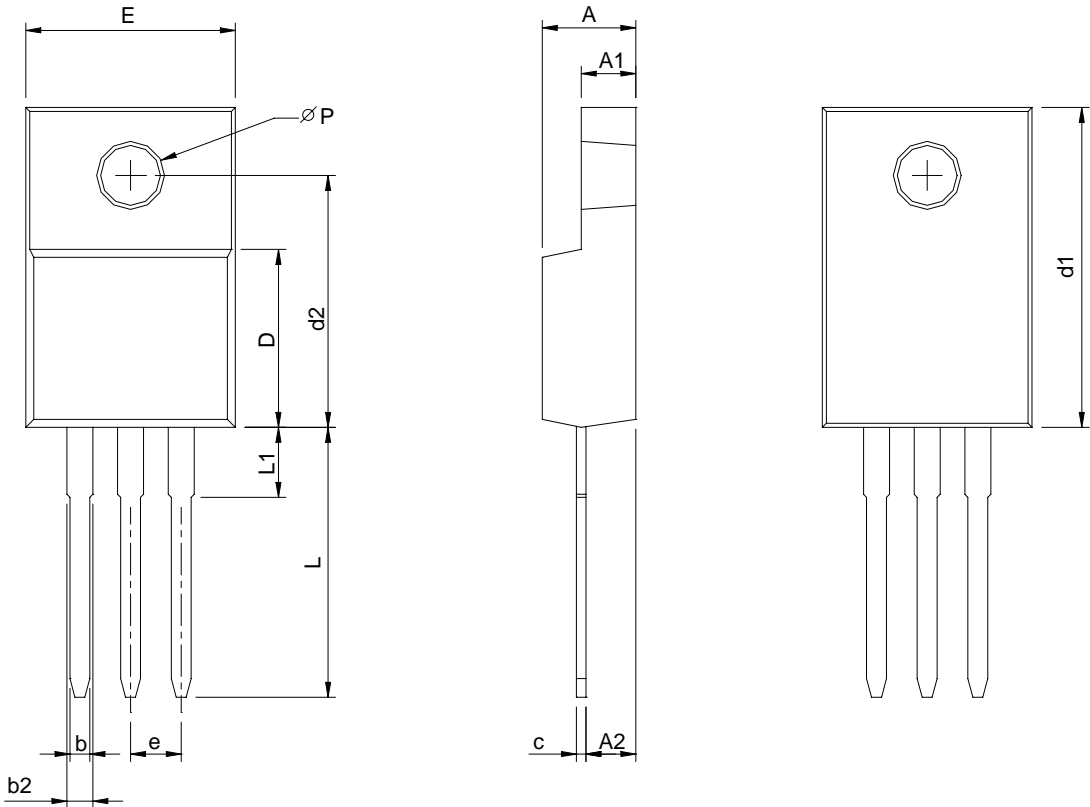


Fig. 11 Transient Thermal Impedance

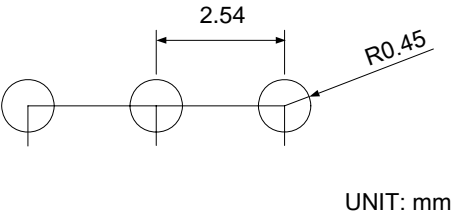


Packaging information



SYMBOL	TO-220F-3L			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	4.20	4.80	0.165	0.189
A1	2.34	3.20	0.092	0.126
A2	2.10	2.90	0.083	0.114
b	0.50	0.90	0.020	0.035
b2	0.91	1.90	0.035	0.075
c	0.30	0.80	0.012	0.031
D	8.10	9.40	0.319	0.370
d1	14.50	16.50	0.571	0.650
d2	12.10	12.90	0.476	0.508
E	9.70	10.70	0.382	0.421
e	2.54 BSC		0.100 BSC	
L	13.00	14.50	0.512	0.570
L1	1.60	4.00	0.063	0.157
P	3.00	3.60	0.118	0.142

RECOMMENDED LAND PATTERN





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