

N-Ch MOSFET

General Description

The WSF60N06A uses advanced trench technology and design to provide excellent RDS(ON) with low gate charge. It can be used in a wide variety of applications.

Features

- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation
- Special process technology for high ESD capability

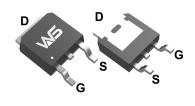
Product Summery

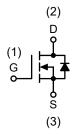
BVDSS	RDSON	ID
60V	12mΩ	60A

Application

- Power switching application
- LED backlighting
- Uninterruptible power supply

TO-252-2L Pin Configuration





Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	60	V
V_{GS}	Gate-Source Voltage	±20	V
I _D @T _C =25℃	Continuous Drain Current, V _{GS} @ 10V ¹	60	Α
I _D @T _C =100℃	Continuous Drain Current, V _{GS} @ 10V ¹	41	Α
I _{DM}	Pulsed Drain Current ²	250	Α
EAS	Single Pulse Avalanche Energy ³	290	mJ
P _D @T _C =25℃	Total Power Dissipation⁴	85	W
T _J T _{STG}	Operating Junction Temperature Range -55 to 150		

Thermal Data

Symbol	Parameter	Тур.	Max.	Unit
$R_{ heta JA}$	Thermal Resistance Junction-Ambient ¹		62	°C/W
$R_{ heta JC}$	Thermal Resistance Junction-Case ¹		0.57	°C/W



Electrical Characteristics (T_J=25 ℃, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit	
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	60			V	
$\triangle BV_{DSS}/\triangle T_{J}$	BV _{DSS} Temperature Coefficient	Reference to 25 $^{\circ}$ C , I _D =1mA		0.057		V/°C	
D	Static Drain-Source On-Resistance ²	V _{GS} =10V , I _D =30A	12 14				
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =4.5V , I _D =20A		15	20	mΩ	
$V_{GS(th)}$	Gate Threshold Voltage	V =V =250A	2.0	3.0	4.0	V	
$\triangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	$V_{GS}=V_{DS}$, $I_D=250uA$		-5.68		mV/℃	
	Dunin Course Legland Cumant	V _{DS} =48V , V _{GS} =0V , T _J =25℃			1		
I _{DSS}	Drain-Source Leakage Current	V _{DS} =48V , V _{GS} =0V , T _J =55℃			5	- uA	
I _{GSS}	Gate-Source Leakage Current	V_{GS} = $\pm 20V$, V_{DS} = $0V$			±100	nA	
gfs	Forward Transconductance	V _{DS} =5V , I _D =15A		30		S	
Rg	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz		1.7	3.4	Ω	
Qg	Total Gate Charge (4.5V)			36	45		
Q _{gs}	Gate-Source Charge	V_{DS} =30V , V_{GS} =4.5V , I_{D} =30A		9.9	18	nC	
Q _{gd}	Gate-Drain Charge			6.6	15		
T _{d(on)}	Turn-On Delay Time	V _{DS} =30V , V _{GS} =10V , I _D =2A ,		5.2	10		
Tr	Rise Time	$R=1\Omega$.		12	14.4		
T _{d(off)}	Turn-Off Delay Time			27	32	ns	
T _f	Fall Time			38	55	1	
C _{iss}	Input Capacitance			2498	2925		
C _{oss}	Output Capacitance	V _{DS} =25V , V _{GS} =0V , f=1MHz		185	203	pF	
C _{rss}	Reverse Transfer Capacitance			80	136		

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current ^{1,6}	V -V -0V Force Current			38	Α
I _{SM}	Pulsed Source Current ^{2,6}	V _G =V _D =0V , Force Current			90	Α
V_{SD}	Diode Forward Voltage ²	V _{GS} =0V , I _S =1A , T _J =25℃			1.2	V
t _{rr}	Reverse Recovery Time	IF 44 - 11/-14 4004/ T1 05°C		35		nS
Q _{rr}	Reverse Recovery Charge	IF=1A ,dI/dt=100A/µs,TJ=25℃		47		nC

Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, $t \le 10$ sec.
- 3. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production
- 5. E_{AS} condition: Tj=25 $^{\circ}\text{C}$,V_DD=30V,V_G=10V,L=0.5mH,Rg=25



Typical Characteristics

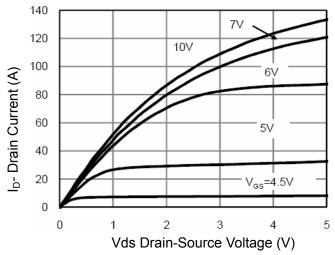


Figure 1 Output Characteristics

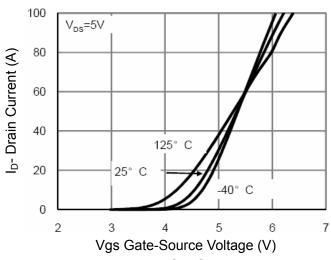


Figure 2 Transfer Characteristics

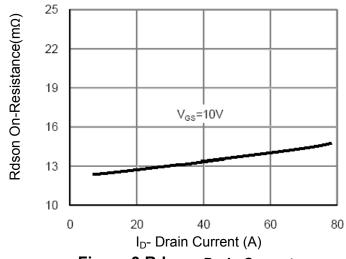


Figure 3 Rdson- Drain Current

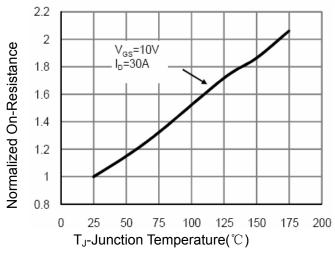


Figure 4 Rdson-JunctionTemperature

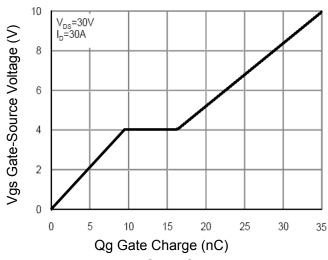


Figure 5 Gate Charge

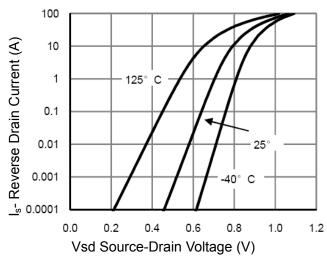


Figure 6 Source- Drain Diode Forward

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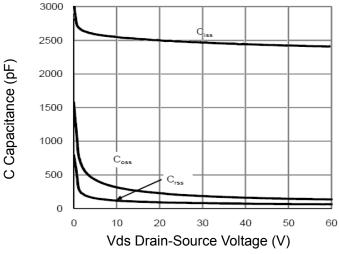


Figure 7 Capacitance vs Vds

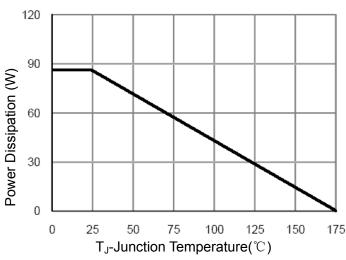


Figure 9 Power De-rating

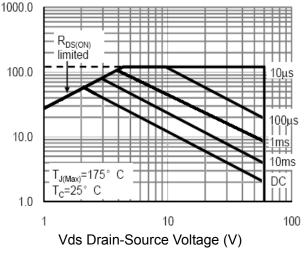


Figure 8 Safe Operation Area

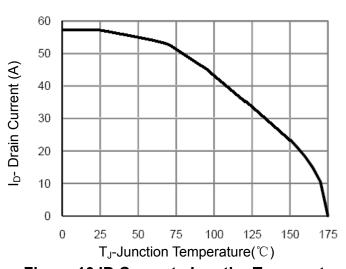


Figure 10 ID Current- JunctionTemperature

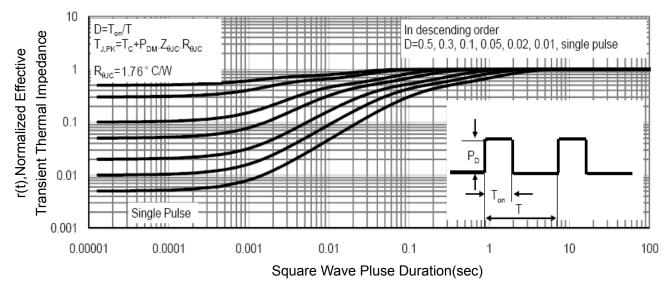
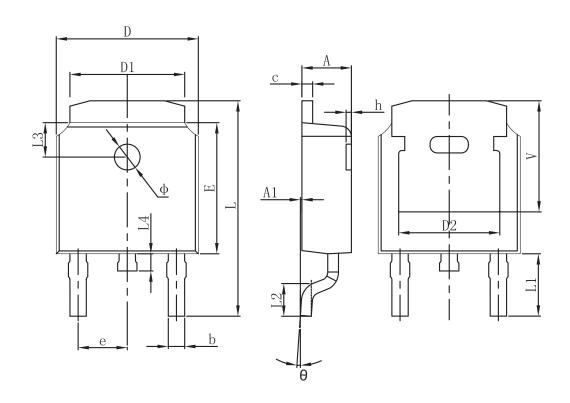


Figure 11 Normalized Maximum Transient Thermal Impedance



Packaging information



Symbol	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.635	0.770	0.025	0.030	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	4.830	REF.	0.190	REF.	
E	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.712	10.312	0.382	0.406	
L1	2.900 REF.		0.114 REF.		
L2	1.400	1.700	0.055	0.067	
L3	1.600 REF.		0.063 REF.		
L4	0.600	1.000	0.024	0.039	
Ф	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.250	REF.	0.207 REF.		



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