

N-Ch MOSFET

General Description

The WSF5N50A is silicon N-channel Enhanced VDMOSFETs, is obtained by the self-aligned planar Technology which reduce the conduction loss, improve switching performance and enhance the avalanche energy.

The transistor can be used in various power switching circuit for system miniaturization and higher efficiency.

Features

- Super Low Gate Charge
- Excellent Cdv/dt effect decline
- Green Device Available

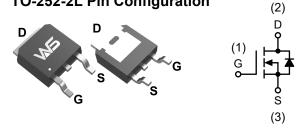
Product Summery

BV _{DSS}	R _{DSON}	I _D		
500V	2400mΩ	4.8A		

Applications

- Uninterruptible Power Supply(UPS)
- Power Factor Correction (PFC)

TO-252-2L Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	500	V
V _{GS}	Gate-Source Voltage	±30	V
I _D @T _C =25℃	Continuous Drain Current, V _{GS} @ 10V ¹	4.8	Α
I _{DM}	Pulsed Drain Current ²	14.4	A
EAS	Single Pulse Avalanche Energy	57	mJ
I _{AR}	Avalanche Current ²	2.4	A
EAR	Repetitive Avalanche Energy	6.4	mJ
P₀@T₀=25℃	Total Power Dissipation	32.9	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Тур.	Max.	Unit
R _{θJA}	Thermal Resistance Junction-ambient ¹		62.5	°C/W
R _{eJC}	Thermal Resistance Junction-Case ¹		6.25	°C/W



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Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit	
BV _{DSS}	Drain-Source Breakdown Voltage	V_{GS} =0V , I _D =250uA	500			V	
$\triangle BV_{DSS} / \triangle T_J$	BVDSS Temperature Coefficient	Reference to 25 $^\circ\!\mathrm{C}$, I_D=1mA		0.25		V/℃	
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V_{GS} =10V , I _D =3.5A		2400	3000	mΩ	
V _{GS(th)}	Gate Threshold Voltage		2.0	3.0	4.0	V	
$ riangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	$v_{GS} - v_{DS}$, $I_D - 2500A$		-4.64		mV/℃	
IDSS	Drain-Source Leakage Current	$V_{\text{DS}}\text{=}400V$, $V_{\text{GS}}\text{=}0V$, $T_{\text{J}}\text{=}25^\circ\!\mathbb{C}$			1	– uA	
USS		$V_{\text{DS}}\text{=}400\text{V}$, $V_{\text{GS}}\text{=}0\text{V}$, $T_{\text{J}}\text{=}150^\circ\!\!\mathbb{C}$			200	u.,	
I _{GSS}	Gate-Source Leakage Current	V_{GS} = $\pm30V$, V_{DS} =0V			±100	nA	
gfs	orward Transconductance	V _{DS} =30V , I _D =2.5A		5.2		S	
Rg	Gate Resistance	V_{DS} =0V , V_{GS} =0V , f=1MHz		4.2		Ω	
Qg	Total Gate Charge (10V)			8			
Q _{gs}	Gate-Source Charge	V_{DS} =400V , V_{GS} =10V , I_{D} =2A		1.2		nC	
Q _{gd}	Gate-Drain Charge			5			
T _{d(on)}	Turn-On Delay Time			7.8			
Tr	Rise Time			33			
T _{d(off)}	Turn-Off Delay Time	$V_{DD}=250V$, $R_{G}=25\Omega$, $I_{D}=3A$.		23		ns	
T _f	Fall Time			59			
Ciss	Input Capacitance			310			
C _{oss}	Output Capacitance	V _{DS} =25V , V _{GS} =0V , f=1MHz		39		pF	
C _{rss}	Reverse Transfer Capacitance			6			

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
I _S	Continuous Source Current ^{1,5}	V _G =V _D =0V , Force Current			3.0	А
I _{SM}	Pulsed Source Current ^{2,5}	V _G =V _D =UV, Force Current			12	А
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V , I _S =3A , TJ=25℃			1.4	V
t _{rr}	Reverse Recovery Time	ls=3A,dl/dt=100A/µs,Tյ=25℃		80		nS
Qrr	Reverse Recovery Charge	15-5A, $17-25C$		1.8		uC

Note :

1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper,t<10sec.

2.The data tested by pulsed , pulse width $\leq 300 us$, duty cycle $\leq 2\%$

3. The power dissipation is limited by 150 °C junction temperature

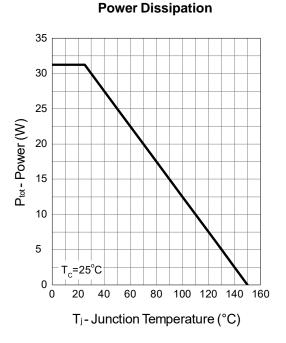
4. The Min. value is 100% EAS tested guarantee.

5. The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.



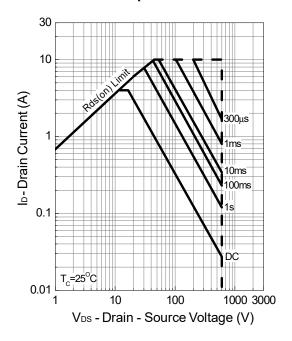
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Typical Characteristics

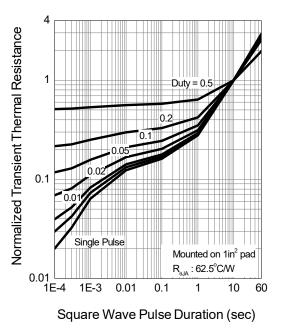


Drain Current 5 4 Ip-Drain Current (A) 3 2 1 T_=25 С 10 0 ∟ 0 20 40 60 80 100 120 140 160 T_j-Junction Temperature (°C)

Safe Operation Area



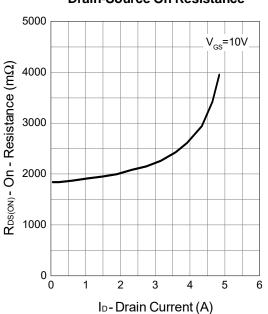
Thermal Transient Impedance





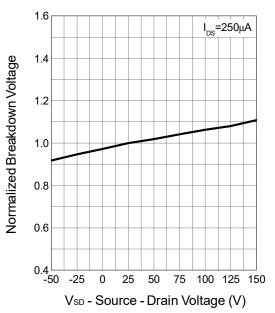
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Typical Characteristics (Cont.)

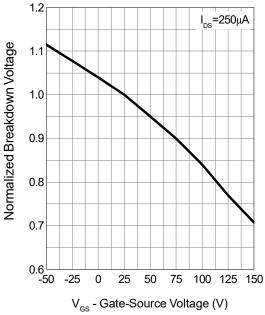


Drain-Source On Resistance

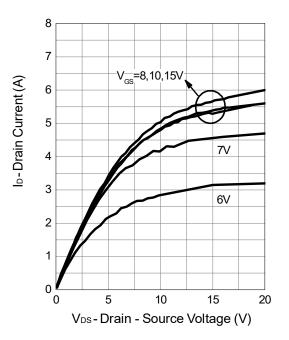
BVDSS vs Junction Temperature



 $V_{GS(th)}$ vs Junction Temperature



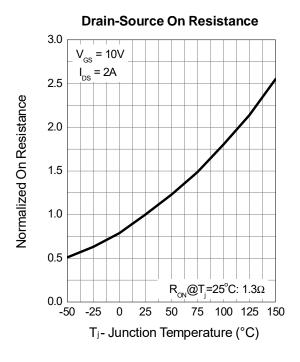
Output Characteristics

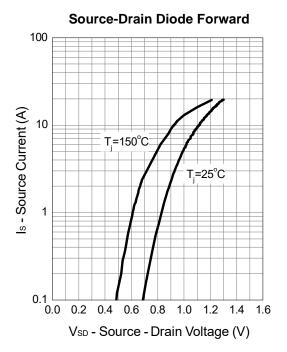




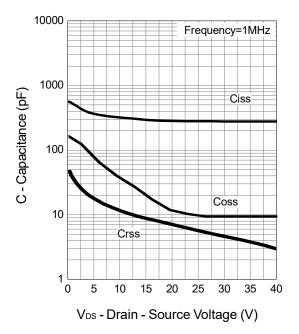
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Typical Characteristics (Cont.)

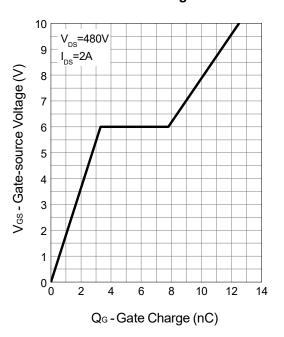




Capacitance



Gate Charge

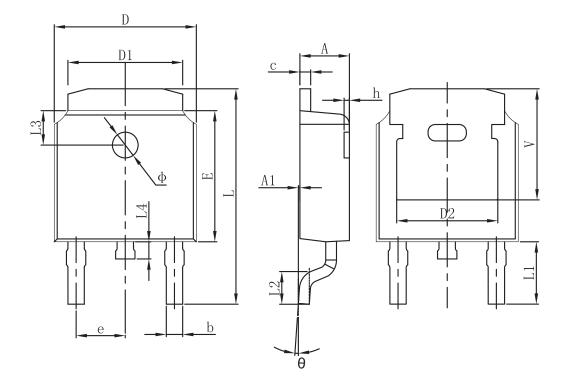




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WSF5N50A

Packaging information



Symbol	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
A	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.635	0.770	0.025	0.030	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	4.830 REF.		0.190 REF.		
E	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.712	10.312	0.382	0.406	
L1	2.900 REF.		0.114	REF.	
L2	1.400	1.700	0.055	0.067	
L3	1.600 REF.		0.063 REF.		
L4	0.600	1.000	0.024	0.039	
Φ	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.250	REF.	0.207 REF.		



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