

General Description

The WSF35N10 is advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

The WSF35N10 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent C_{dv}/dt effect decline
- Green Device Available

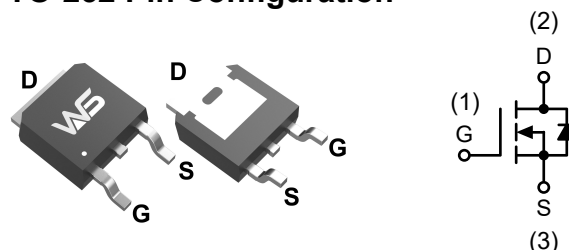
Product Summary

BV_{DSS}	$R_{DS(ON)}$	I_D
100V	36m Ω	35A

Applications

- Automotive lighting
- Load switch
- Uninterruptible power supply

TO-252 Pin Configuration



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Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_C=25^\circ C$	Continuous Drain Current, V_{GS} @ 10V	35	A
$I_D@T_C=100^\circ C$	Continuous Drain Current, V_{GS} @ 10V	13	A
I_{DM}	Pulsed Drain Current ¹	90	A
$P_D@T_C=25^\circ C$	Total Power Dissipation	42	W
$P_D@T_C=25^\circ C$	Total Power Dissipation ³	1.7	W
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ C$

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient	---	62.5	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction-Case	---	3.6	$^\circ C/W$

Electrical Characteristics ($T_J=25^{\circ}\text{C}$, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V$, $I_D=250\mu A$	100	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=10V$, $I_D=10A$	---	36	48	$m\Omega$
		$V_{GS}=4.5V$, $I_D=6A$	---	39	55	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=250\mu A$	1.0	1.5	2.2	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=100V$, $V_{GS}=0V$	---	---	1.0	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V$, $V_{DS}=0V$	---	---	± 100	nA
Q_g	Total Gate Charge	$V_{DS}=80V$, $V_{GS}=4.5V$, $I_D=20A$	---	20	---	nC
Q_{gs}	Gate-Source Charge		---	3.1	---	
Q_{gd}	Gate-Drain Charge		---	14	---	
$T_{d(on)}$	Turn-On Delay Time	$V_{DD}=80V$, $V_{GS}=4.5V$, $R_G=3.1\Omega$, $I_D=20A$	---	11	---	ns
T_r	Rise Time		---	91	---	
$T_{d(off)}$	Turn-Off Delay Time		---	40	---	
T_f	Fall Time		---	71	---	
C_{iss}	Input Capacitance	$V_{DS}=25V$, $V_{GS}=0V$, $f=1\text{MHz}$	---	1964	---	pF
C_{oss}	Output Capacitance		---	90	---	
C_{rss}	Reverse Transfer Capacitance		---	74	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	$V_G=V_D=0V$, Force Current	---	---	30	A
I_{SM}	Pulsed Source Current		---	---	80	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0V$, $I_S=20A$	---	---	1.2	V

Note :

1. The data tested by surface mounted on a 1 inch 2 FR-4 board with 2OZ copper.
2. The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
3. The EAS data shows Max. rating . The test condition is $V_{DD}=72V$, $V_{GS}=10V$, $L=0.1mH$, $I_{AS}=10A$
4. The power dissipation is limited by 150°C junction temperature
5. The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Characteristics

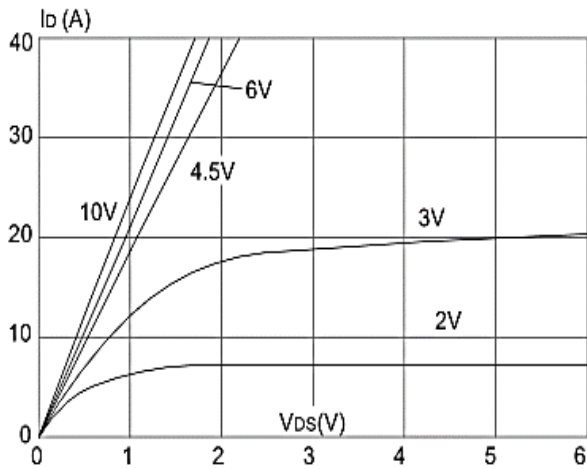


Figure 1: Output Characteristics

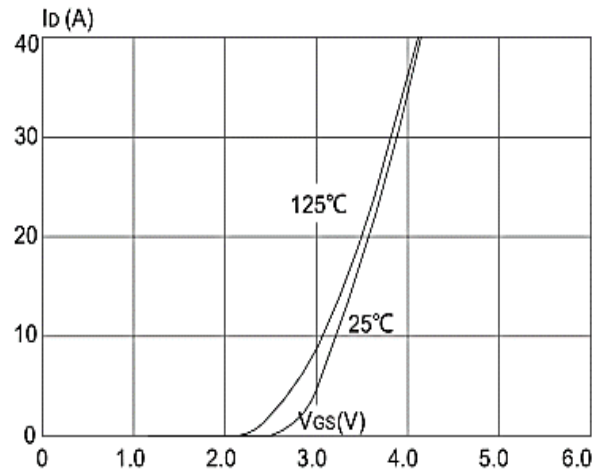


Figure 2: Typical Transfer Characteristics

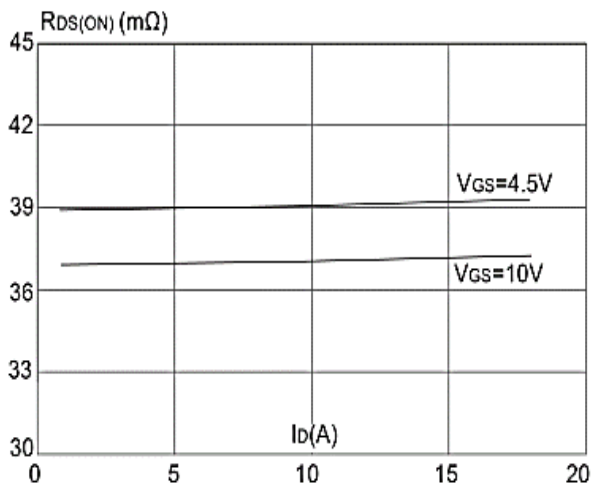


Figure 3: On-resistance vs. Drain Current

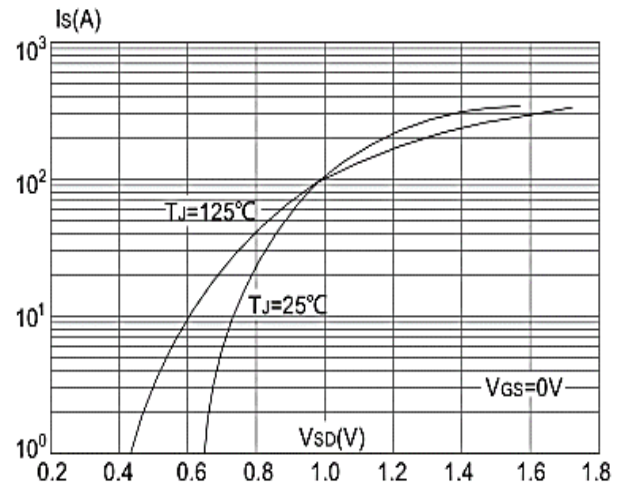


Figure 4: Body Diode Characteristics

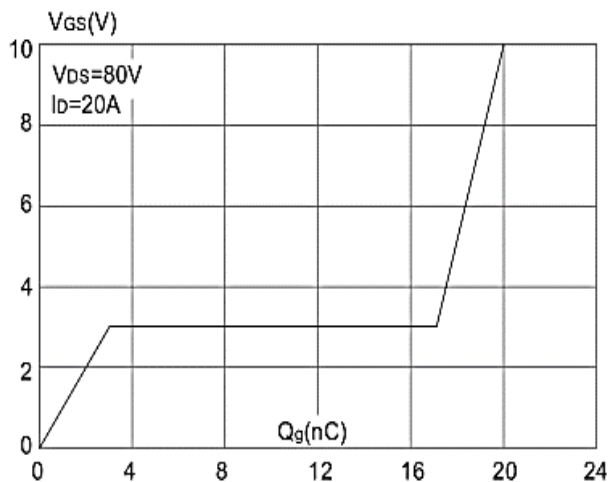


Figure 5: Gate Charge Characteristics

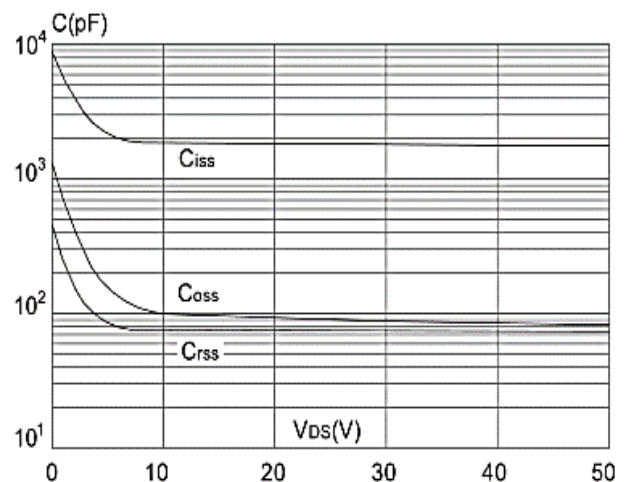


Figure 6: Capacitance Characteristics

Typical Characteristics (Cont.)

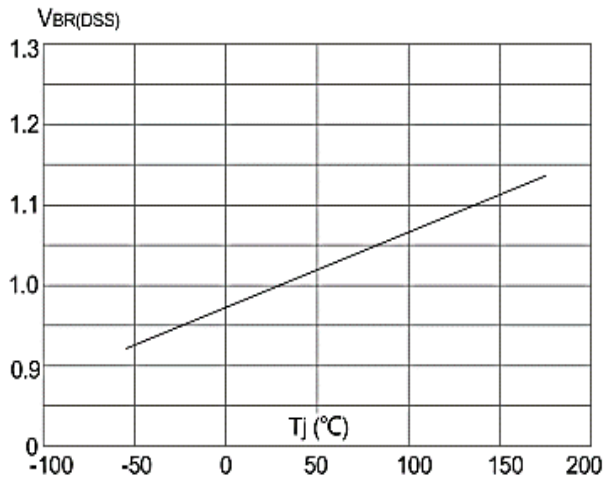


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

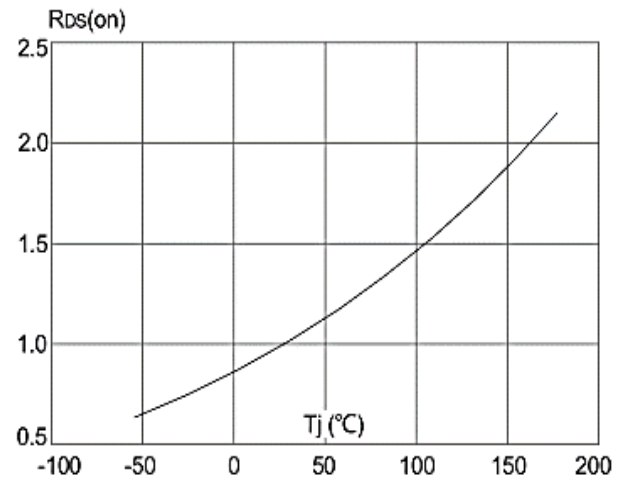


Figure 8: Normalized on Resistance vs. Junction Temperature

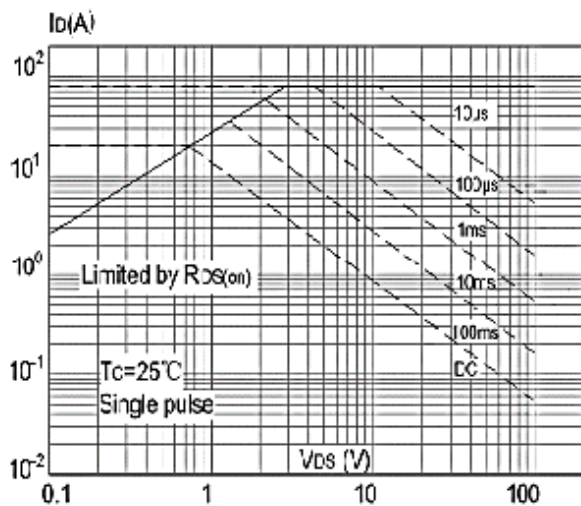


Figure 9: Maximum Safe Operating Area vs. Case Temperature

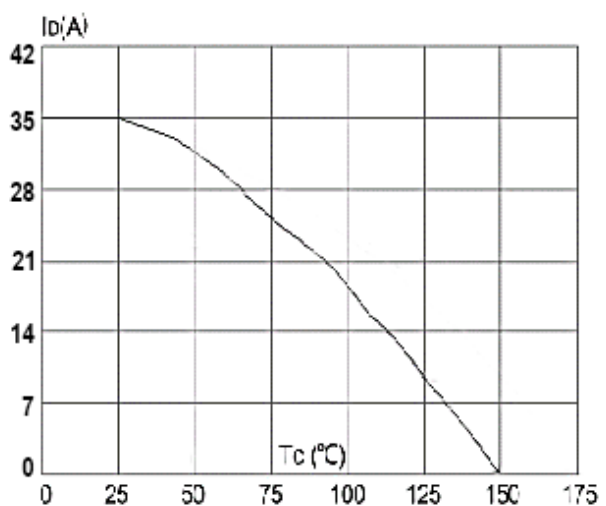


Figure 10: Maximum Continuous Drain Current

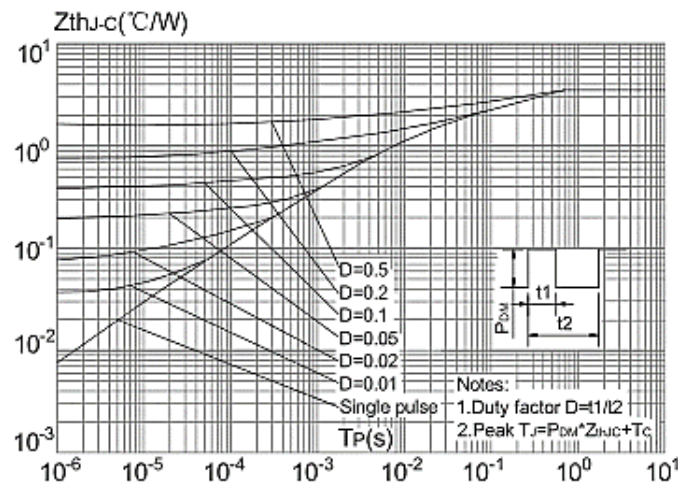
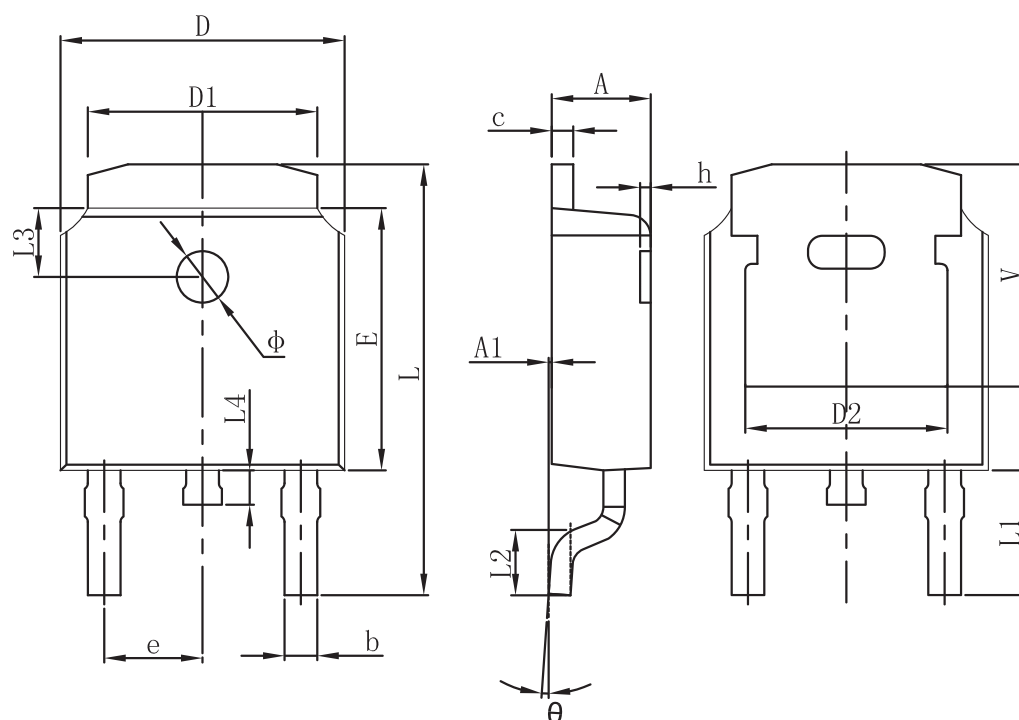


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Case

Packaging information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.635	0.770	0.025	0.030
c	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	4.830 REF.		0.190 REF.	
E	6.000	6.200	0.236	0.244
e	2.186	2.386	0.086	0.094
L	9.712	10.312	0.382	0.406
L1	2.900 REF.		0.114 REF.	
L2	1.400	1.700	0.055	0.067
L3	1.600 REF.		0.063 REF.	
L4	0.600	1.000	0.024	0.039
Φ	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.250 REF.		0.207 REF.	



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