

## **General Description**

The WSF3036 is the highest performance trench N-ch MOSFETs with extreme high cell density , which provide excellent  $R_{\text{DSON}}$  and gate charge for most of the synchronous buck converter applications .

The WSF3036 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

#### **Features**

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

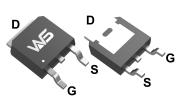
## **Product Summery**

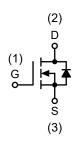
BVDSS	RDSON	I <sub>D</sub>
30V	16mΩ	36A

## **Applications**

- High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch

## **TO-252-2L Pin Configuration**





## **Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	30	V
$V_{GS}$	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>C</sub> =25℃	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	36	Α
I <sub>D</sub> @T <sub>C</sub> =100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	26	Α
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup>	58	Α
EAS	Single Pulse Avalanche Energy <sup>3</sup>	70	mJ
I <sub>AS</sub>	Avalanche Current	20	Α
P <sub>D</sub> @T <sub>C</sub> =25℃	Total Power Dissipation⁴	22	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	$^{\circ}$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^{\circ}$

## **Thermal Data**

Symbol	Parameter		Max.	Unit
R <sub>0JA</sub>	Thermal Resistance Junction-Ambient (<10s) <sup>1</sup>		25	°C/W
$R_{ heta JA}$	Thermal Resistance Junction-ambient (Steady State) <sup>1</sup>		62	°C/W
$R_{ heta JC}$	Thermal Resistance Junction-Case <sup>1</sup>		5	°C/W

**N-Ch MOSFET** 

## Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS}$ =0V , $I_D$ =250uA	30			V
$\triangle BV_{DSS}/\triangle T_{J}$	BVDSS Temperature Coefficient	Reference to 25°C , I <sub>D</sub> =1mA		0.023		V/°C
В	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V , I <sub>D</sub> =10A		16	26	mΩ
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =4.5V , I <sub>D</sub> =5A		25	38	
$V_{GS(th)}$	Gate Threshold Voltage	-V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	1.0	1.5	2.5	V
$\triangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient	VGS-VDS , ID -250UA		-5.2		mV/℃
	Drain Source Lookage Current	$V_{DS=}24V$ , $V_{GS}=0V$ , $T_{J}=25^{\circ}\mathrm{C}$			1	
I <sub>DSS</sub>	Drain-Source Leakage Current	$V_{DS=}24V$ , $V_{GS}=0V$ , $T_{J}=55^{\circ}\mathrm{C}$			5	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}$ = $\pm 20 V$ , $V_{DS}$ = $0 V$			±100	nA
gfs	Forward Transconductance	V <sub>DS</sub> =15V , I <sub>D</sub> =10A		10		S
Rg	Gate Resistance	V <sub>DS</sub> =24V , V <sub>GS</sub> =0V , f=1MHz		2.5		Ω
$Q_g$	Total Gate Charge (4.5V)			7.0		
$Q_gs$	Gate-Source Charge	$V_{DS=}20V$ , $V_{GS}=4.5V$ , $I_{D}=10A$		1.3		nC
Q <sub>gd</sub>	Gate-Drain Charge			2.4		
T <sub>d(on)</sub>	Turn-On Delay Time			4.0		
Tr	Rise Time	$V_{DD}$ =12V , $V_{GS}$ =10V , $R_{G}$ =3.3 $\Omega$ ,		9.2		
T <sub>d(off)</sub>	Turn-Off Delay Time	I <sub>D</sub> =5A		5.8		ns
T <sub>f</sub>	Fall Time			21		
Ciss	Input Capacitance			530		
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> =25V , V <sub>GS</sub> =0V , f=1MHz		65		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			50		

## **Guaranteed Avalanche Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
EAS	Single Pulse Avalanche Energy <sup>5</sup>	V <sub>DD</sub> =25V , L=0.1mH , I <sub>AS</sub> =10A	16			mJ

#### **Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
I <sub>S</sub>	Continuous Source Current <sup>1,6</sup>	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			9.5	Α
I <sub>SM</sub>	Pulsed Source Current <sup>2,6</sup>	VG-VD-UV, FOICE CUITEIIL			55	Α
V <sub>SD</sub>	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =15A , T <sub>j</sub> =25℃			1.2	V

#### Note:

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper,t<10sec.
- 2. The data tested by pulsed , pulse width  $\leq$  300us , duty cycle  $\leq$  2%
- 3.The EAS data shows Max. rating . The test condition is  $V_{\text{DD}}$ =25V, $V_{\text{GS}}$ =10V,L=0.1mH, $I_{\text{AS}}$ =10A
- 4.The power dissipation is limited by 150  $^{\circ}$ C junction temperature
- 5. The Min. value is 100% EAS tested guarantee.
- 6. The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications, should be limited by total power dissipation.





## **Typical Characteristics**

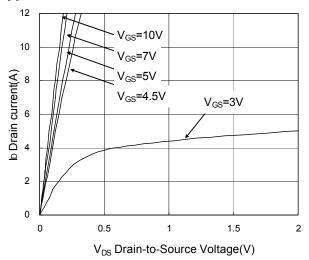


Fig.1 Typical Output Characteristics

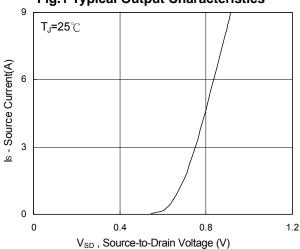
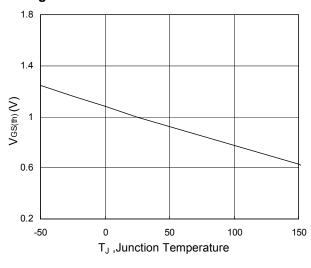


Fig.3 Forward characteristics of reverse



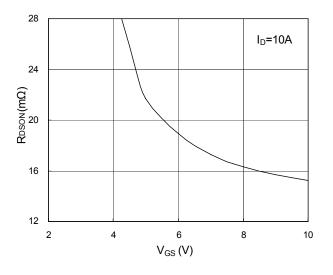


Fig.2 On-Resistance vs. G-S Voltage

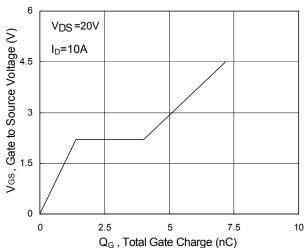


Fig.4 Gate-charge characteristics

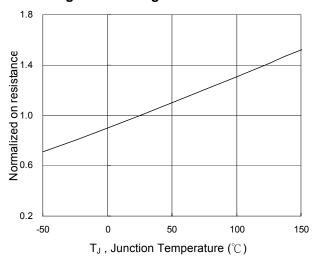


Fig.6 Normalized  $R_{\text{DSON}}$  vs.  $T_{\text{J}}$ 



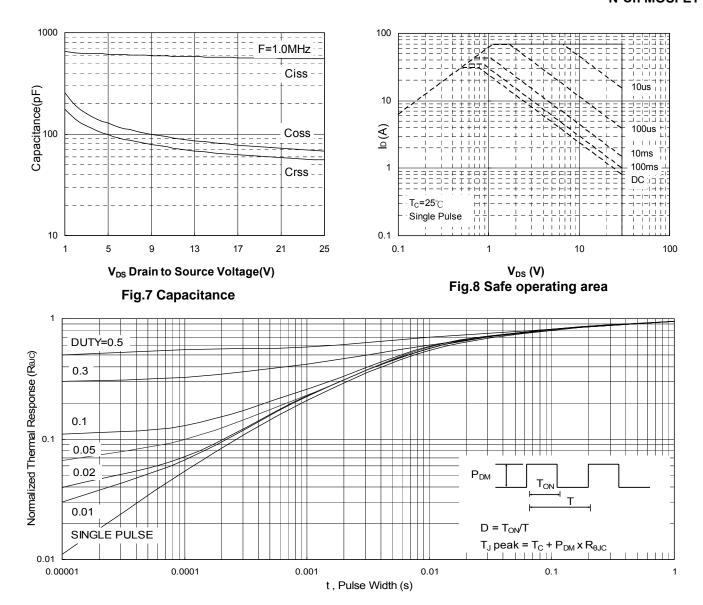


Fig.9 Normalized Maximum Transient Thermal Impedance

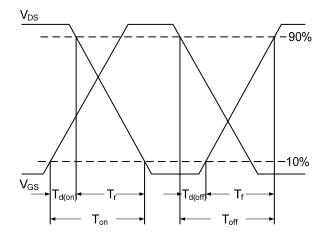


Fig.10 Switching time waveform

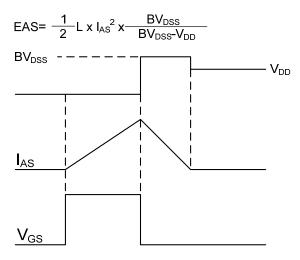
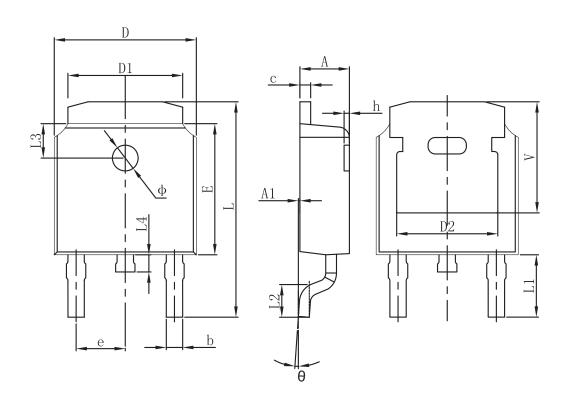


Fig.11 Unclamped inductive switching wave.

N-Ch MOSFET

# **Packaging information**



Symbol	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.635	0.770	0.025	0.030	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	4.830 REF.		0.190	REF.	
E	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.712	10.312	0.382	0.406	
L1	2.900 REF.		0.114	REF.	
L2	1.400	1.700	0.055	0.067	
L3	1.600 REF.		0.063	REF.	
L4	0.600	1.000	0.024	0.039	
Ф	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.250	REF.	0.207 REF.		



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