

General Description

The WSF20N20G is N-channel Enhanced VDMOSFETs, is obtained by the self-aligned planar Technology which reduce the conduction loss, improve switching .

performance and enhance the avalanche energy. The transistor can be used in various power switching circuit for system miniaturization and higher efficiency..

Features

- Super Low Gate Charge
- Excellent Cdv/dt effect decline
- Green Device Available

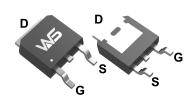
Product Summery

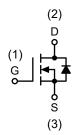
BVDSS	RDSON	ID
200V	120mΩ	18A

Applications

- Uninterruptible Power Supply(UPS)
- Power Factor Correction (PFC)

TO-252-2L Pin Configuration





Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	200	V
V_{GS}	Gate-Source Voltage	±20	V
I _D @T _C =25℃	Continuous Drain Current, V _{GS} @ 10V ¹	18	Α
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ 10V ¹	10	А
I _{DM}	Pulsed Drain Current ²	72	А
EAS	Single Pulse Avalanche Energy ³	340	mJ
I _{AS}	I _{AS} Avalanche Current		А
P _D @T _C =25℃	Total Power Dissipation ³	104	W
T _{STG}	Storage Temperature Range -55		$^{\circ}$
T _J	Operating Junction Temperature Range -55 to 150		$^{\circ}$

Thermal Data

Symbol	Parameter		Max.	Unit
R _{0JA}	Thermal Resistance Junction-ambient ¹		60	°C/W
$R_{ heta JC}$	Thermal Resistance Junction-Case ¹		1.2	°C/W



Electrical Characteristics (T $_{\rm J}$ =25 $^{\circ}$ C, unless otherwise noted)

Symbol	Parameter	Conditions		Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V_{GS} =0V , I_D =250uA	200			V
$\triangle BV_{DSS}/\triangle T_{J}$	BVDSS Temperature Coefficient	Reference to 25℃ , I _D =1mA		0.25		V/°C
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V , I _D =4.5A		120	160	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	\/ -\/ -2500A	2.0	3.5	4.0	٧
$\triangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	$V_{GS}=V_{DS}$, $I_D=250uA$		-4.63		mV/℃
	Drain Source Lookage Current	V_{DS} =200V , V_{GS} =0V , T_J =25 $^{\circ}$ C			1	- uA
I _{DSS}	Drain-Source Leakage Current	V _{DS} =160V , V _{GS} =0V , T _J =125℃			10	
I _{GSS}	Gate-Source Leakage Current	V_{GS} = $\pm30V$, V_{DS} = $0V$			±100	nA
R_g	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz		0.12		Ω
Qg	Total Gate Charge (10V)			40		
Q _{gs}	Gate-Source Charge	V _{DS} =160V , V _{GS} =10V , I _D =18A		5.2		nC
Q _{gd}	Gate-Drain Charge			18		
T _{d(on)}	Turn-On Delay Time			24		
Tr	Rise Time	V_{DD} =100V , V_{GS} =10V ,		45		
T _{d(off)}	Turn-Off Delay Time	$R_G=25\Omega,I_D=18A$		101		ns
T _f	Fall Time			95		
C _{iss}	Input Capacitance			1317		
Coss	Output Capacitance	V _{DS} =25V , V _{GS} =0V , f=1MHz		181		pF
C _{rss}	Reverse Transfer Capacitance			76		

Guaranteed Avalanche Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
EAS	Single Pulse Avalanche Energy ⁵	V _{DD} =25V , L=0.1mH , I _{AS} =15A	250			mJ

Diode Characteristics

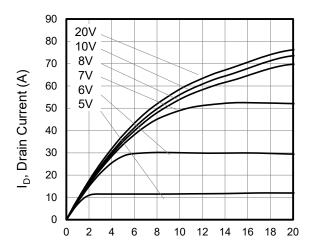
Symbol	Parameter Conditions		Min.	Тур.	Max.	Unit
Is	Continuous Source Current ^{1,6}	V =V =0V Force Current			18	Α
I _{SM}	Pulsed Source Current ^{2,6}	V _G =V _D =0V , Force Current			72	Α
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V , I _S =18A , T _J =25℃			1.4	V
t _{rr}	Reverse Recovery Time			230		nS
Q _{rr}	Reverse Recovery Charge	lF=15A , dl/dt=100A/μs , T _J =25℃		1.8		uC

Note:

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper,t<10sec.
- 2.The data tested by pulsed , pulse width $\,\leq\,300\text{us}$, duty cycle $\,\leq\,2\%$
- 3.The EAS data shows Max. rating . The test condition is V_{DD} =25V, V_{GS} =10V,L=0.1mH, I_{AS} =15A
- 5.The Min. value is 100% EAS tested guarantee.
- 6.The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.

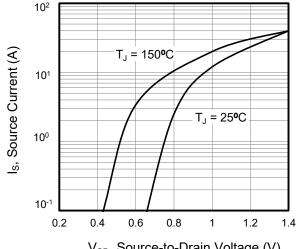


Typical Characteristics



V_{DS}, Drain-to-Source Voltage (V)

Figure 1. Output Characteristics (T_J = 25°C)



 V_{SD} , Source-to-Drain Voltage (V) Figure 2. Body Diode Forward Voltage

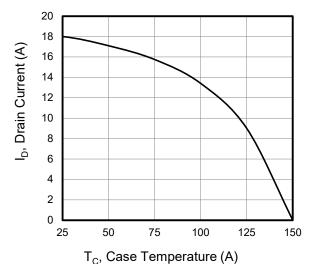


Figure 3. Drain Current vs. Temperature

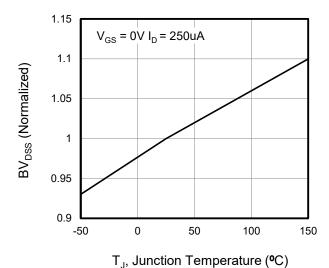


Figure 4. BV_{DSS} Variation vs. Temperature

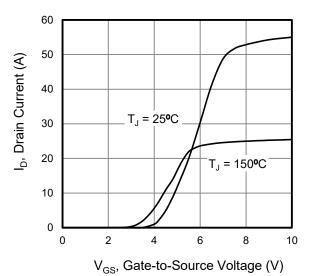


Figure 5. Transfer Characteristics

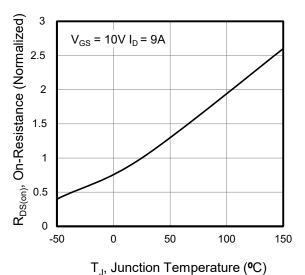
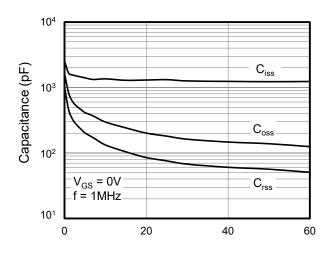


Figure 6. On-Resistance vs. Temperature





 $V_{\rm DS}$, Drain-to-Source Voltage (V)

Figure 7. Capacitance

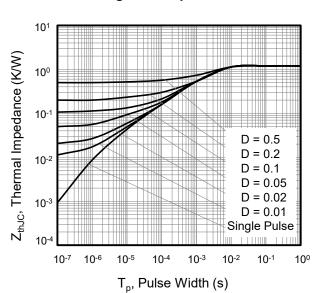
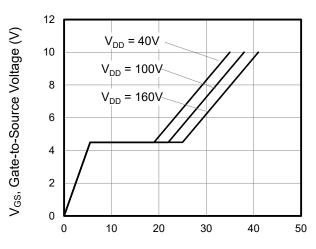


Figure 10. Transient Thermal Impedance

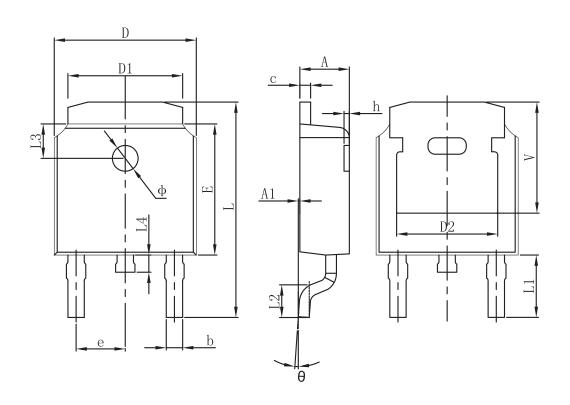


 Q_g , Total Gate Charge (nC)

Figure 8. Gate Charge

N-Ch MOSFET

Packaging information



Symbol	Dimensions	In Millimeters	Dimension	s In Inches
Syllibol	Min.	Max.	Min.	Max.
Α	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.635	0.770	0.025	0.030
С	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	4.830	REF.	0.190	REF.
Е	6.000	6.200	0.236	0.244
е	2.186	2.386	0.086	0.094
L	9.712	10.312	0.382	0.406
L1	2.900 REF.		0.114	REF.
L2	1.400	1.700	0.055	0.067
L3	1.600 REF.		0.063	REF.
L4	0.600	1.000	0.024	0.039
Ф	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.250	REF.	0.207	REF.



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