

General Description

The WSF09N20G is the highest performance trench N-Ch MOSFET with extreme high cell density,which provide excellent RDSON and gate charge for most of the synchronous buck converter applications .

The WSF09N20G meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent Cdv/dt effect decline
- Green Device Available

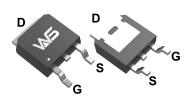
Product Summery

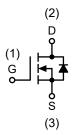
BVDSS	RDSON	ID
200V	210mΩ	9A

Applications

- High Frequency Point-of-Load Synchronous Buck Converter
- Networking DC-DC Power System
- Load Switch

TO-252-2L Pin Configuration





Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	200	V
V_{GS}	Gate-Source Voltage	±20	V
I _D @T _C =25℃	Continuous Drain Current, V _{GS} @ 10V ¹	9	Α
I _D @T _C =100℃	Continuous Drain Current, V _{GS} @ 10V ¹	3.13	Α
I _D @T _A =25℃	Continuous Drain Current, V _{GS} @ 10V ¹	9	Α
I _D @T _A =70°C	Continuous Drain Current, V _{GS} @ 10V ¹	5.8	А
I _{DM}	Pulsed Drain Current ²	36	А
EAS	Single Pulse Avalanche Energy ³	320	mJ
I _{AS}	Avalanche Current	9	Α
P _D @T _C =25℃	Total Power Dissipation ³	83	W
P _D @T _c =100℃	Total Power Dissipation ³	47	W
T _{STG}	Storage Temperature Range	-55 to 150	℃
TJ	T _J Operating Junction Temperature Range		$^{\circ}$

Thermal Data

Symbol	Parameter		Max.	Unit
R _{0JA}	Thermal Resistance Junction-ambient ¹		30	°C/W
$R_{ heta JC}$	Thermal Resistance Junction-Case ¹		1.6	°C/W



Electrical Characteristics (T $_{\rm J}$ =25 $^{\circ}$ C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	200			V
$\triangle BV_{DSS}/\triangle T_{J}$	BVDSS Temperature Coefficient	Reference to 25℃ , I _D =1mA		0.25		V/℃
В	Static Drain-Source On-Resistance ²	V _{GS} =10V , I _D =4.5A		210	250	mΩ
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =6.0V , I _D =3.6A		260	290	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V -V 1 -250A	2	3	4	V
$\triangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	$V_{GS}=V_{DS}$, $I_D=250uA$		-4.63		mV/℃
	Dunin Course Lookers Current	V _{DS} =200V , V _{GS} =0V , T _J =25℃			1	
I _{DSS}	Drain-Source Leakage Current	V _{DS} =160V , V _{GS} =0V , T _J =125℃			10	uA
I _{GSS}	Gate-Source Leakage Current	V_{GS} = $\pm 30V$, V_{DS} = $0V$			±100	nA
gfs	Forward Transconductance	V _{DS} =30V , I _D =4.5A		0.21		S
Rg	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz		2	4	Ω
Q_g	Total Gate Charge (10V)			11.8		
Q_gs	Gate-Source Charge	V _{DS} =160V , V _{GS} =10V , I _D =9A		2.36		nC
Q_gd	Gate-Drain Charge			3.98		
T _{d(on)}	Turn-On Delay Time			10.33		
T _r	Rise Time	V _{DD} =100V , V _{GS} =10V ,		10.7		
$T_{d(off)}$	Turn-Off Delay Time	R_G =10 Ω I_D =9A R_L =10 Ω		29.1		ns
T _f	Fall Time			11.1		
C _{iss}	Input Capacitance			780		
C _{oss}	Output Capacitance	V _{DS} =25V , V _{GS} =0V , f=1MHz		51.2		pF
C _{rss}	Reverse Transfer Capacitance			3.2		

Guaranteed Avalanche Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
EAS	Single Pulse Avalanche Energy ⁵	V _{DD} =25V , L=0.1mH , I _{AS} =5A		320		mJ

Diode Characteristics

Symbol	Parameter Conditions		Min.	Тур.	Max.	Unit
Is	Continuous Source Current ^{1,6}	V =V =0V Force Current			9	Α
I _{SM}	Pulsed Source Current ^{2,6}	V _G =V _D =0V , Force Current			36	Α
V_{SD}	Diode Forward Voltage ²	V _{GS} =0V , I _S =5A , T _J =25°C			1.4	V
t _{rr}	Reverse Recovery Time			201		nS
Q _{rr}	Reverse Recovery Charge	lF=5A,dI/dt=100A/μs,T _J =25℃		663		nC

Note:

- 1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper, t<10 sec.
- 2.The data tested by pulsed , pulse width $\,\leq\,300\text{us}$, duty cycle $\,\leq\,2\%$
- 3.The EAS data shows Max. rating . The test condition is $V_{\text{DD}}\text{=}25\text{V}, V_{\text{GS}}\text{=}10\text{V}, L\text{=}0.1\text{mH}, I_{\text{AS}}\text{=}5\text{A}$
- 5.The Min. value is 100% EAS tested guarantee.
- 6.The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.



Typical Characteristics

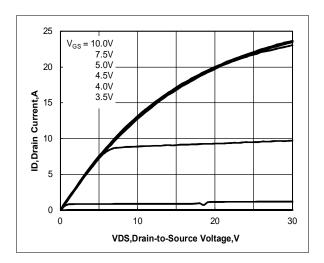


Figure 1. Output Characteristics

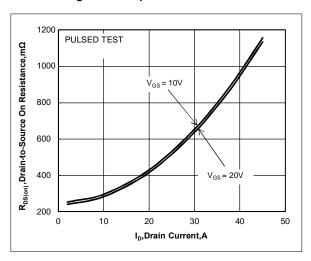


Figure 3. Drain-to-Source On Resistance vs.

Drain Current and Gate Voltage

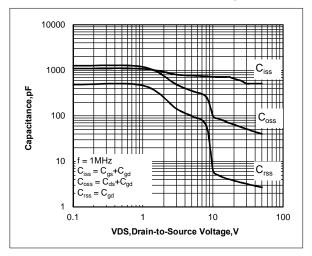


Figure 5. Capacitance Characteristics

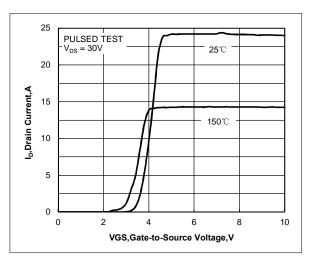


Figure 2. Transfer Characteristics

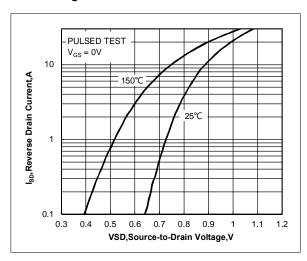


Figure 4. Body Diode Forward Voltage vs.
Source Current and Temperature

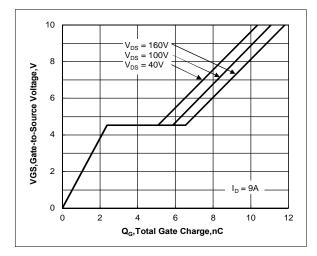


Figure 6. Gate Charge Characteristics



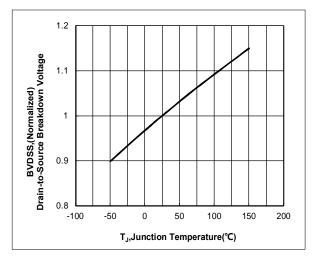


Figure 7. Normalized Breakdown Voltage vs.

Junction Temperature

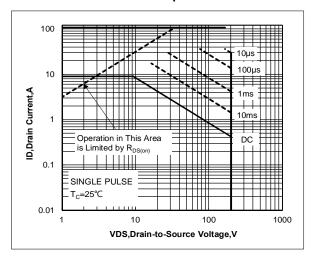


Figure 9. Maximum Safe Operating Area for RU9N20A

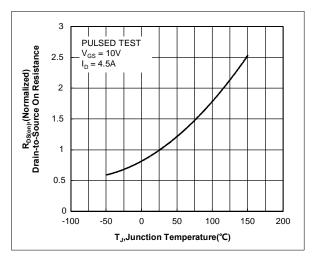


Figure 8. Normalized On Resistance vs.

Junction Temperature

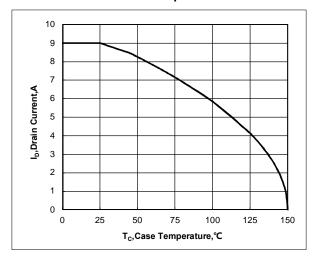


Figure 10. Maximum Continuous Drain Current vs.

Case Temperature

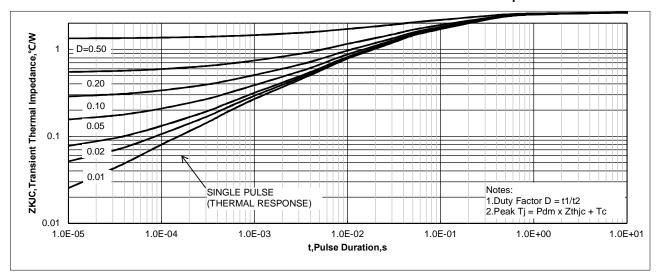
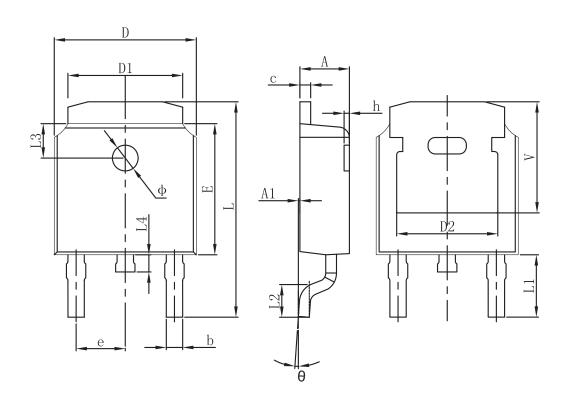


Figure 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case for RU9N20A

N-Ch MOSFET

Packaging information



Symbol	Dimensions	In Millimeters	Dimension	s In Inches	
Syllibol	Min.	Max.	Min.	Max.	
Α	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.635	0.770	0.025	0.030	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	4.830	REF.	0.190	REF.	
E	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.712	10.312	0.382	0.406	
L1	2.900	REF.	0.114	REF.	
L2	1.400	1.700	0.055	0.067	
L3	1.600 REF.		0.063	REF.	
L4	0.600	1.000	0.024	0.039	
Ф	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.250	REF.	0.207	7 REF.	



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