

General Description

The WSD3076DN33 is the highest performance trench N-Ch and P-Channel MOSFETs with extreme high cell density, which provide excellent $R_{DS(ON)}$ and gate charge for most of the synchronous buck converter applications.

The WSD3076DN33 meet the RoHS and Green Product requirement, 100% E_{AS} guaranteed with full function reliability approved.

Features

- 100% UIS + R_g Tested
- ESD Protection
- Reliable and Rugged
- Lead Free and Green Devices Available (RoHS Compliant)
- Moisture Sensitivity Level MSL1 (per JEDEC J-STD-020D)

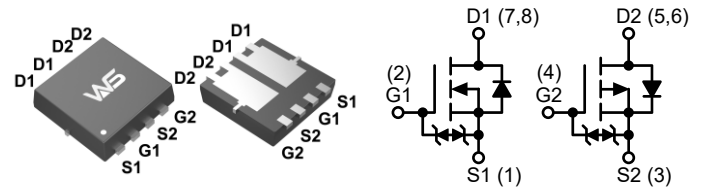
Product Summary

BV_{DSS}	$R_{DS(ON)}$	I_D
30V	11m Ω	30A
-30V	18.5m Ω	-26A

Applications

- Synchronous Rectification.
- Motor Control.
- High Current, High Speed Switching.
- Portable, equipment application.

DFN3X3-8L Pin Configuration



Absolute Maximum Ratings ($T_A=25^\circ\text{C}$, Unless Otherwise Noted)

Symbol	Parameter	Rating		Units	
		N-Channel	P-Channel		
V_{DS}	Drain-Source Voltage	30	-30	V	
V_{GS}	Gate-Source Voltage	± 20	± 25		
I_D	Continuous Drain Current	$T_A=25^\circ\text{C}$	30	-26	A
		$T_A=70^\circ\text{C}$	23	-19	
I_{DM}^1	Pulse Drain Current Tested	$T_A=25^\circ\text{C}$	90	-78	A
E_{AS}^3	Avalanche Energy, Single pulse	$L=0.1\text{mH}$	12.8	12.8	mJ
I_{AS}^3	Avalanche Current, Single pulse	$L=0.1\text{mH}$	16	-16	A
P_D	Maximum Power Dissipation	$T_A=25^\circ\text{C}$	1.4	1.4	W
		$T_A=70^\circ\text{C}$	0.9	0.9	
$R_{\theta JA}^2$	Thermal Resistance-Junction to Ambient	Steady State	90	90	$^\circ\text{C/W}$
T_{STG}	Storage Temperature Range	-55 to 150		$^\circ\text{C}$	
T_J	Maximum Junction Temperature	150			

N-Channel Electrical Characteristics ($T_A=25^\circ\text{C}$, Unless Otherwise Noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	30	---	---	V
$R_{DS(ON)}^4$	Drain-Source On-state Resistance	$V_{GS}=10V, I_D=7A$	---	11	15	m Ω
		$V_{GS}=4.5V, I_D=5A$	---	15	20	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	1.3	1.8	2.3	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=24V, V_{GS}=0V$ $T_J=85^\circ\text{C}$	---	---	1.0	μA
			---	---	30	
I_{GSS}	Gate Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 10	μA
R_G^5	Gate Resistance	$V_{DS}=0V, V_{GS}=0V, f=1.0\text{MHz}$	---	4.5	---	Ω
Q_g^5	Total Gate Charge	$V_{DS}=15V, V_{GS}=10V, I_{DS}=7A$	---	12.5	17.5	nC
Q_g^5	Total Gate Charge	$V_{DS}=15V, V_{GS}=4.5V, I_{DS}=7A$	---	6.0	---	
Q_{gs}^5	Gate-Source Charge		---	2.2	---	
Q_{gd}^5	Gate-Drain Charge		---	2.5	---	
$T_{d(on)}^5$	Turn-On Delay Time	$V_{DD}=15V, R_L=15\Omega, I_{DS}=1A,$ $V_{GEN}=10V, R_G=6\Omega$	---	9	17	ns
T_r^5	Turn-On Rise Time		---	10	18	
$T_{d(off)}^5$	Turn-Off Delay Time		---	25	45	
T_f^5	Turn-Off Fall Time		---	6.5	12	
C_{iss}^5	Input Capacitance	$V_{DS}=15V, V_{GS}=0V, f=1.0\text{MHz}$	---	725	943	pF
C_{oss}^5	Output Capacitance		---	105	---	
C_{rss}^5	Reverse Transfer Capacitance		---	70	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
I_S	Continuous Source Current	$T_A=25^\circ\text{C}$	---	---	1.2	A
V_{SD}^4	Diode Forward Voltage	$I_{SD}=1A, V_{GS}=0V$	---	0.75	1.1	V
t_{rr}	Reverse Recovery Time	$I_{SD}=7A, di_{SD}/dt=100A/\mu s$	---	11	---	ns
Q_{rr}	Reverse Recovery Charge		---	5.0	---	nC

Note:

1. Pulse width limited by max. junction temperature.
2. Surface mounted on 1in² pad area, steady state $t = 999s$.
3. UIS tested and pulse width limited by maximum junction temperature (initial temperature $T_J=25^\circ\text{C}$).
4. Pulse test ; pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
5. Guaranteed by design, not subject to production testing.

P-Channel Electrical Characteristics (T_A=25°C, Unless Otherwise Noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units	
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =-250μA	-30	---	---	V	
R _{DS(ON)} ⁴	Drain-Source On-state Resistance	V _{GS} =-10V, I _D =-5A	---	18.5	27	mΩ	
		V _{GS} =-4.5V, I _D =-3A	---	25	43		
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =-250μA	-1.3	-1.8	-2.3	V	
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =-24V, V _{GS} =0V	---	---	-1.0	μA	
		T _J =85°C	---	---	-30		
I _{GSS}	Gate Leakage Current	V _{GS} =±25V, V _{DS} =0V	---	---	±10	μA	
R _G ⁵	Gate Resistance	V _{DS} =0V, V _{GS} =0V, f=1.0MHz	---	11	---	Ω	
Q _g ⁵	Total Gate Charge	V _{DS} =-15V, V _{GS} =-4.5V, I _{DS} =-5A	---	7.4	---	nC	
Q _g ⁵	Total Gate Charge	V _{DS} =-15V, V _{GS} =-10V, I _{DS} =-5A	---	14.3	20		
Q _{gs} ⁵	Gate-Source Charge		---	2.2	---		
Q _{gd} ⁵	Gate-Drain Charge		---	3.5	---		
T _{d(on)} ⁵	Turn-On Delay Time	V _{DD} =-15V, R _L =15Ω, I _{DS} =-1A	---	9	16	ns	
T _r ⁵	Turn-On Rise Time		---	11	20		
T _{d(off)} ⁵	Turn-Off Delay Time		V _{GEN} =-10V, R _G =6Ω	---	58		104
T _f ⁵	Turn-Off Fall Time		---	39	70		
C _{iss} ⁵	Input Capacitance	V _{DS} =-15V, V _{GS} =0V, f=1.0MHz	---	725	943	pF	
C _{oss} ⁵	Output Capacitance		---	127	---		
C _{rss} ⁵	Reverse Transfer Capacitance		---	105	---		

Diode Characteristics

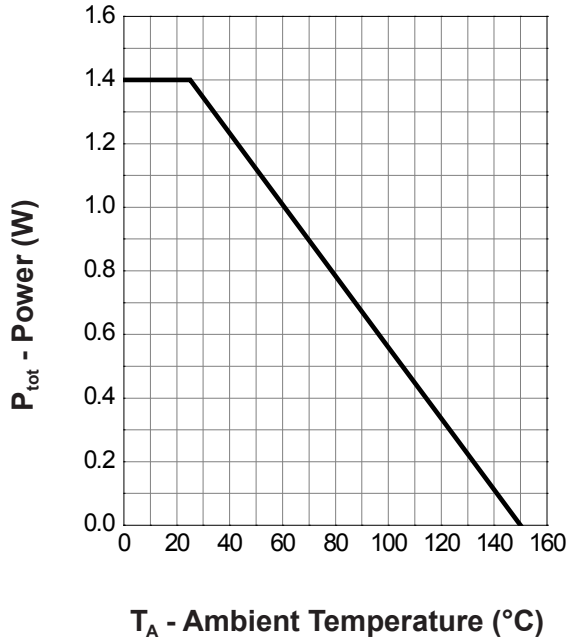
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
I _S	Continuous Source Current	T _A =25°C	---	---	-1.2	A
V _{SD} ⁴	Diode Forward Voltage	I _{SD} =-1A, V _{GS} =0V	---	-0.8	-1.1	V
t _{rr}	Reverse Recovery Time	I _{SD} =-5A, di _{SD} /dt=100A/μs	---	13	---	ns
Q _{rr}	Reverse Recovery Charge		---	6.0	---	nC

Note:

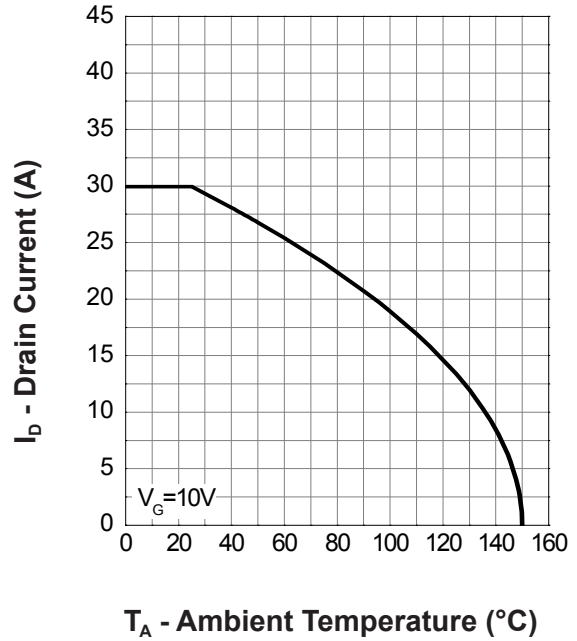
1. Pulse width limited by max. junction temperature.
2. Surface mounted on 1in² pad area, steady state t = 999s.
3. UIS tested and pulse width limited by maximum junction temperature (initial temperature T_J=25°C).
4. Pulse test ; pulse width≤300μs, duty cycle≤2%.
5. Guaranteed by design, not subject to production testing.

N-Channel Typical Characteristics

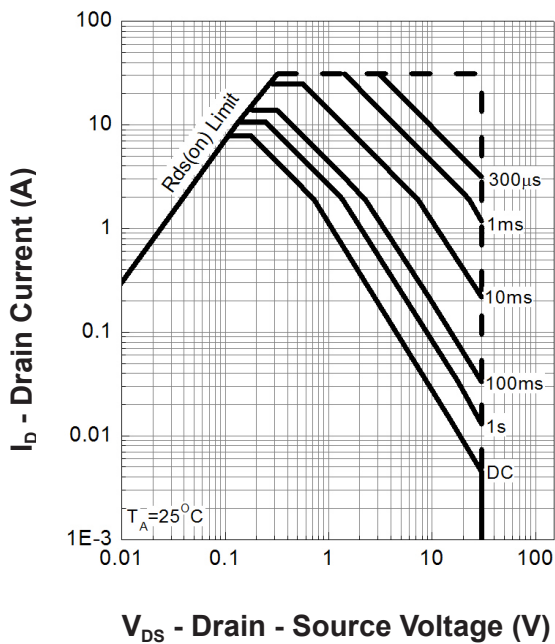
Power Dissipation



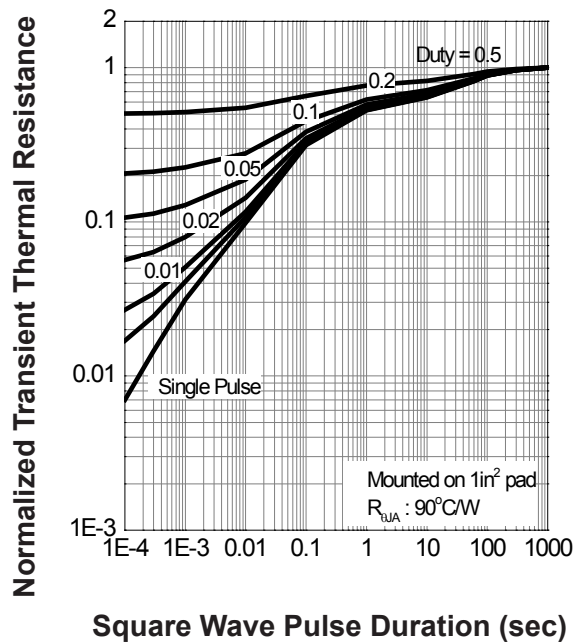
Drain Current



Safe Operation Area

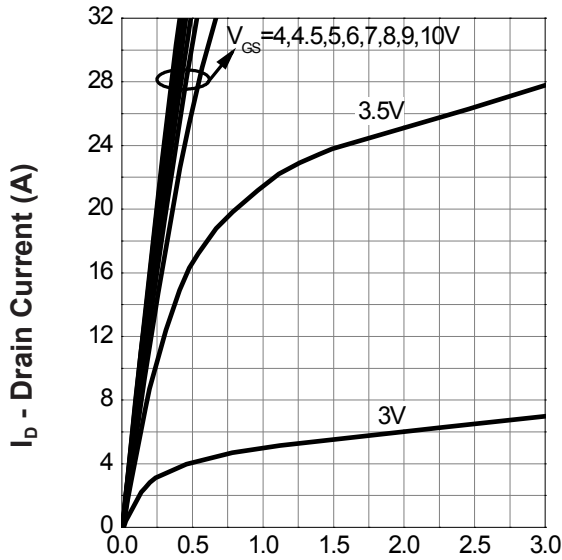


Thermal Transient Impedance



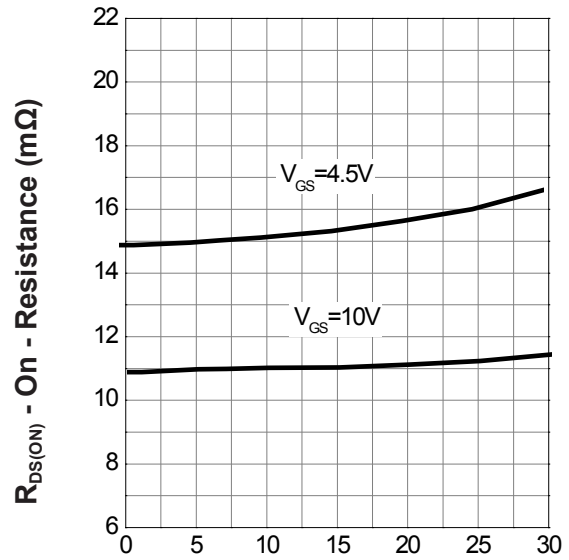
N-Channel Typical Characteristics (Cont.)

Output Characteristics



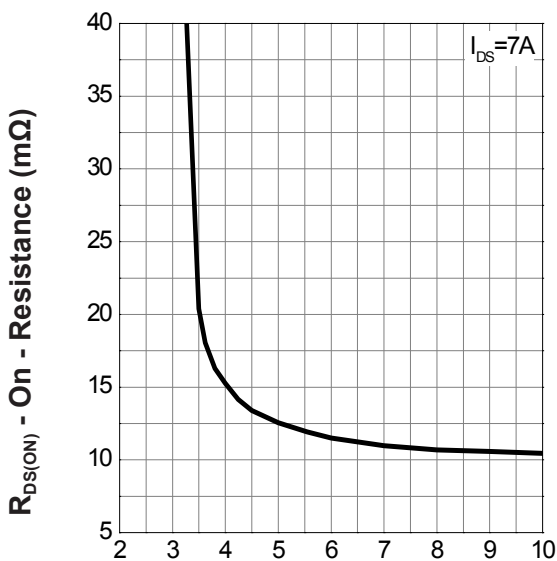
V_{DS} - Drain - Source Voltage (V)

Drain-Source On Resistance



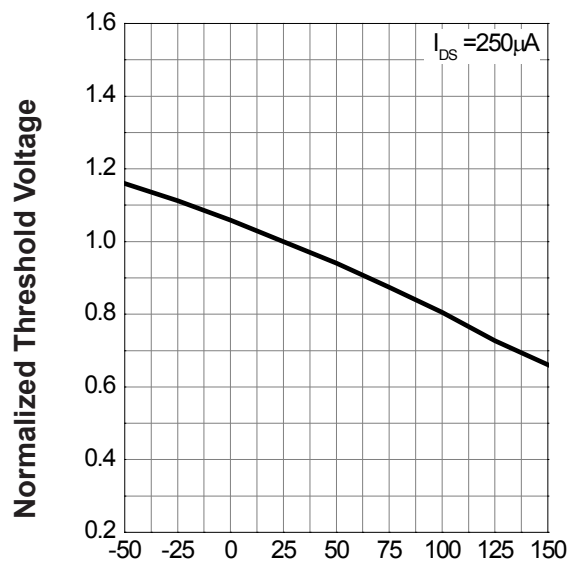
I_D - Drain Current (A)

Gate-Source On Resistance



V_{GS} - Gate - Source Voltage (V)

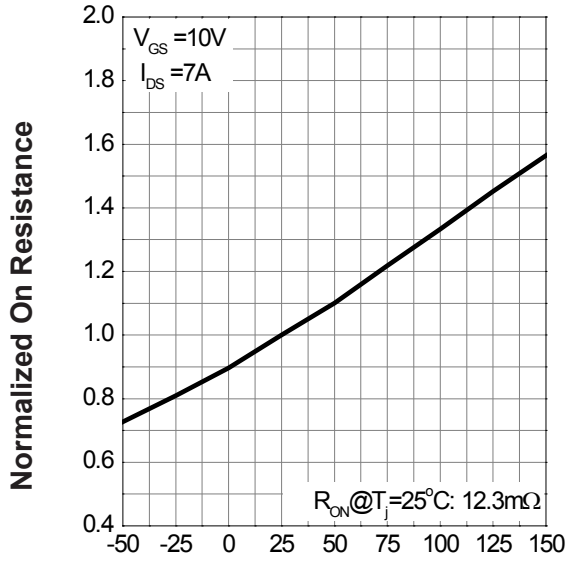
Gate Threshold Voltage



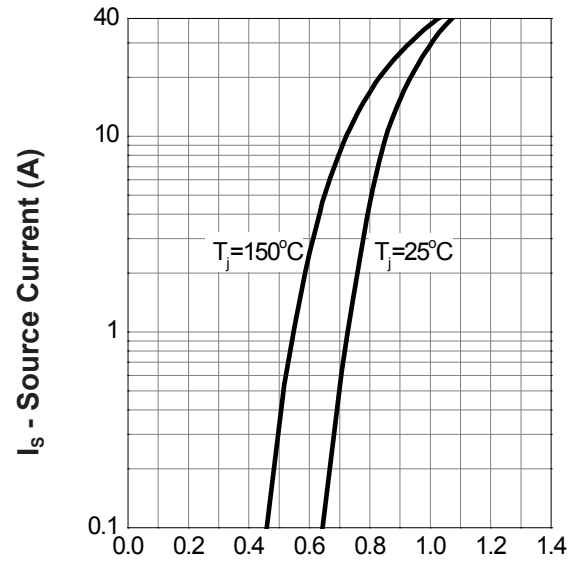
T_j - Junction Temperature (°C)

N-Channel Typical Characteristics (Cont.)

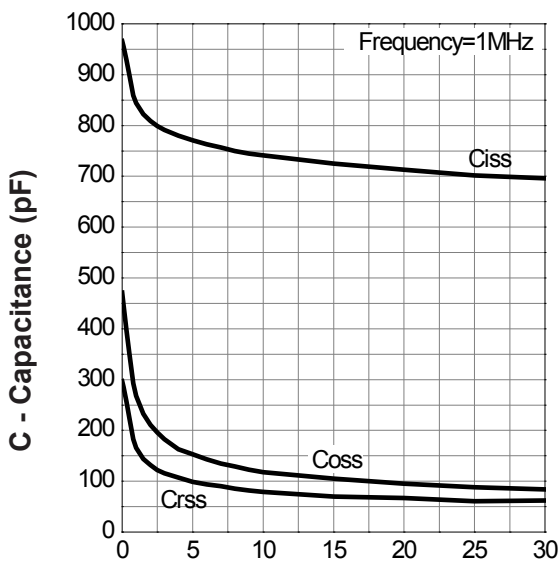
Drain-Source On Resistance



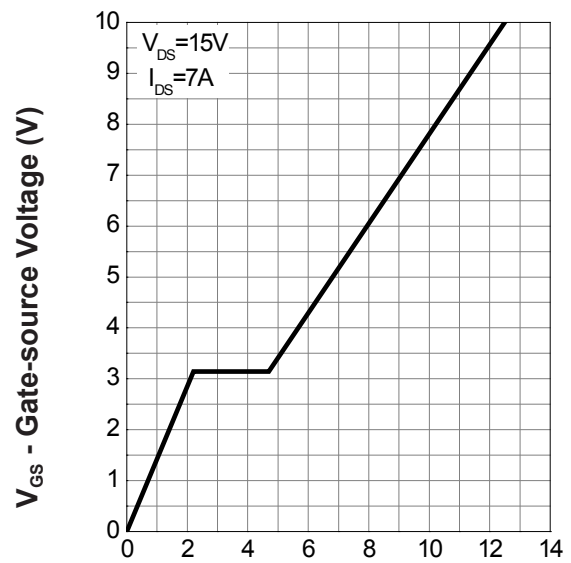
Source-Drain Diode Forward



Capacitance

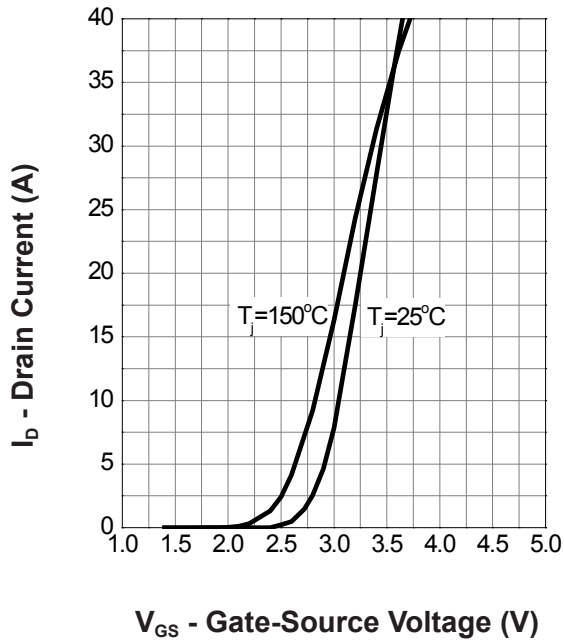


Gate Charge



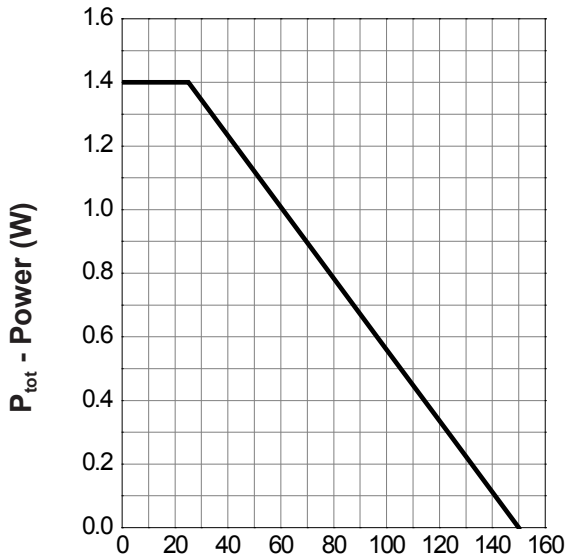
N-Channel Typical Characteristics (Cont.)

Transfer Characteristics



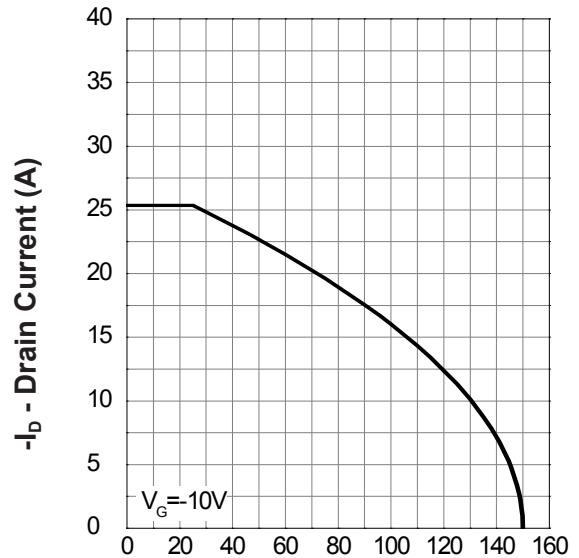
P-Channel Typical Characteristics

Power Dissipation



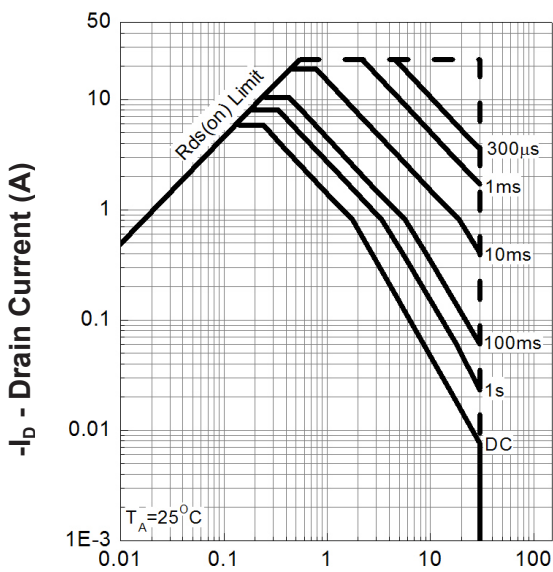
T_A - Ambient Temperature (°C)

Drain Current



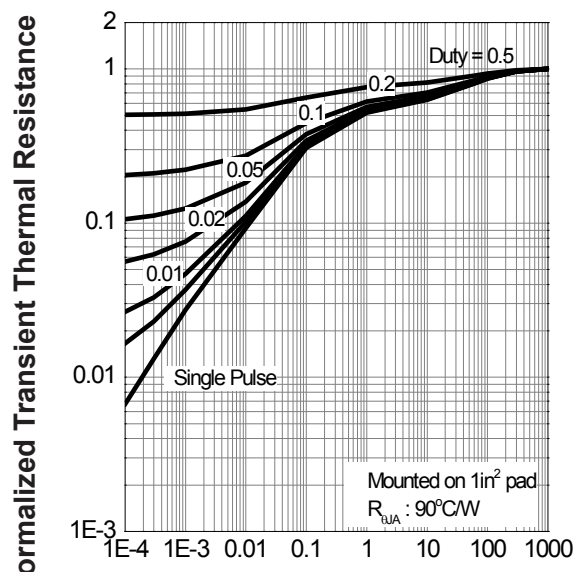
T_A - Ambient Temperature (°C)

Safe Operation Area



$-V_{DS}$ - Drain - Source Voltage (V)

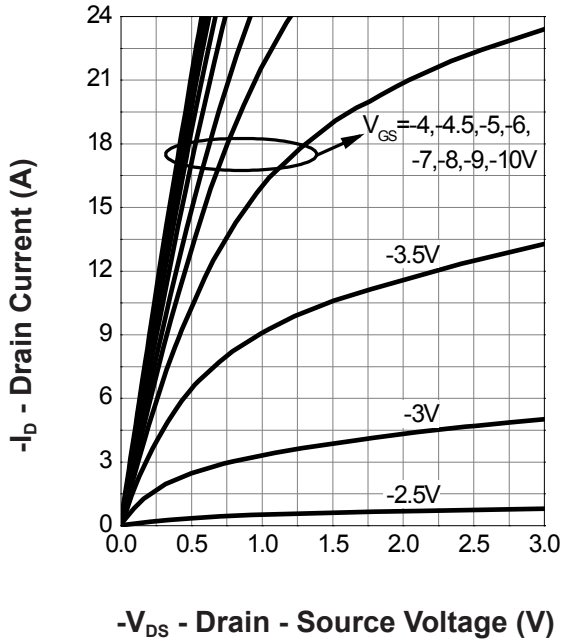
Thermal Transient Impedance



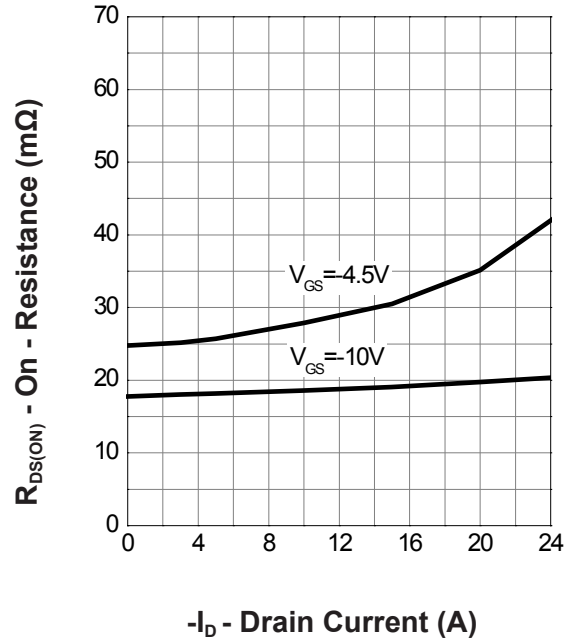
Square Wave Pulse Duration (sec)

P-Channel Typical Characteristics (Cont.)

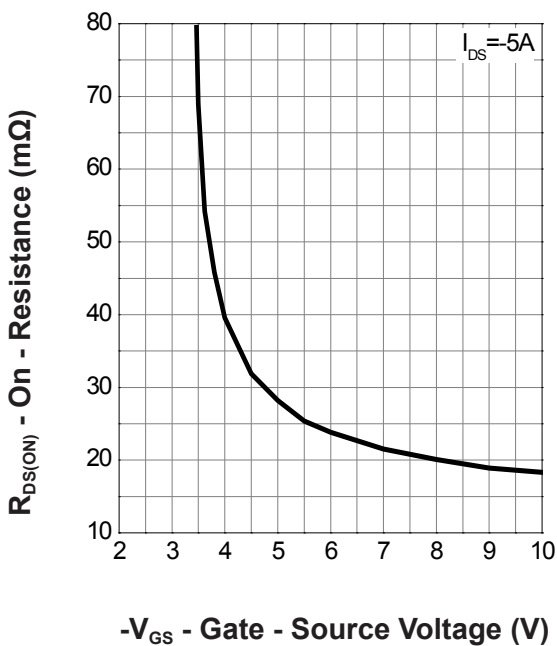
Output Characteristics



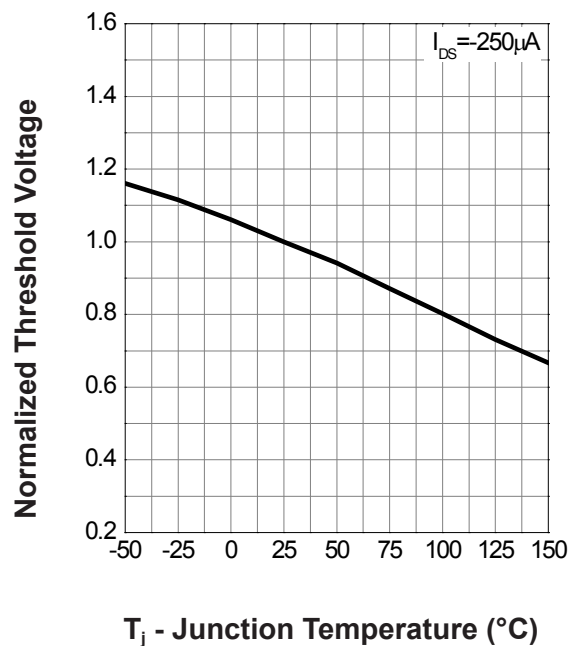
Drain-Source On Resistance



Gate-Source On Resistance

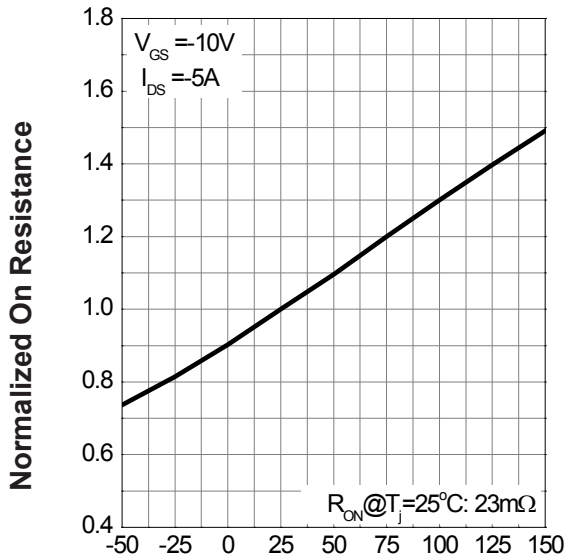


Gate Threshold Voltage



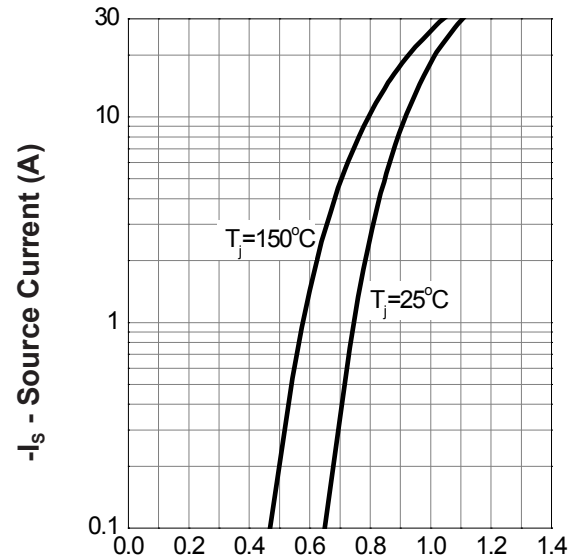
P-Channel Typical Characteristics (Cont.)

Drain-Source On Resistance



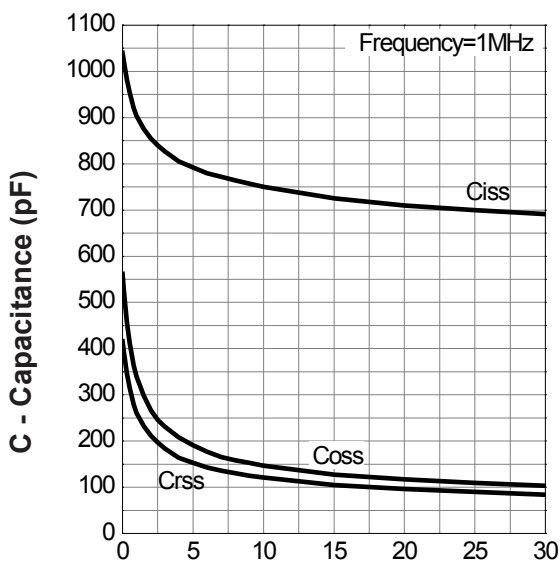
T_j - Junction Temperature ($^{\circ}\text{C}$)

Source-Drain Diode Forward



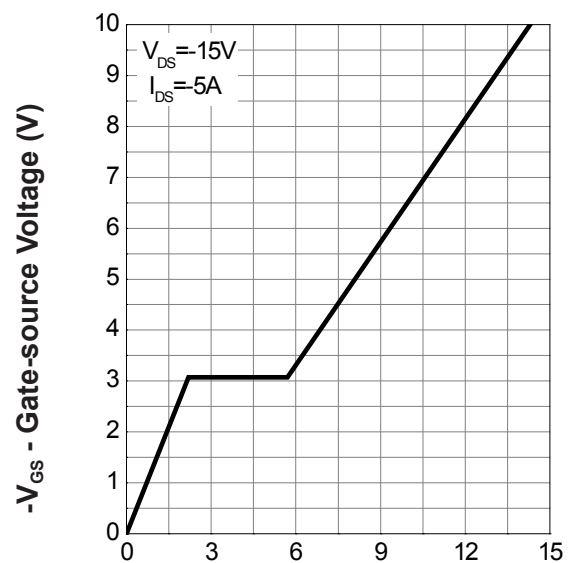
$-V_{SD}$ - Source - Drain Voltage (V)

Capacitance



$-V_{DS}$ - Drain-Source Voltage (V)

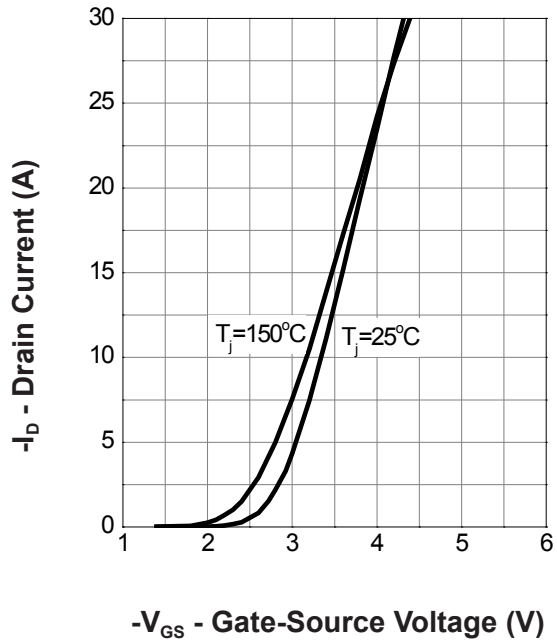
Gate Charge

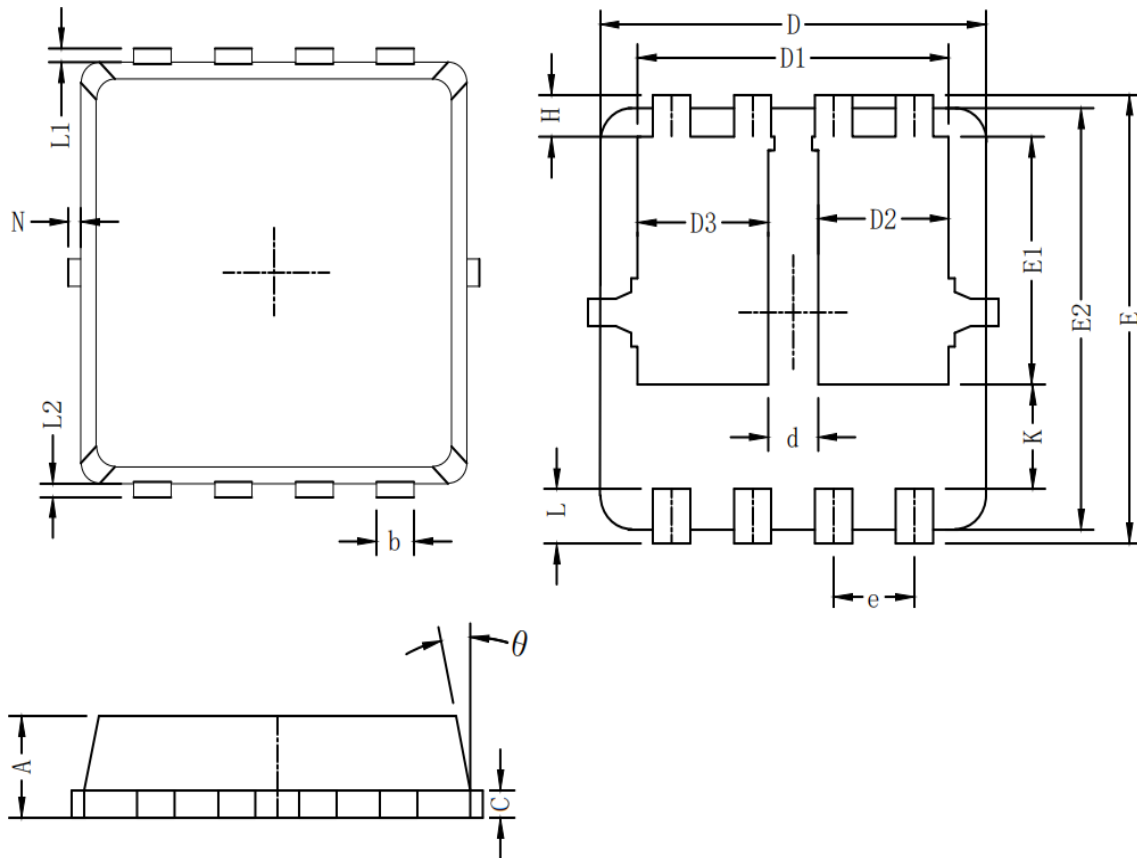


Q_G - Gate Charge (nC)

P-Channel Typical Characteristics (Cont.)

Transfer Characteristics



Packaging information


Symbol	Dim in mm		
	min	typ	max
A	0.6	0.75	0.9
b	0.2	0.3	0.4
C	0.15	0.2	0.25
D	3	3.1	3.2
D1	2.3	2.45	2.6
D2/D3	0.8	1	1.2
E	3.15	3.3	3.45
E1	1.43	1.73	1.93
E2	2.9	3.05	3.2
e	0.65BSC		
H	0.2	0.35	0.5
K	0.57	0.77	0.87
L	0.3	0.4	0.5
L1/L2	0.1REF		
θ	8°	10°	13°
N	0		0.15
d	0.3	0.4	0.5

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