

General Description

The WST2002 is the highest performance trench N-Ch MOSFET with extreme high cell density , which provide excellent R_{DSON} and gate charge for most of the small power switching and load switch applications.

The WST2002 meet the RoHS and Green Product requirement with full function reliability approved.

Features

- High-speed switching
- Green Device Available
- ESD Protected:2KV

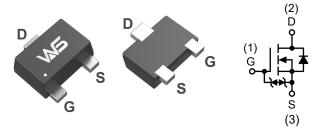
Product Summery

BV _{DSS}	R _{DSON}	I _D
30V	7000mΩ	0.6A

Applications

- High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC
- Networking DC-DC Power System
- Load Switch

SOT-723-3L Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	30	V
V_{GS}	Gate-Source Voltage	±20	V
I _D @T _A =25℃	Continuous Drain Current, V _{GS} @ 10V	0.6	Α
I _D @T _A =70°C	Continuous Drain Current, V _{GS} @ 10V	0.55	Α
I _{DM}	Pulsed Drain Current	0.8	А
P _D @T _A =25°C	Total Power Dissipation	0.15	W
T _{STG}	Storage Temperature Range	-55 to 150	$^{\circ}$
T _J	Operating Junction Temperature Range	-55 to 150	$^{\circ}$

Thermal Data

Symbol	Parameter	Тур.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient		625	°C/W



Electrical Characteristics (T_J=25 ℃, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit	
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =10uA	30			V	
$\triangle BV_{DSS}/\triangle T_{J}$	BV _{DSS} Temperature Coefficient	Reference to 25℃, I _D =1mA		0.05		V/℃	
R _{DS(ON)}	Static Drain Source On Decistance	V _{GS} =10V , I _D =0.5A		7000	7500	mΩ	
	Static Drain-Source On-Resistance	V _{GS} =4.5V , I _D =0.05A		8000	9000		
V _{GS(th)}	Gate Threshold Voltage)/ -\/ -250\	0.8	1.6	2.5	V	
$\triangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	$-V_{GS}=V_{DS}$, $I_D=250uA$		-3.7		mV/℃	
,	Drain-Source Leakage Current	V_{DS} =30V , V_{GS} =0V , T_{J} =25 $^{\circ}$ C			1	- uA	
I _{DSS}		V_{DS} =30V , V_{GS} =0V , T_J =55 $^{\circ}\mathrm{C}$			3		
I _{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V$, V_{DS} =0V			±10	uA	
gfs	Forward Transconductance	V _{DS} =5V , I _D =0.1A		80		mS	
T _{d(on)}	Turn-On Delay Time	V _{DD} =30V , V _{GS} =10V ,		12	20	ns	
T _{d(off)}	Turn-Off Delay Time	R_G =10Ω, I_D =0.2A RL=150Ω		20	30	115	
C _{iss}	Input Capacitance			10	25		
C _{oss}	Output Capacitance	V _{DS} =25V , V _{GS} =0V , f=1MHz		25	50	pF	
C _{rss}	Reverse Transfer Capacitance			3.0	5.0		

^{*} Pw≤300μs, Duty cycle≤1%

Typical Characteristics

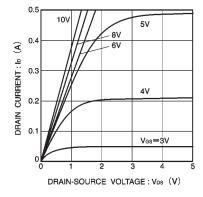


Fig.1 Typical output characteristics

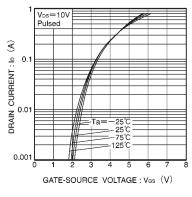


Fig.2 Typical transfer characteristics

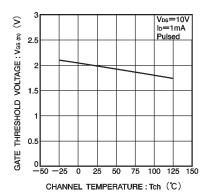


Fig.3 Gate threshold voltage vs. channel temperature



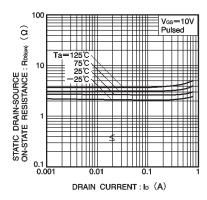


Fig.4 Static drain-source on-state resistance vs. drain current (I)

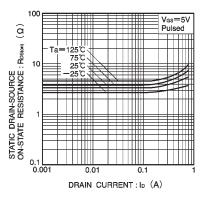


Fig.5 Static drain-source on-state resistance vs. drain current (II)

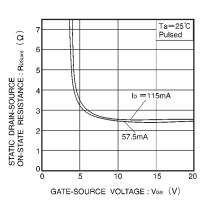


Fig.6 Static drain-source on-state resistance vs. gate-source voltage

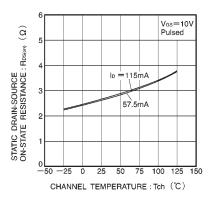


Fig.7 Static drain-source on-state resistance vs. channel temperature

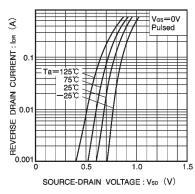


Fig.8 Reverse drain current vs. source-drain voltage (I)

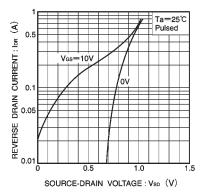


Fig.9 Reverse drain current vs. source-drain voltage (I)

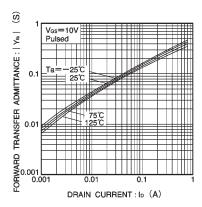


Fig.10 Forward transfer admittance vs. drain current

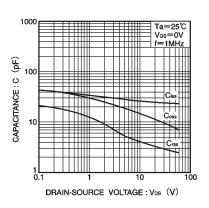


Fig.11 Typical capacitance vs. drain-source voltage

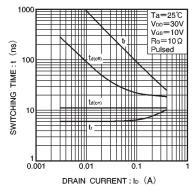
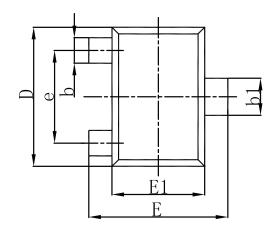
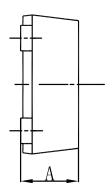


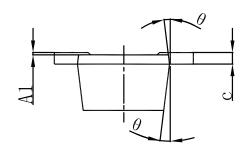
Fig.12 Switching characteristics
(See Figures 13 and 14 for the measurement circuit and resultant waveforms)

N-Ch MOSFET

Packaging information







Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min.	Max.	Min.	Max.	
А	0.430	0.500	0.017	0.020	
A1	0.000	0.050	0.000	0.002	
b	0.170	0.270	0.007	0.011	
b1	0.270	0.370	0.011	0.015	
С	0.080	0.150	0.003	0.006	
D	1.150	1.250	0.045	0.049	
E	1.150	1.250	0.045	0.049	
E1	0.750	0.850	0.030	0.033	
е	0.800TYP.		0.031TYP.		
θ	7° REF.		7° REF.		



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