

WSP4447

P-Ch MOSFET

### **General Description**

The WSP4447 is the highest performance trench P-Ch MOSFET with extreme high cell density , which provide excellent  $R_{\text{DSON}}$  and gate charge for most of the synchronous buck converter applications .

The WSP4447 meet the RoHS and Green Product requirement , 100% EAS guaranteed with full function reliability approved.

#### Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline

**Absolute Maximum Ratings** 

- 100% EAS Guaranteed
- Green Device Available

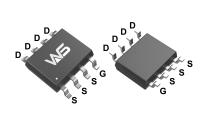
## **Product Summery**

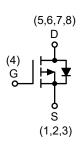
BV <sub>DSS</sub>	R <sub>DSON</sub>	I <sub>D</sub>
-40V	13mΩ	-11A

#### Applications

- High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch

### **SOP-8L Pin Configuration**





Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	-40	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>A</sub> =25℃	Continuous Drain Current, V <sub>GS</sub> @ -10V <sup>1</sup>	-11	А
I <sub>D</sub> @T <sub>A</sub> =70℃	Continuous Drain Current, V <sub>GS</sub> @ -10V <sup>1</sup>	-9.0	A
I <sub>DM</sub> a	300µs Pulsed Drain Current (VGS=-10V)	-44	A
Eas⁵	Avalanche Energy, Single pulse (L=0.1mH)	54	mJ
las <sup>b</sup>	Avalanche Current, Single pulse (L=0.1mH)	-33	А
P <sub>D</sub> @T <sub>A</sub> =25℃	Total Power Dissipation <sup>4</sup>	2.0	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range -55 to 150		°C

## Thermal Data

Symbol	Parameter	Тур.	Max.	Unit
R <sub>θJA</sub>	Thermal Resistance Junction-Ambient <sup>1</sup>		75	°C/W
R <sub>eJC</sub>	Thermal Resistance Junction-Case <sup>1</sup> -		24	℃ <b>/W</b>



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## Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit	
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =-250uA	-40			V	
$\triangle BV_{DSS} / \triangle T_J$	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25 $^\circ\!\mathrm{C}$ , I_D=-1mA		-0.018		V/℃	
Б	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =-10V , I <sub>D</sub> =-13A		13	16	- mΩ	
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =-4.5V , I <sub>D</sub> =-5A		18	26	m£2	
V <sub>GS(th)</sub>	Gate Threshold Voltage		-1.4	-1.9	-2.4	V	
$ riangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient	$V_{GS} - V_{DS}$ , $I_D - 2500A$		5.04		mV/℃	
	Drain-Source Leakage Current	$V_{DS}$ =-32V , $V_{GS}$ =0V , $T_{J}$ =25 $^{\circ}$ C			-1		
I <sub>DSS</sub>	Drain-Source Leakage Current	$V_{DS}$ =-32V , $V_{GS}$ =0V , T <sub>J</sub> =55 $^\circ$ C			-5	uA	
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}$ = $\pm20V$ , $V_{DS}$ = $0V$			±100	nA	
gfs	Forward Transconductance	V <sub>DS</sub> =-5V , I <sub>D</sub> =-10A		18		S	
Qg	Total Gate Charge (-4.5V)			32			
Q <sub>gs</sub>	Gate-Source Charge	V <sub>DS</sub> =-20V , V <sub>GS</sub> =-10V , I <sub>D</sub> =-11A		5.2		nC	
Q <sub>gd</sub>	Gate-Drain Charge			8			
T <sub>d(on)</sub>	Turn-On Delay Time			12			
Tr	Rise Time	$V_{DD}$ =-20V , $V_{GS}$ =-10V ,		14		20	
T <sub>d(off)</sub>	Turn-Off Delay Time	$R_G=6\Omega$ , $I_D=-1A$ , $RL=20\Omega$		22		ns	
T <sub>f</sub>	Fall Time			41			
C <sub>iss</sub>	Input Capacitance			1500			
Coss	Output Capacitance	V <sub>DS</sub> =-15V , V <sub>GS</sub> =0V , f=1MHz		235		pF	
C <sub>rss</sub>	Reverse Transfer Capacitance			180			

## **Diode Characteristics**

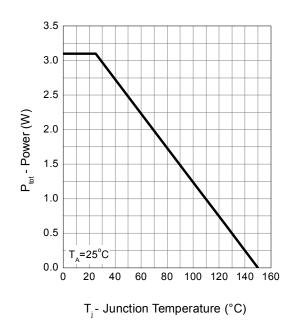
Symbol	Parameter	Conditions		Тур.	Max.	Unit
Is	Continuous Source Current <sup>1,6</sup>				-3.0	А
I <sub>SM</sub>	Pulsed Source Current <sup>2,6</sup>	$V_G = V_D = 0V$ , Force Current			-18	А
V <sub>SD</sub>	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =-1A , T <sub>J</sub> =25℃			-1.1	V
trr	Reverse Recovery Time	I⊧=-11A,dl/dt=100A/µs,Tյ=25℃		24		nS
Qrr	Reverse Recovery Charge			18		nC

Note

1,Pulse test; pulse width $\leq$ 300 $\mu$ s, duty cycle $\leq$ 2%.

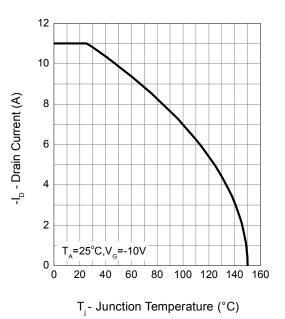
2, Guaranteed by design, not subject to production testing.



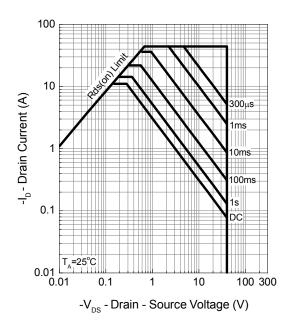


**Power Dissipation** 

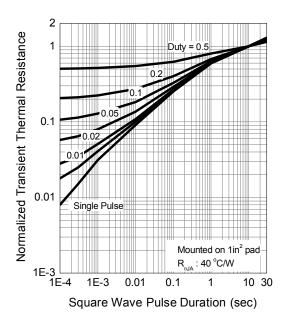
Drain Current



Safe Operation Area



Thermal Transient Impedance

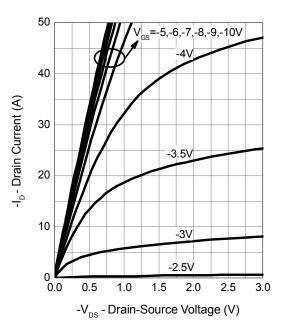


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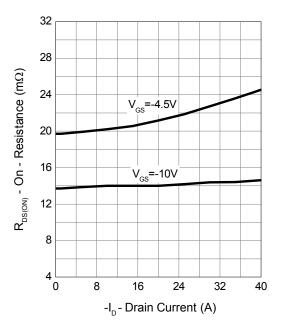


# **Typical Operating Characteristics (Cont.)**

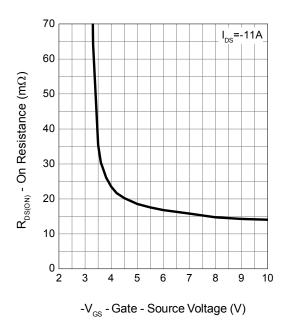


#### **Output Characteristics**

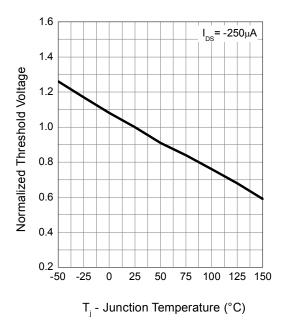
**Drain-Source On Resistance** 



**Gate-Source On Resistance** 



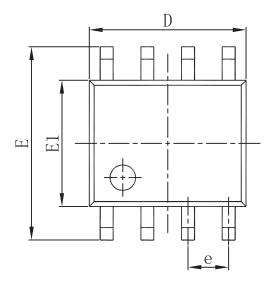
Gate Threshold Voltage

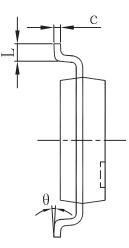


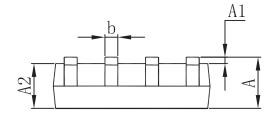


WSP4445 P-Ch MOSFET

## **Packaging information**





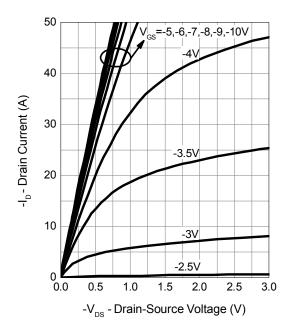


Grande al	Dimensions In Millimeters		Dimensions In Inches			
Symbol	Min	Max	Min	Max		
А	1.350	1.750	0.053	0.069		
A1	0.100	0.250	0.004	0.010		
A2	1.350	1.550	0.053	0.061		
b	0.330	0.510	0.013	0.020		
с	0.170	0.250	0.007	0.010		
D	4.800	5.000	0.189	0.197		
e	1.270 (	(BSC) 0.050 (BSC)		1.270 (BSC)		(BSC)
Е	5.800	6.200	0.228	0.244		
E1	3.800	4.000	0.150	0.157		
L	0.400	1.270	0.016	0.050		
θ	0°	8°	0 °	8°		



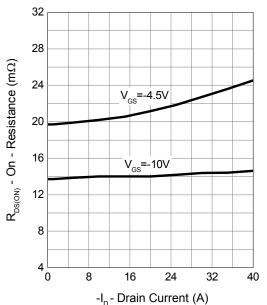
WSP4447 P-Ch MOSFET

# Typical Operating Characteristics (Cont.)

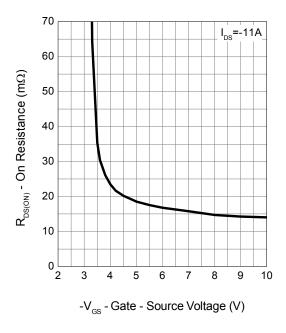


**Output Characteristics** 

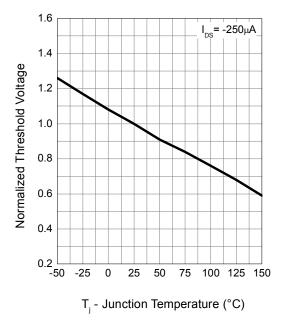
Drain-Source On Resistance



**Gate-Source On Resistance** 



Gate Threshold Voltage





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