

## **General Description**

The WSK26N20 is obtained by the self-aligned planar Technology which reduce the conduction loss, improve switching performance and enhance the avalanche energy. The transistor can be used in various power switching circuit for system miniaturization and higher efficiency. converter applications .

The WSK26N20 meet the RoHS and Green Product requirement,100% EAS guaranteed with full function reliability approved.

#### **Features**

- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

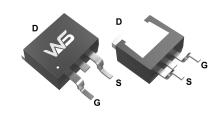
## **Product Summery**

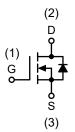
BV <sub>DSS</sub>	R <sub>DSON</sub>	I <sub>D</sub>
200V	80mΩ	26A

# **Applications**

- Uninterruptible Power Supply(UPS)
- Power Factor Correction (PFC)

# **TO-263-2L Pin Configuration**





## **Absolute Maximum Ratings**

Symbol	Parameter	Units	
$V_{DS}$	Drain-Source Voltage(V <sub>GS</sub> =0V)	200	V
$V_{GS}$	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>C</sub> =25℃	Continuous Drain Current, V <sub>GS</sub> @ 10V	26	Α
I <sub>DM</sub>	Pulsed Drain Current T <sub>C</sub> =25°C	90	Α
EAS	Avalanche Energy, Single pulse	340	mJ
IAR	Avalanche Current	20	Α
EAR	Repetitive Avalanche Energy	8.3	mJ
P <sub>D</sub> @T <sub>C</sub> =25℃	Total Power Dissipation	104	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	$^{\circ}$
TJ	Operating Junction Temperature Range	-55 to 150	$^{\circ}$

#### **Thermal Data**

Symbol	Parameter	Тур.	Max.	Unit	
R <sub>0JA</sub>	Thermal Resistance Junction-Ambient	Thermal Resistance Junction-Ambient			
R <sub>θJC</sub>	Thermal Resistance Junction-Case		1.2	°C/W	



# Electrical Characteristics (T<sub>J</sub>=25 C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit	
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	200			V	
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =10V , I <sub>D</sub> =9A		80	100	mΩ	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=250uA$	2.0	3.0	4.0	V	
1	Zero Coto Voltago Proin Current	$V_{DS}$ =200V , $V_{GS}$ =0V , $T_{J}$ =25 $^{\circ}$ C			5	uA	
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{DS}$ =160V , $V_{GS}$ =0V , $T_J$ =125 $^{\circ}$ C			100	uA	
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±20V			±100	nA	
$Q_g$	Total Gate Charge			41			
Q <sub>gs</sub>	Gate-Source Charge	V <sub>DD</sub> =160V , V <sub>GS</sub> =10V , I <sub>D</sub> =18A		5.5		nC	
$Q_{gd}$	Gate-Drain Charge			19.5			
T <sub>d(on)</sub>	Turn-On Delay Time			24			
Tr	Rise Time	- -V <sub>DD</sub> =100V, R <sub>G</sub> =25Ω, I <sub>D</sub> =18A		45			
T <sub>d(off)</sub>	Turn-Off Delay Time	- VDD-100V, NG-2322, ID-10A		101		ns	
T <sub>f</sub>	Fall Time			95			
C <sub>iss</sub>	Input Capacitance			1318			
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> =25V , V <sub>GS</sub> =0V , f=1MHz		180		pF	
C <sub>rss</sub>	Reverse Transfer Capacitance			75			

# **Diode Characteristics**

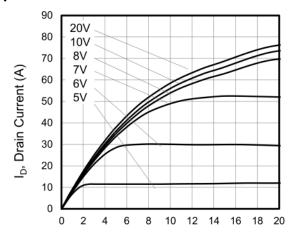
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current	Tc=25℃			18	Α
I <sub>SM</sub>	Pulsed Diode Forward Current	10-200			72	^
$V_{SD}$	Diode Forward Voltage	V <sub>GS</sub> =0V , I <sub>SD</sub> =18A , T <sub>J</sub> =25℃			1.4	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>GS</sub> =0V , I <sub>S</sub> =18A ,diF/dt=100A		230		nS
Q <sub>rr</sub>	Reverse Recovery Charge	/us		1.8		uC

#### Note:

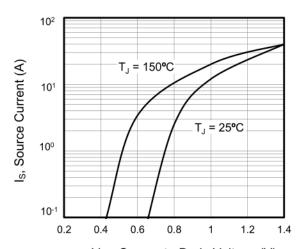
- $1 \cdot$  The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper.
- 2. The EAS data shows Max. rating . IAS =20A, VDD = 50V, RG = 25  $\Omega$ , Starting TJ = 25 C
- $3\,^{{}_{\sim}}$  The test condition is Pulse Test: Pulse width  $\leqslant$  300µs, Duty Cycle  $\leqslant$  1%
- 4. The power dissipation is limited by 150 °C junction temperature
- 5. The data is theoretically the same as ID and IDM, in real applications, should be limited by total power dissipation..



# **Typical Characteristics**



 $V_{DS}$ , Drain-to-Source Voltage (V) Figure 1. Output Characteristics (T $_{\rm J}$  = 25°C)



V<sub>SD</sub>, Source-to-Drain Voltage (V) Figure 2. Body Diode Forward Voltage

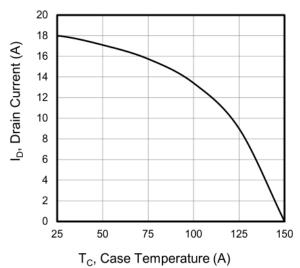


Figure 3. Drain Current vs. Temperature

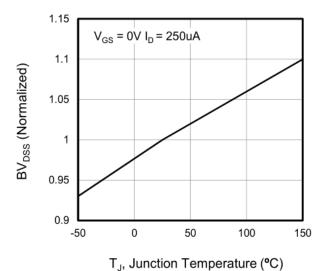


Figure 4. BV<sub>DSS</sub> Variation vs. Temperature

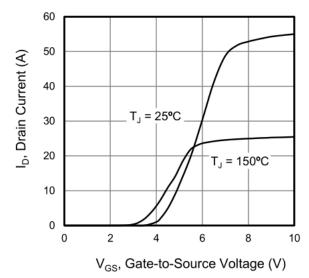


Figure 5. Transfer Characteristics

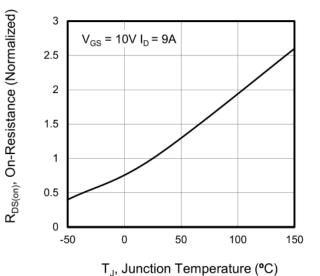
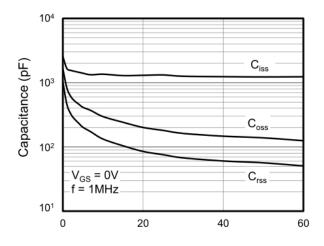
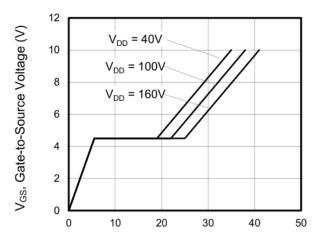


Figure 6. On-Resistance vs. Temperature





V<sub>DS</sub>, Drain-to-Source Voltage (V) **Figure 7. Capacitance** 



Q<sub>g</sub>, Total Gate Charge (nC) **Figure 8. Gate Charge** 

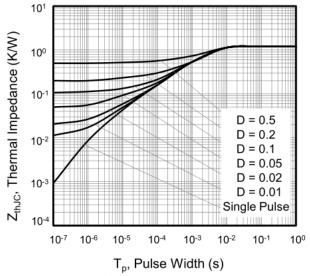
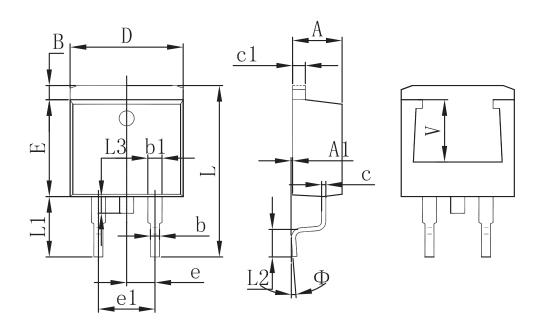


Figure 10. Transient Thermal Impedance



# Packaging information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
Symbol	Min.	Max.	Min.	Max.
Α	4.470	4.670	0.176	0.184
A1	0.000	0.150	0.000	0.006
В	1.120	1.420	0.044	0.056
b	0.710	0.910	0.028	0.036
b1	1.170	1.370	0.046	0.054
С	0.310	0.530	0.012	0.021
c1	1.170	1.370	0.046	0.054
D	10.010	10.310	0.394	0.406
E	8.500	8.900	0.335	0.350
е	2.540 TYP.		0.100 TYP.	
e1	4.980	5.180	0.196	0.204
L	14.940	15.500	0.588	0.610
L1	4.950	5.450	0.195	0.215
L2	2.340	2.740	0.092	0.108
L3	1.300	1.700	0.051	0.067
Ф	0°	8°	0°	8°
V	5.600 REF.		0.220	REF.



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