

General Description

The WSD60P06DN56 is the highest performance trench P-Channel MOSFETs with extreme high cell density, which provide excellent $R_{DS(ON)}$ and gate charge for most of the synchronous buck converter applications.

The WSD60P06DN56 meet the RoHS and Green Product requirement, 100% E_{AS} guaranteed with full function reliability approved.

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% E_{AS} Guaranteed
- Green Device Available

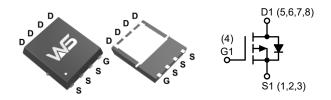
Product Summery

BV _{DSS}	R _{DS(ON)}	I _D
-60V	20mΩ	-60A

Applications

- Power Management
- Load Switch

DFN5X6-8L Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	-60	V
V_{GS}	Gate-Source Voltage	±20	V
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ -10V	-60	
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ -10V	-31	A
I _{DM}	Pulsed Drain Current	-140	
P _D @T _C =25°C	Total Power Dissipation	56	W
T _{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range -55 to 150		

Thermal Data

Symbol	Parameter	Тур.	Max.	Units
$R_{ heta JA}$	Thermal Resistance Junction-Ambient		62.5	°C/W
$R_{ heta JC}$	Thermal Resistance Junction-Case		2.2	C/VV



Electrical Characteristics (T_J=25°C, Unless Otherwise Noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =-250μA	-60			V
D	Static Drain-Source On-Resistance	V _{GS} =-10V , I _D =-18A		20	25	m0
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =-4.5V , I _D =-12A		23	30	mΩ
V _{GS(th)}	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_{D}=-250\mu A$	-1.0	-1.85	-2.5	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =-32V , V _{GS} =0V ,T _J =25°C			-1.0	μA
I _{GSS}	Gate-Body Leakage Current	V _{GS} =±20V , V _{DS} =0V			±100	nA
Q_g	Total Gate Charge			72		
Q_{gs}	Gate-Source Charge	V_{DS} =-30V , V_{GS} =-10V , I_{D} =-17A		11		nC
Q_{gd}	Gate-Drain Charge			16		
T _{d(on)}	Turn-On Delay Time			12		
T _r	Rise Time	V_{DD} =-30V , R_L =30 Ω , I_D =-1A ,		10		200
$T_{d(off)}$	Turn-Off Delay Time	V_{GEN} =-10V , R_{G} =6 Ω		125		ns
T _f	Fall Time			45		
C _{iss}	Input Capacitance			3300		
C _{oss}	Output Capacitance	V _{DS} =-30V , V _{GS} =0V , f=1.0MHz		265		pF
C _{rss}	Reverse Transfer Capacitance			125		

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
I _S	Continuous Source Current	T _C =25°C			-20	Α
V _{SD}	Diode Forward Voltage	V _{GS} =0V , I _S =-1A , T _J =25°C			-1.2	V

Note:

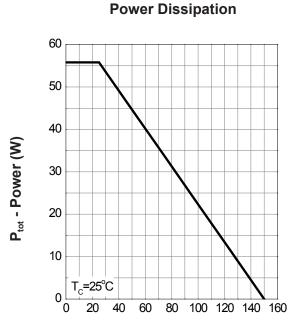
^{1.} The value of R_{θJA} is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with T_A=25°C. The value in any given application depends on the user's specific board design.

^{2.} Repetitive rating, pulse width limited by junction temperature.

^{3.} The current rating is based on the t≤10s junction to ambient thermal resistance rating.

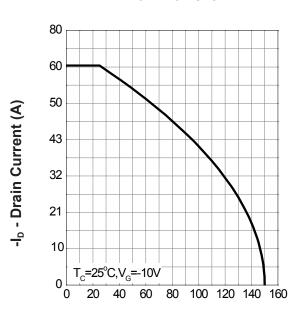


Typical Characteristics



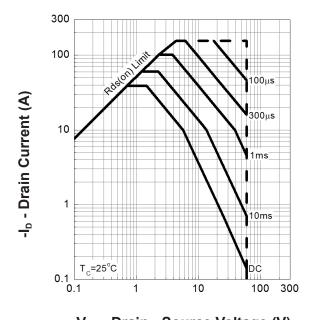
T_i - Junction Temperature (°C)

Drain Current



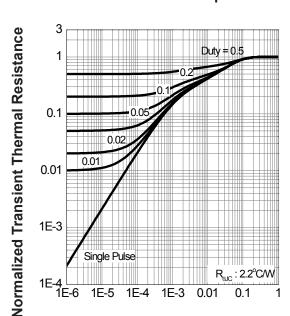
T_i - Junction Temperature (°C)

Safe Operation Area



- V_{DS} - Drain - Source Voltage (V)

Thermal Transient Impedance

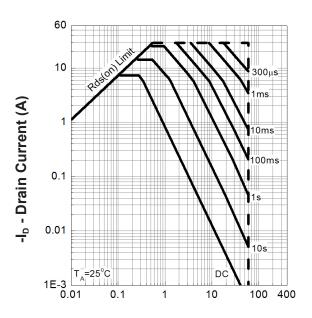


Square Wave Pulse Duration (sec)



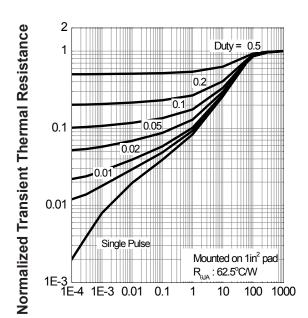
Typical Characteristics (Cont.)

Safe Operation Area



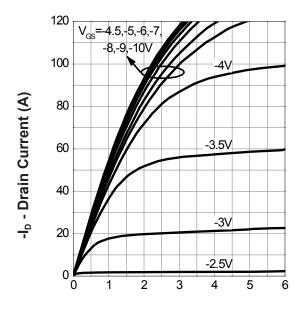
-V_{DS} - Drain - Source Voltage (V)

Thermal Transient Impedance



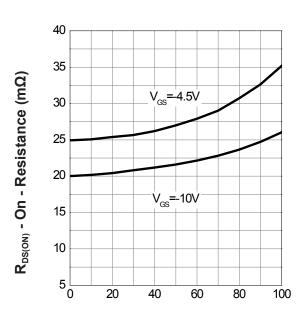
Square Wave Pulse Duration (sec)

Output Characteristics



-V_{DS} - Drain - Source Voltage (V)

Drain-Source On Resistance

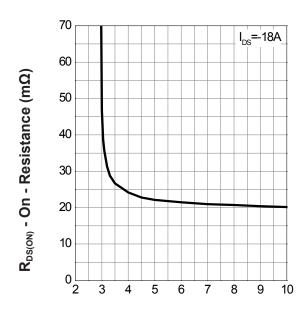


-I_D - Drain Current (A)



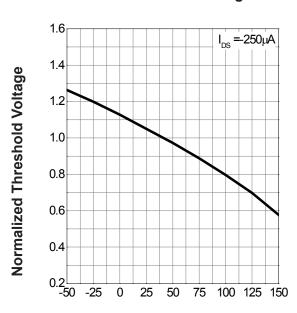
Typical Characteristics (Cont.)

Gate-Source On Resistance



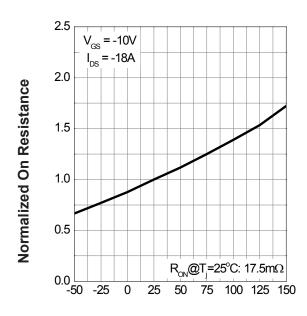
-V_{GS} - Gate - Source Voltage (V)

Gate Threshold Voltage



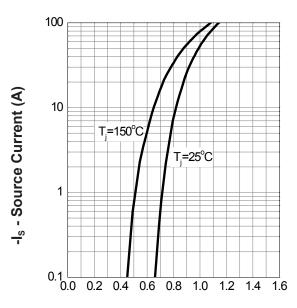
T_i - Junction Temperature (°C)

Drain-Source On Resistance



 T_j - Junction Temperature (°C)

Source-Drain Diode Forward

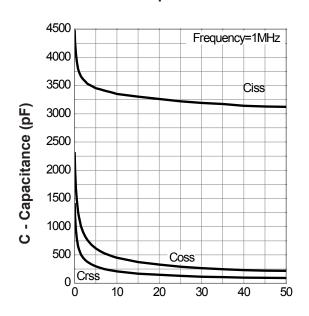


-V_{SD} - Source - Drain Voltage (V)



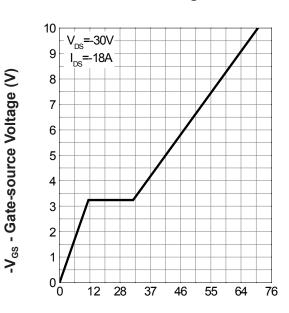
Typical Characteristics (Cont.)

Capacitance



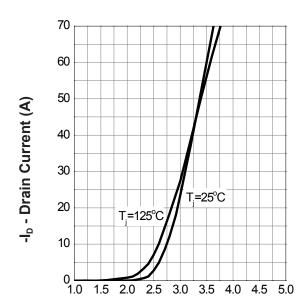
-V_{DS} - Drain-Source Voltage (V)

Gate Charge



Q_G - Gate Charge (nC)

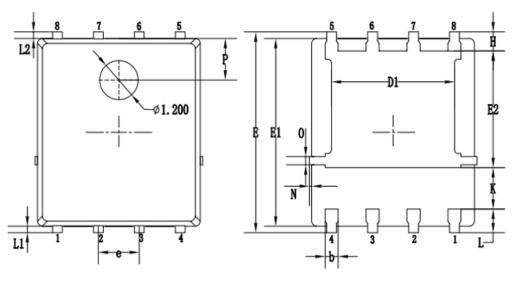
Transfer Characteristics

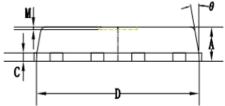


-V_{GS} - Gate-Source Voltage (V)



Packaging information





CVMDOLC		MILLIMETERS			
SYMBOLS	MIN.	NOM.	MAX.		
Α	0.90	1.05	1.20		
b	0.35	0.40	0.50		
С	0.20	0.25	0.35		
D	4.90	5.05	5.20		
D1	3.72	3.82	3.92		
E	6.00	6.15	6.30		
E1	5.60	5.75	5.90		
E2	3.47	3.57	3.67		
е		1.27 BSC.			
Н	0.48	0.58	0.68		
K	1.17	1.27	1.37		
L	0.64	0.74 0.84			
L1/L2		0.20 REF.			
θ	8°	10°	12°		
М		0.08 REF.			
N	0	-	0.15		
0		0.25 REF.			
Р		1.28 REF.			



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