

N-Channel MOSFET

General Description

Features

Super Low Gate Charge

100% E_{AS} Guaranteed
Green Device Available

• Excellent CdV/dt effect decline

Absolute Maximum Ratings

The WSD60N12GDN56 is SGT II technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

The WSD60N12GDN56 meet the RoHS and Green Product requirement, 100% E_{AS} guaranteed with full function reliability approved.

Advanced high cell density Trench technology

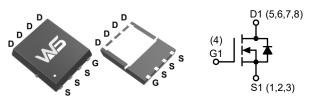
Product Summery

BV _{DSS}	R _{DS(ON)}	I _D
120V	10mΩ	70A

Applications

- Mobile phone fast charging
- Brushless motor
- Home appliance control board

DFN5X6-8L Pin Configuration



Symbol Units **Parameter** Rating **Drain-Source Voltage** 120 V_{DS} V ±20 V_{GS} Gate-Source Voltage I_D@T_C=25°C 70 **Continuous Drain Current** А Pulsed Drain Current 150 I_{DP} Avalanche Energy, Single pulse 53.8 mJ E_{AS} P_D@T_C=25°C W 140 **Total Power Dissipation** Storage Temperature Range -55 to 150 T_{STG} °C $T_{\rm J}$ **Operating Junction Temperature Range** -55 to 150

Thermal Data

Symbol	Parameter	Тур.	Max.	Units
R _{θJA}	Thermal Resistance Junction-Ambient ¹		25	°C/M
R _{θJC}	Thermal Resistance Junction-Case ¹	Thermal Resistance Junction-Case ¹		°C/W



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Electrical Characteristics (T_J=25°C, Unless Otherwise Noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250µA	120			V
D	Statia Drain Source On Registence	V _{GS} =10V , I _D =10A		10	15	
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =4.5V , I _D =10A		18	25	-mΩ
V _{GS(th)}	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_{D}=250\mu A$	1.2		2.5	V
I _{DSS}	Drain-Source Leakage Current	V_{DS} =80V, V_{GS} =0V, T_{J} =25°C			1.0	μA
I _{GSS}	Gate-Source Leakage Current	V_{GS} =±20V, V_{DS} =0V			±100	nA
Qg	Total Gate Charge (10V)			33		
Q _{gs}	Gate-Source Charge	V_{DS} =50V , V_{GS} =10V , I_{D} =25A		5.6		nC
Q _{gd}	Gate-Drain Charge			7.2		
T _{d(on)}	Turn-On Delay Time			22		
Tr	Rise Time	V _{DD} =50V,V _{GS} =10V,		10		
T _{d(off)}	Turn-Off Delay Time	R _G =2Ω , I _D =25A		85		ns
T _f	Fall Time			112		
C _{iss}	Input Capacitance			2640		
C _{oss}	Output Capacitance	V _{DS} =50V , V _{GS} =0V , f = 1.0MHz		330		pF
C _{rss}	Reverse Transfer Capacitance			11		

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
۱ _S	Continuous Source Current	$V_G = V_D = 0V$, Force Current			50	Δ
I _{SP}	Pulsed Source Current				150	A
V _{SD}	Diode Forward Voltage	V _{GS} =0V, I _S =12A,T _J =25°C			1.3	V
t _{rr}	Reverse Recovery Time	− I _F =25A , di/dt=100A/µs , T _J =25°C		62		ns
Q _{rr}	Reverse Recovery Charge			135		nC

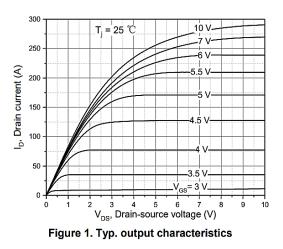
Note:

- 1. Calculated continuous current based on maximum allowable junction temperature.
- 2. Repetitive rating: pulse width limited by max. junction temperature.
- 3. $\ensuremath{\mathsf{P}}_{\ensuremath{\mathsf{D}}}$ is based on max. junction temperature, using junction-case thermal resistance.
- 4. The value of $R_{\theta JA}$ is measured with the device mounted on 1 in² FR-4 board with 2oz. Copper, in a still air environment with T_A =25°C.
- 5. V_{DD}=50V, R_G=25\Omega, L=0.3mH, starting T_J=25°C.



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Typical Characteristics



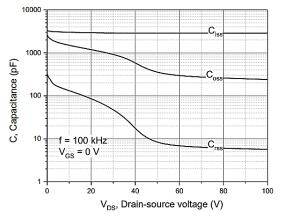


Figure 3. Typ. capacitances

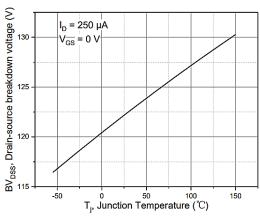


Figure 5. Drain-source breakdown voltage

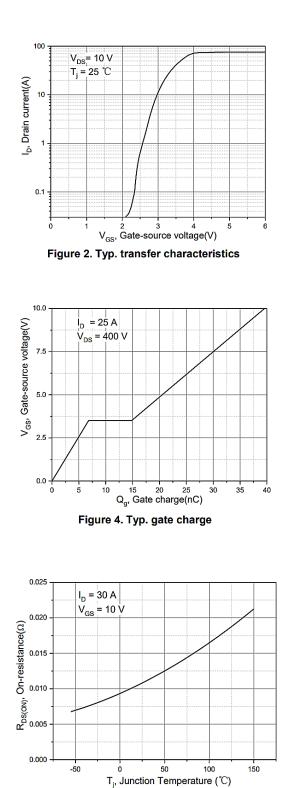


Figure 6. Drain-source on-state resistance



N-Channel MOSFET

Typical Characteristics (Cont.)

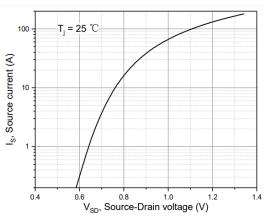
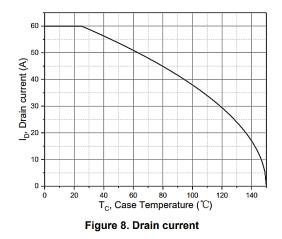
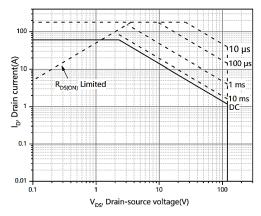


Figure 7. Forward characteristic of body diode

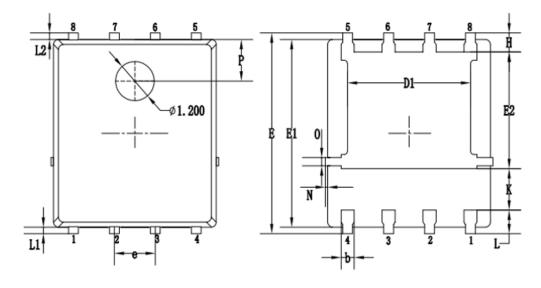


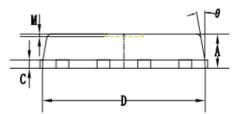




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Packaging information





SYMBOLS -		MILLIMETERS				
	MIN.	NOM.	MAX.			
А	0.90	1.05	1.20			
b	0.35	0.40	0.50			
С	0.20	0.25	0.35			
D	4.90	5.05	5.20			
D1	3.72	3.82	3.92			
E	6.00	6.15	6.30			
E1	5.60	5.75	5.90			
E2	3.47	3.57	3.67			
е		1.27 BSC.				
Н	0.48	0.58	0.68			
К	1.17	1.27	1.37			
L	0.64	0.74 0.84				
L1/L2		0.20 REF.				
θ	8°	10°	12°			
М		0.08 REF.				
Ν	0	-	0.15			
0		0.25 REF.				
Р		1.28 REF.				



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