

### General Description

The WSD30L88DN56 is the highest performance trench Dual P-Channel MOSFET with extreme high cell density, which provide excellent  $R_{DS(ON)}$  and gate charge for most of the synchronous buck converter applications.

The WSD30L88DN56 meet the RoHS and Green Product requirement 100%  $E_{AS}$  guaranteed with full function reliability approved.

### Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100%  $E_{AS}$  Guaranteed
- Green Device Available

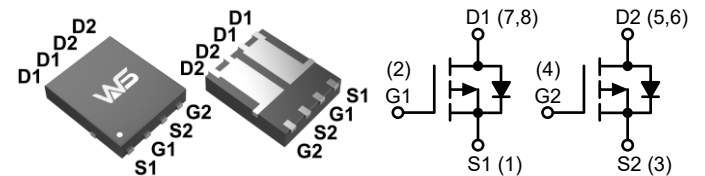
### Product Summary

$BV_{DSS}$	$R_{DS(ON)}$	$I_D$
-30V	11.5m $\Omega$	-49A

### Applications

- High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch

### DFN5X6-8L Pin Configuration



### Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-30	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	
$I_D @ T_C = 25^\circ C$	Continuous Drain Current, $V_{GS} @ -10V$ <sup>1</sup>	-49	A
$I_D @ T_C = 100^\circ C$	Continuous Drain Current, $V_{GS} @ -10V$ <sup>1</sup>	-23	
$I_{DM}$	Pulsed Drain Current <sup>2</sup>	-120	
$E_{AS}$	Single Pulse Avalanche Energy <sup>3</sup>	68	mJ
$P_D @ T_C = 25^\circ C$	Total Power Dissipation <sup>4</sup>	40	W
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 150	

### Thermal Data

Symbol	Parameter	Typ.	Max.	Units
$R_{\theta JA}$	Thermal Resistance, Junction-Ambient <sup>1</sup>	---	50	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance, Junction-Case <sup>1</sup>	---	2.4	

**Electrical Characteristics (T<sub>J</sub>=25°C, Unless Otherwise Noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =-250μA	-30	---	---	V
ΔBV <sub>DSS</sub> /ΔT <sub>J</sub>	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25°C, I <sub>D</sub> =-1mA	---	-0.0332	---	V/°C
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-10A	---	11.5	16	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-5A	---	16	20	
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =-250μA	-1.2	-1.5	-2.5	V
ΔV <sub>GS(th)</sub>	V <sub>GS(th)</sub> Temperature Coefficient		---	4.4	---	mV/°C
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =-24V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	---	---	1.0	μA
		V <sub>DS</sub> =-24V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C	---	---	5.0	
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	---	---	±100	nA
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =-5V, I <sub>D</sub> =-10A	35	---	---	S
R <sub>g</sub>	Gate Resistance	V <sub>DS</sub> =0V, V <sub>GS</sub> =0V, f = 1.0MHz	---	7	---	Ω
Q <sub>g</sub>	Total Gate Charge (-4.5V)	V <sub>DS</sub> =-15V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-5A	---	22	---	nC
Q <sub>gs</sub>	Gate-Source Charge		---	5.5	---	
Q <sub>gd</sub>	Gate-Drain Charge		---	5.9	---	
T <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> =-15V, V <sub>GEN</sub> =-10V, R <sub>G</sub> =3Ω, I <sub>D</sub> =-1A	---	9	---	ns
T <sub>r</sub>	Rise Time		---	13	---	
T <sub>d(off)</sub>	Turn-Off Delay Time		---	48	---	
T <sub>f</sub>	Fall Time		---	20	---	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =-15V, V <sub>GS</sub> =0V, f = 1.0MHz	---	2135	---	pF
C <sub>oss</sub>	Output Capacitance		---	282	---	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	255	---	

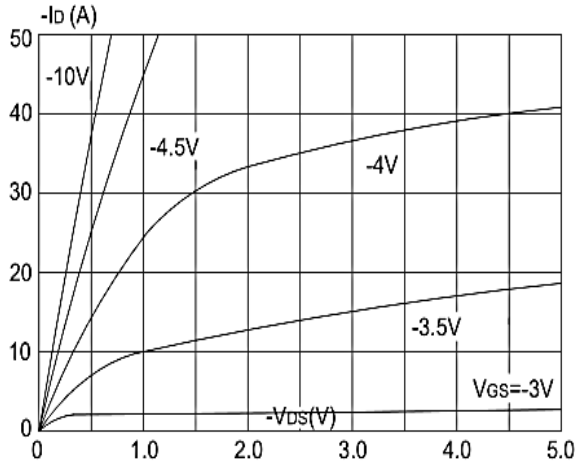
**Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
I <sub>S</sub>	Continuous Source Current <sup>1,6</sup>	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	-29.5	A
I <sub>SM</sub>	Pulsed Source Current <sup>2,6</sup>		---	---	-44	
V <sub>SD</sub>	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V, I <sub>S</sub> =-1A, T <sub>J</sub> =25°C	---	---	-1.2	V

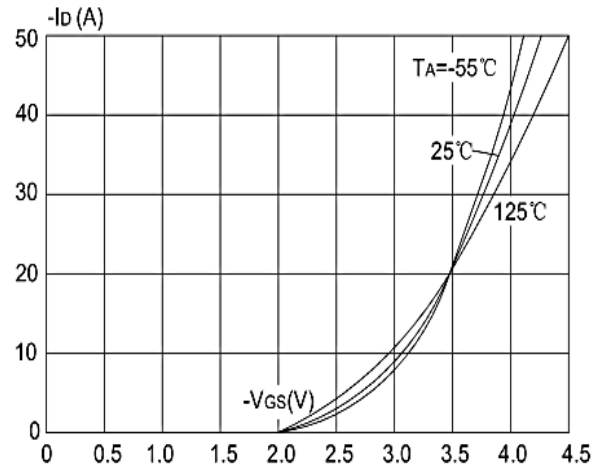
**Note:**

- The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 20Z copper, t < 10sec.
- The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
- The E<sub>AS</sub> data shows Max. rating. The test condition is V<sub>DD</sub>=-15V, V<sub>GS</sub>=-10V, L=0.1mH, I<sub>AS</sub>=-36A
- The power dissipation is limited by 150°C junction temperature.
- The Min. value is 100% E<sub>AS</sub> tested guarantee.
- The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub>, in real applications, should be limited by total power dissipation.

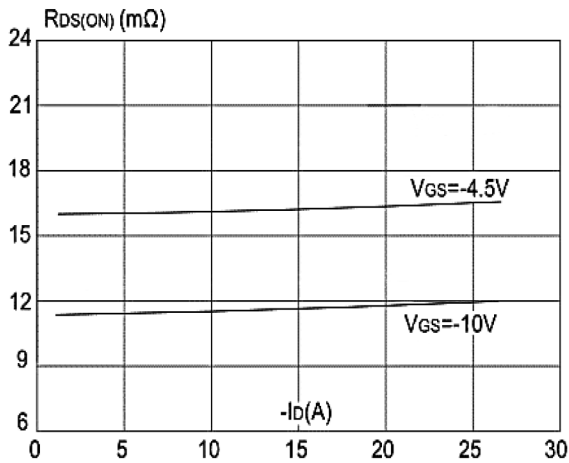
**Typical Characteristics**



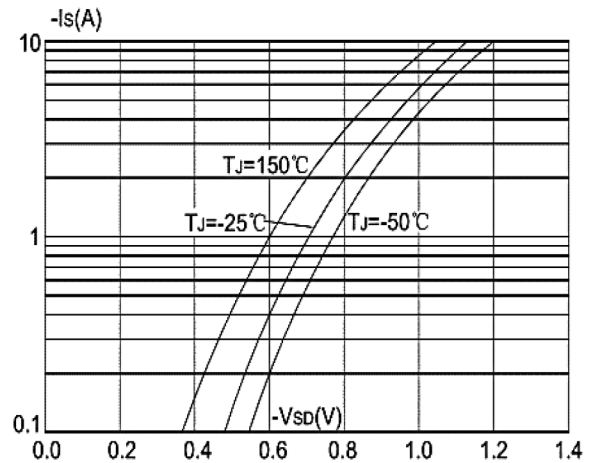
**Figure 1: Output Characteristics**



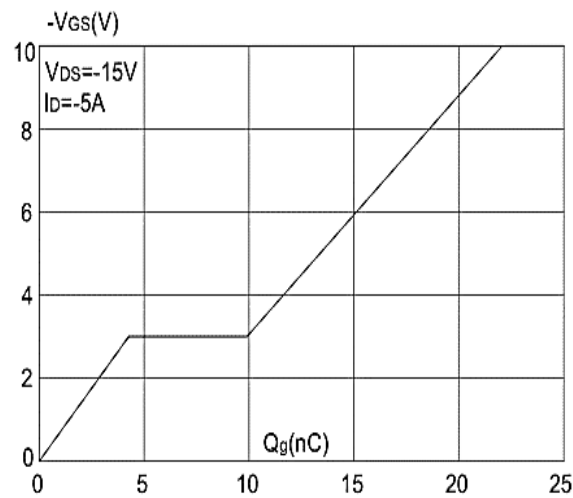
**Figure 2: Typical Transfer Characteristics**



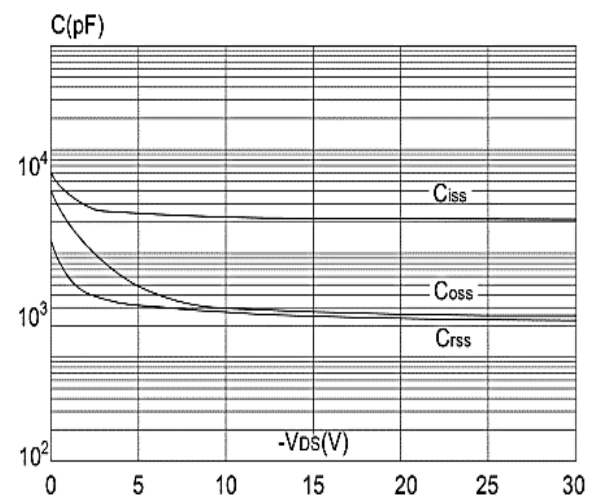
**Figure 3: On-resistance vs. Drain Current**



**Figure 4: Body Diode Characteristics**

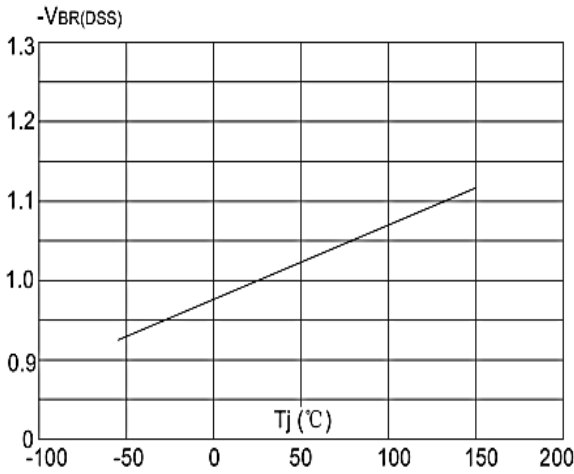


**Figure 5: Gate Charge Characteristics**

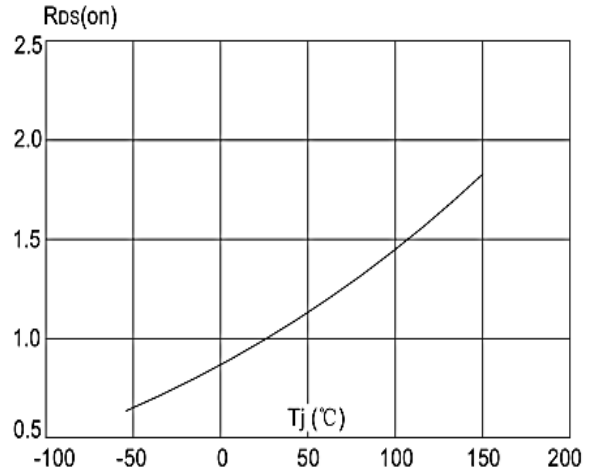


**Figure 6: Capacitance Characteristics**

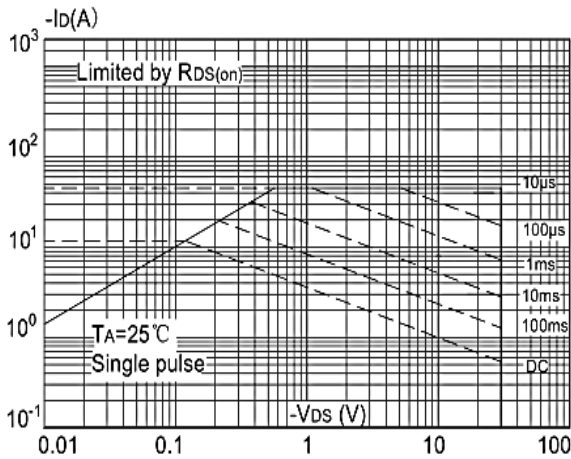
**Typical Characteristics (Cont.)**



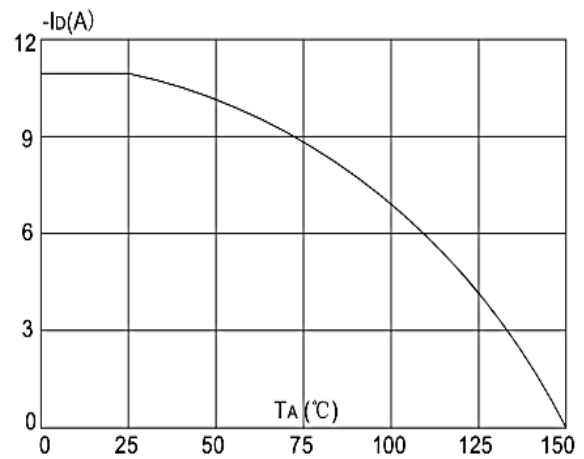
**Figure 7: Normalized Breakdown Voltage vs. Junction Temperature**



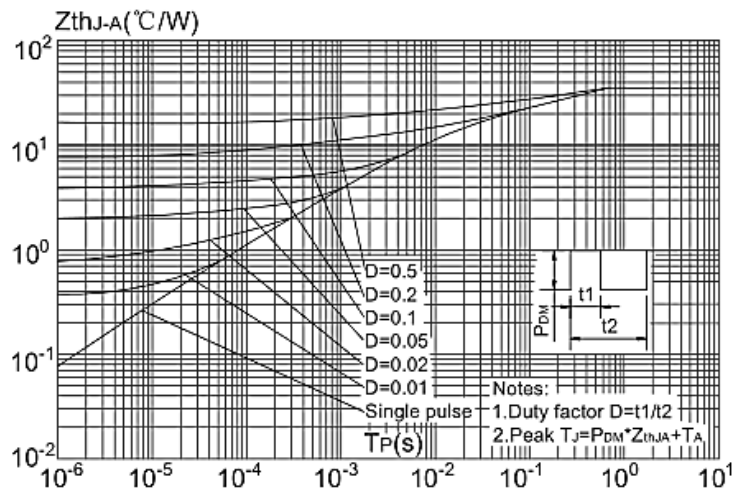
**Figure 8: Normalized on Resistance vs. Junction Temperature**



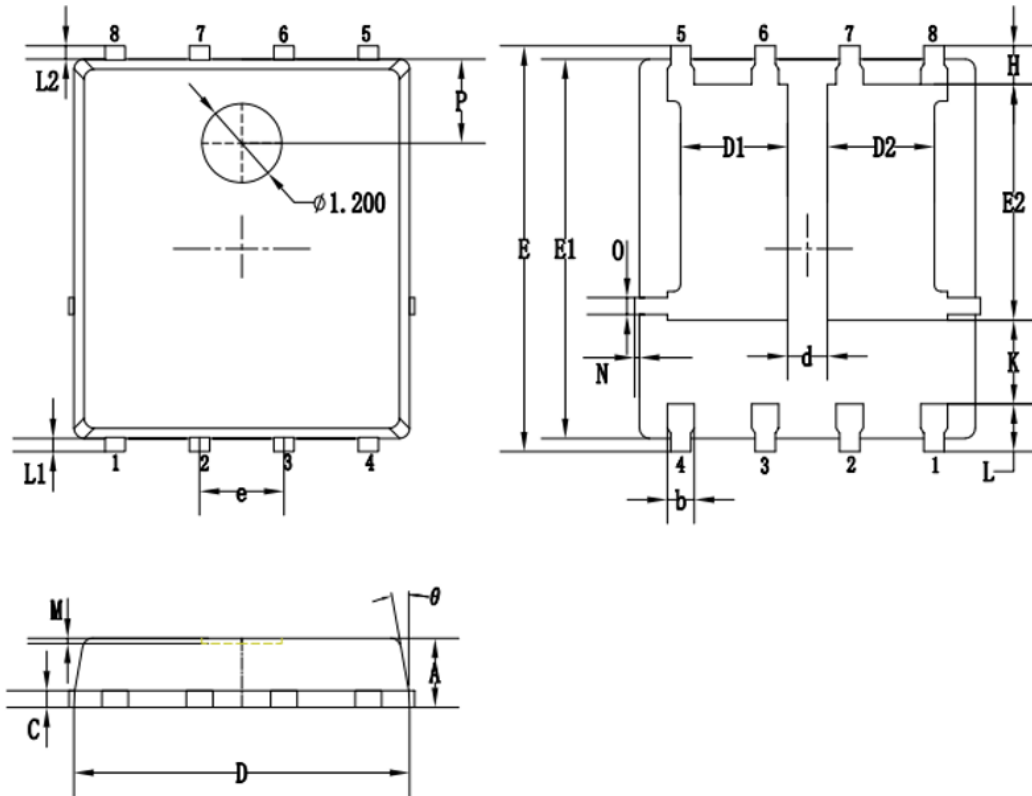
**Figure 9: Maximum Safe Operating Area**



**Figure 10: Maximum Continuous Drain Current vs. Ambient Temperature**



**Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Ambient**

**Packaging information**


SYMBOLS	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.90	1.05	1.20
b	0.35	0.40	0.50
C	0.20	0.25	0.35
D	4.90	5.05	5.20
D1/D2	1.51	1.61	1.71
d	0.50	0.60	0.70
E	6.00	6.15	6.30
E1	5.60	5.75	5.90
E2	3.47	3.57	3.67
e	1.27 BSC.		
H	0.48	0.58	0.68
K	1.17	1.27	1.37
L	0.64	0.74	0.84
L1/L2	0.20 REF.		
$\theta$	8°	10°	12°
M	0.08 REF.		
N	0	-	0.15
O	0.25 REF.		
P	1.28 REF.		

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